



## Assignment 4

Q1)

⇒ An interrupt is a condition that halts the microprocessors temporarily, to work on a different task & return to its previous task.

- It is an event or signal that requests to attention of CPU.
- This halt allows peripheral devices to access the microprocessor

Different types of interrupts in 8086 microprocessor:

### 1) Hardware Interrupt:

Hardware interrupt are those interrupts which are caused by any peripheral devices by sending a signal through a specified pin to the microprocessor. These are 2 hardware interrupts in 8086 microprocessor.

A) NMI (Non-maskable interrupt): It is a single pin non-maskable hardware interrupt which cannot be disabled. It is the highest priority interrupt in 8086 microprocessor. After its execution, this interrupt generates a Type 2 interrupt.

B) INTR (Interrupt Request): It provides a single interrupt request & is activated by I/O port. This interrupt can be masked





or delayed. It is a level triggered interrupt. It can receive any interrupt type.

Software Interrupt: These are instructions that are inserted within the program to generate interrupts.

- These are 256 software interrupts in 8086 microprocessor.
- The instructions are of the format INT type where type ranges from 00 to FF.
- The starting address ranges from 0000H to 003FFH.
- These are 2 byte instructions. IP is loaded from type  $\ast 04H$  & CS is loaded from the next address given by (type  $\ast 04H$ ) + 02H.

Some important ones are:-

- A) TYPE 0 corresponds to division by 0
- B) TYPE 2 corresponds to NMI is used in power failure conditions
- C) TYPE 3 represents a break-point interrupt
- D) TYPE 4 is the overflow interrupt



## Q2) Advantages

- 1) Debugging & Verifying: Looking at computer generated assembly code or the disassembly window in a debugger is useful for finding errors & compiler optimizer
- 2) It can access machine - dependent registers & I/O
- 3) It allows optimizations for memory allocation, threading etc.
- 4) Builds interfaces between code fragments using conventions
- 5) Access to unusual programming modes of your processor
- 6) Complete control of the code
- 7) Assembler can detect errors & produce required message