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Devarshi Das,
Assistant Professor,
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Subject: Internship certificate

To,
Whomsoever it may concern.

Mr. Girdhari Jakhar successfully completed his summer internship in the Integrated Circuit and System Design Lab (ISDL) under my supervision during June and July 2025

His work focused on analog circuit design and simulation using 180 nm CMOS technology. Key tasks included: comparing NMOS I-V characteristics across SCL and GDPK processes; designing and analysing common-source, common-gate, and differential amplifiers with constraints on current, gain, and bandwidth; post-layout simulation of a CMOS inverter; PVT analysis of a transimpedance amplifier (TIA) and ring VCOs (3- and 5-inverter); and PCB design for TIA testing.

Feel free to contact me if you have any questions.

Thanks and regards,

-Devarshi