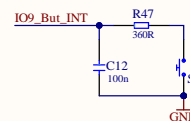
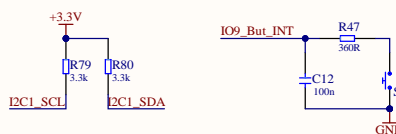
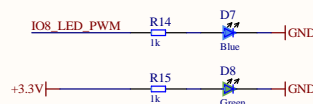


Boot Mode	GPIO0	GPIO46
Default Configuration	1 (Pull-up)	0 (Pull-down)
SPI Boot (default)	1	Any value
Download Boot	0	0
Invalid combination ¹	0	1



- Chip boot mode- GPIO0 and GPIO46
- VDD_SPI voltage- GPIO45
- ROM messages printing- GPIO46
- JTAG signal source- GPIO3

Strapping Pin	Default Configuration	Bit Value
GPI00	Pull-up	1
GPI03	Floating	–
GPI045	Pull-down	0
GPI046	Pull-down	0

EFUSE_VDD_SPI_FORCE	GPIO45	eFuse ¹	Voltage	VDD_SPI power source ²
0	0	ignored	3.3 V	VDDQ3P_RTC via R _{SP1}
	1		1.8 V	Flash Voltage Regulator
1	ignored	0	1.8 V	Flash Voltage Regulator
		1	3.3 V	VDDQ3P_RTC via R _{SP1}

eFuse 1 ^a	eFuse 2 ^b	eFuse 3 ^c	GPIO3	JTAG Signal Source
0	0	0	Ignored	USB Serial/JTAG Controller
		1	0	JTAG pins MTDI, MTCK, MTMS, and MTDI
			1	USB Serial/JTAG Controller
0	1	Ignored	Ignored	JTAG pins MTDI, MTCK, MTMS, and MTDI
1	0	Ignored	Ignored	USB Serial/JTAG Controller
1	1	Ignored	Ignored	JTAG is disabled