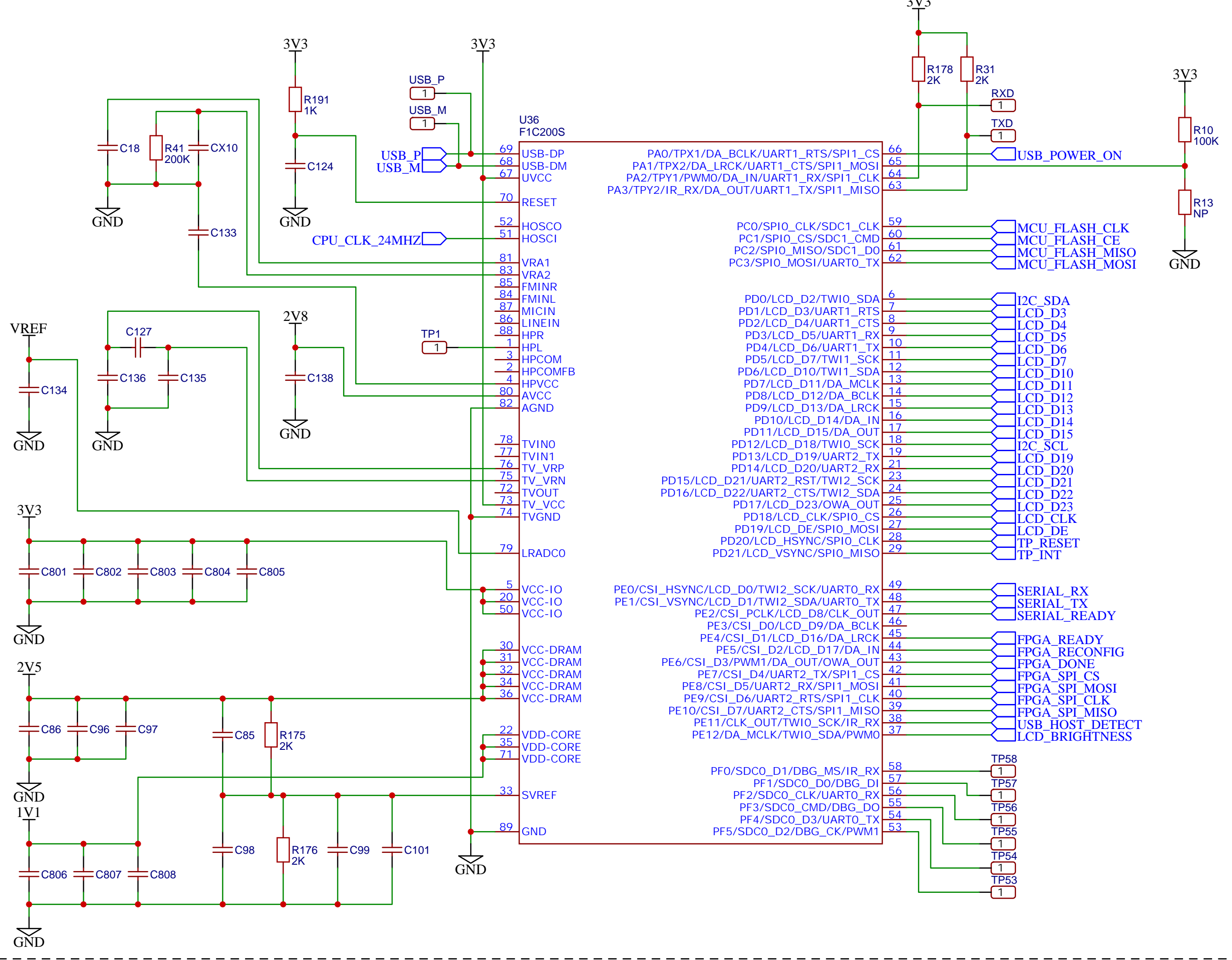
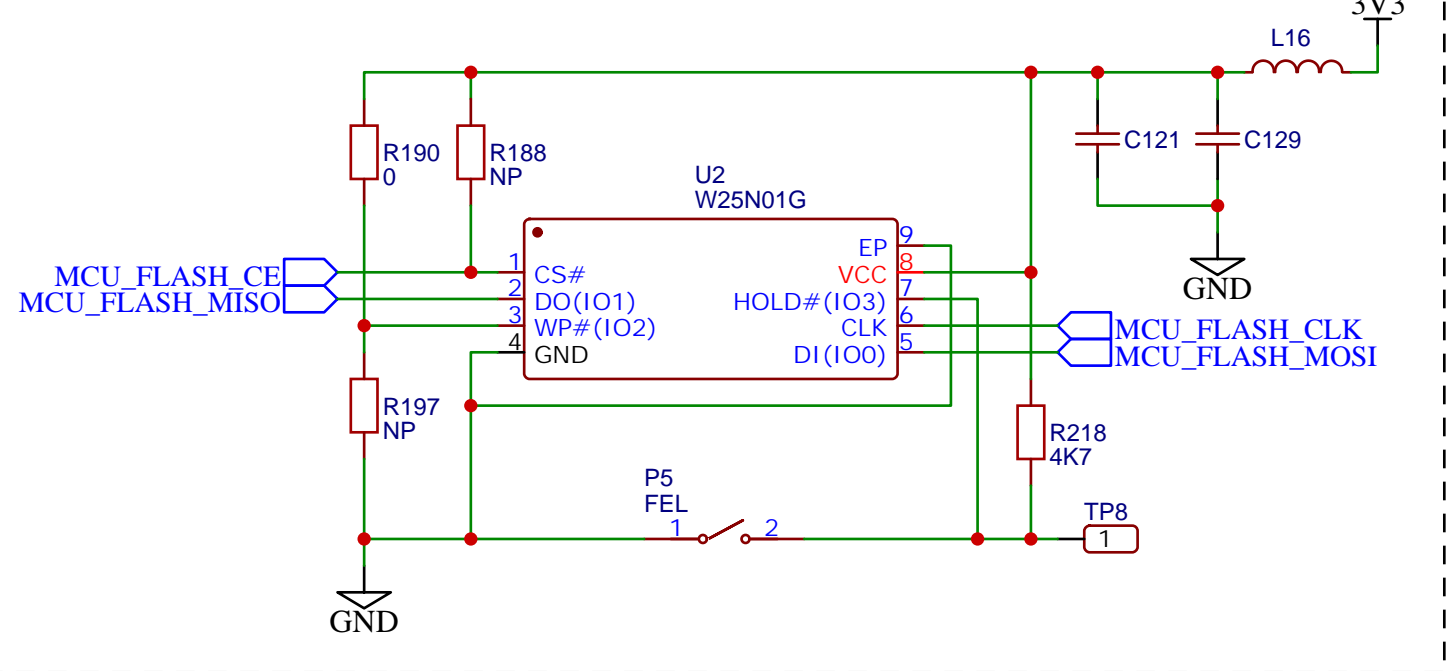


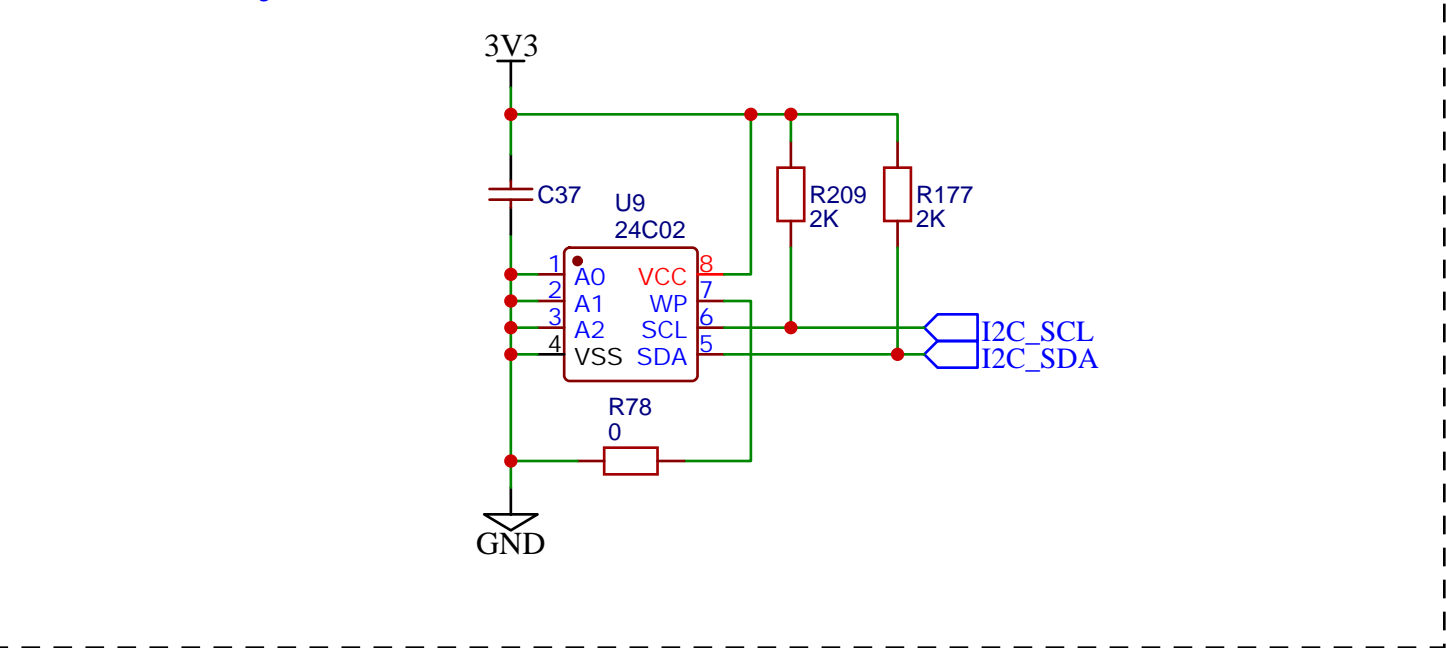
Central Processing Unit



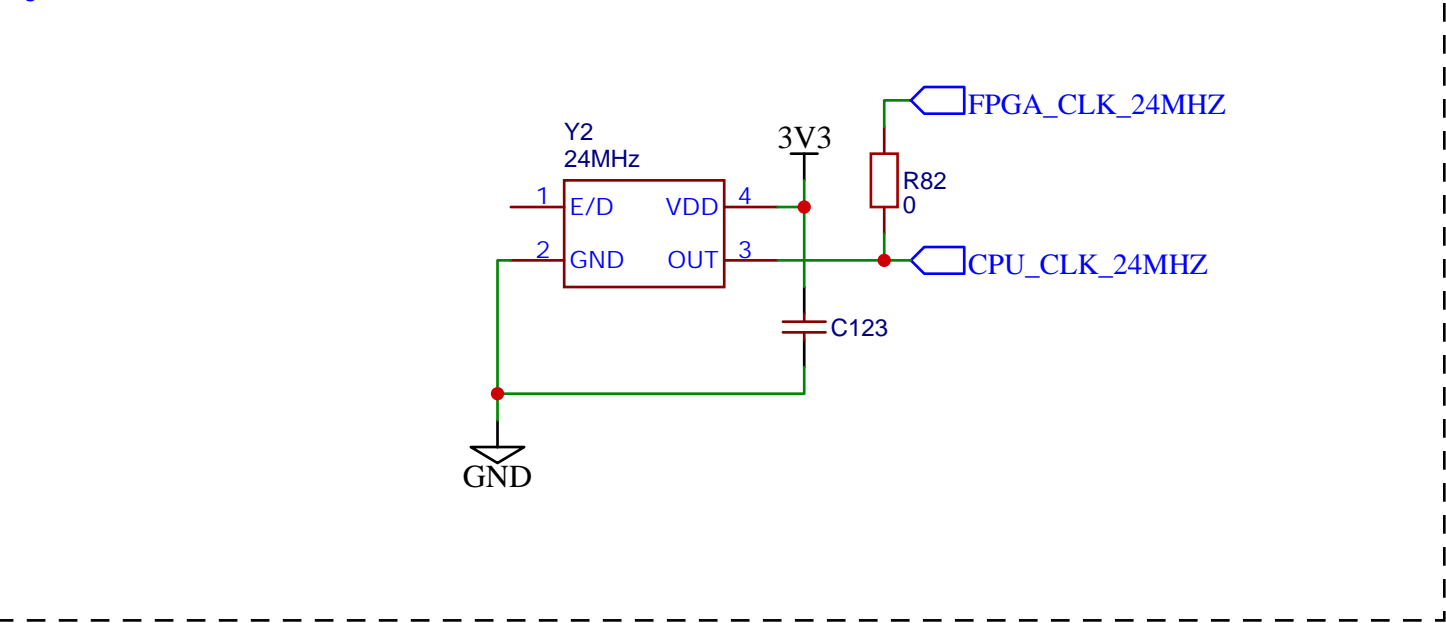
FLASH Memory



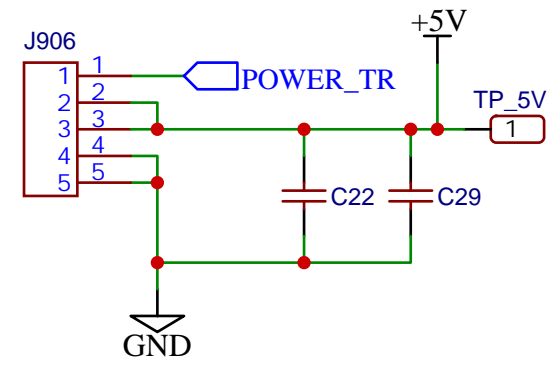
EEPROM Memory



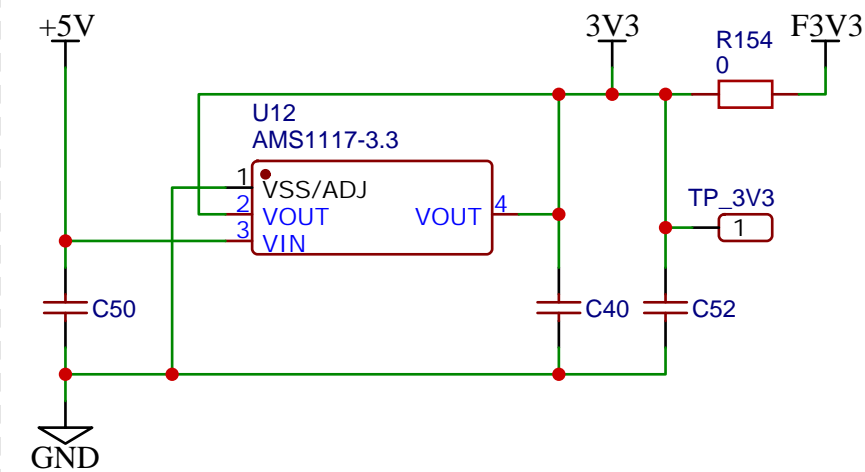
System Clock Generation



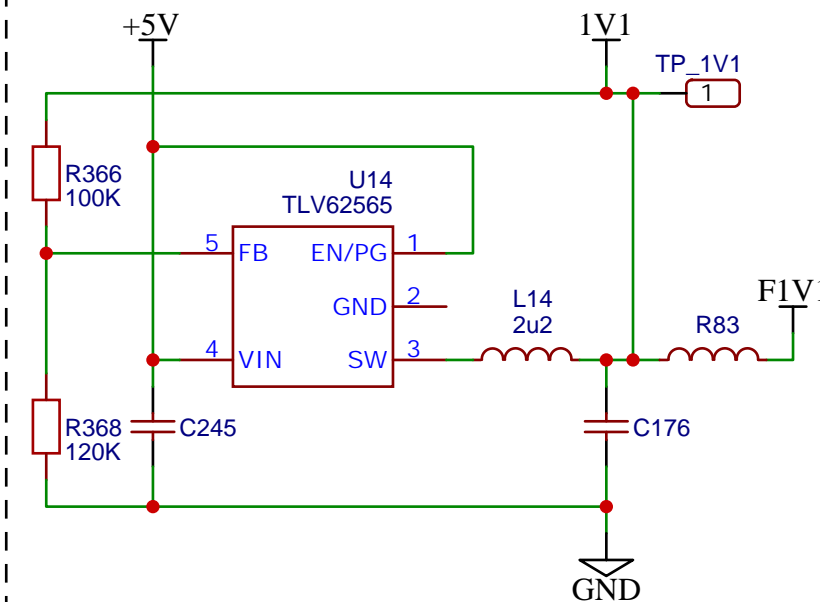
Digital Power In



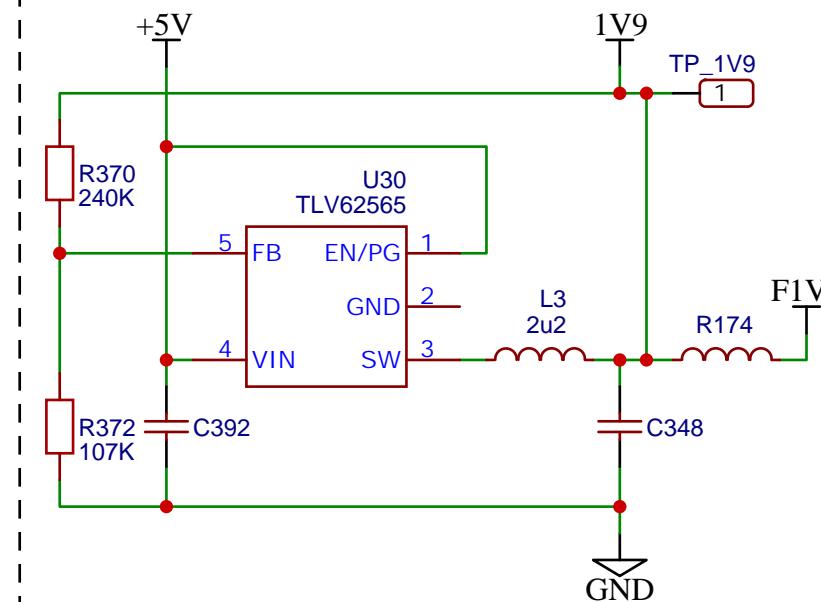
Digital Logic 3V3 Power Supply



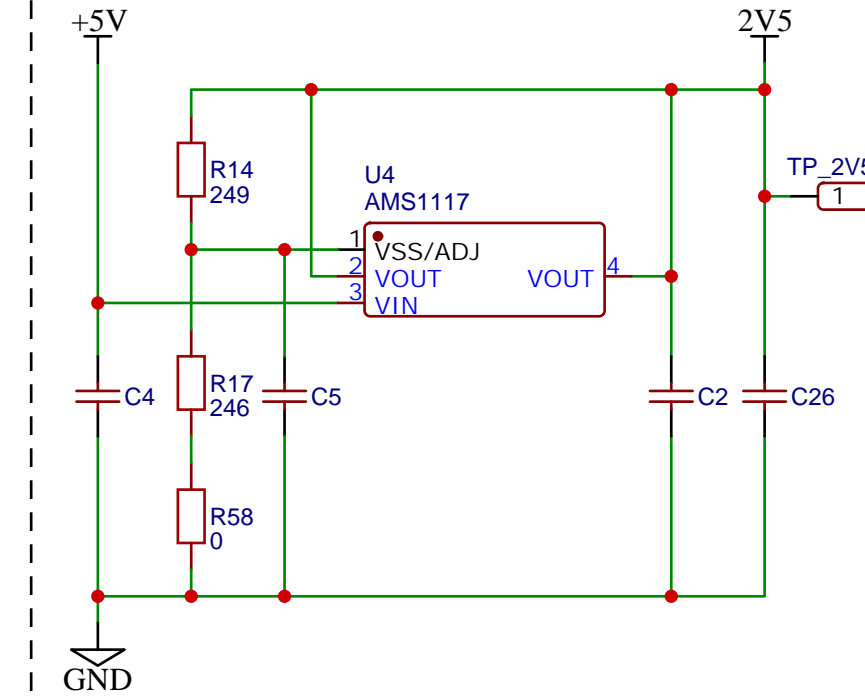
Digital Logic 1V1 Power Supply



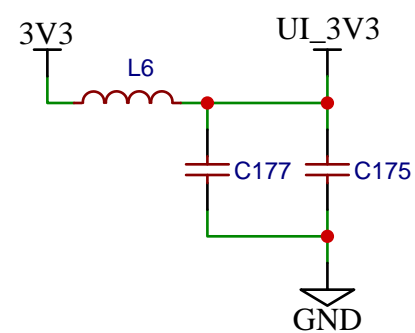
Digital Logic 1V9 Power Supply



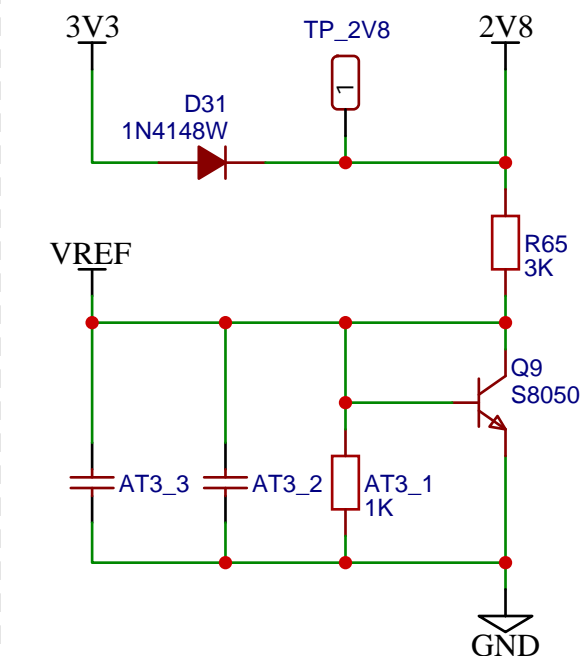
Digital Logic 2V5 Power Supply



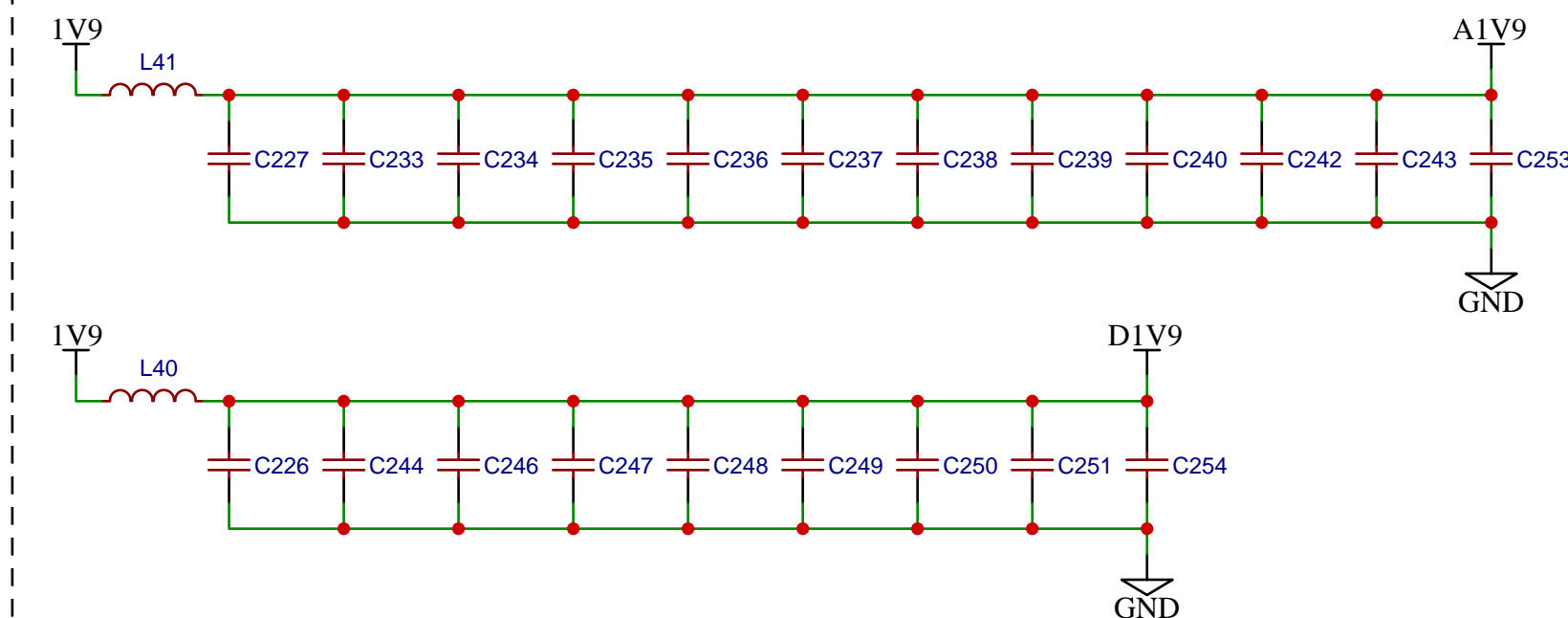
User Interface Power Filter



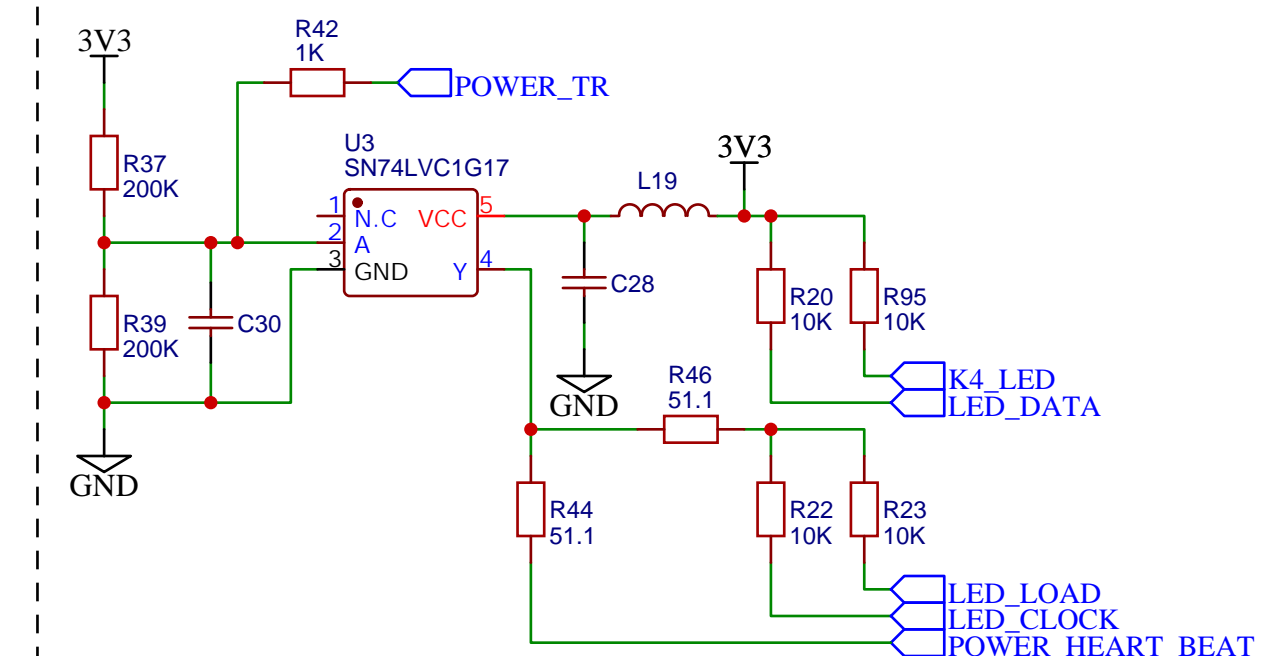
MCU Reference Voltage



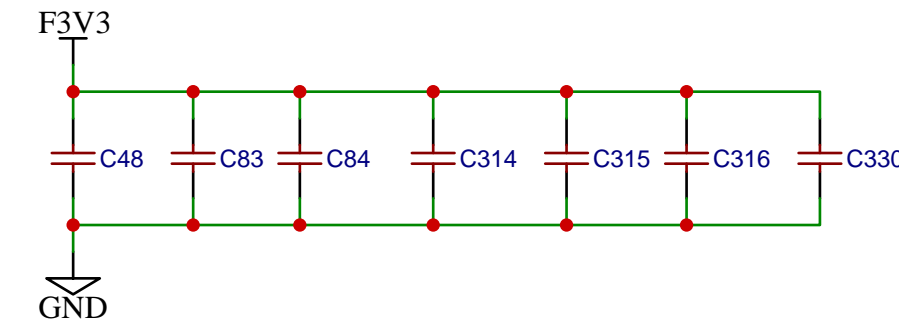
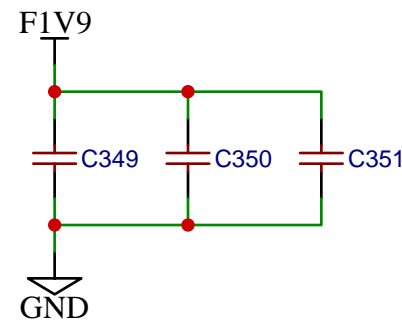
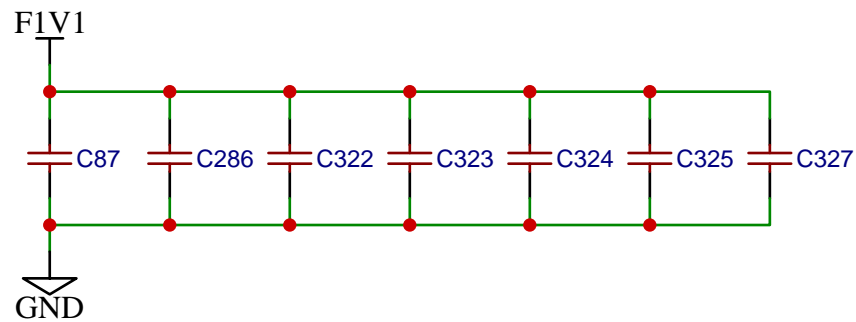
ADC Power Supply



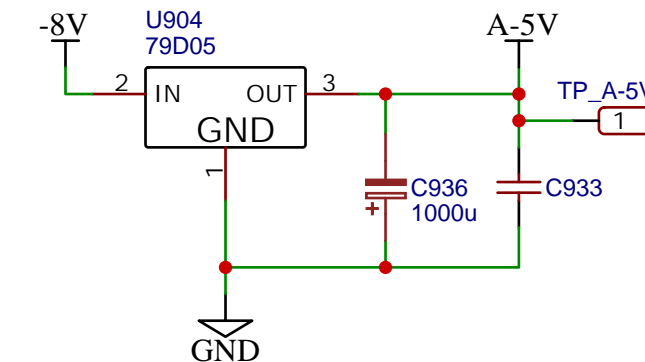
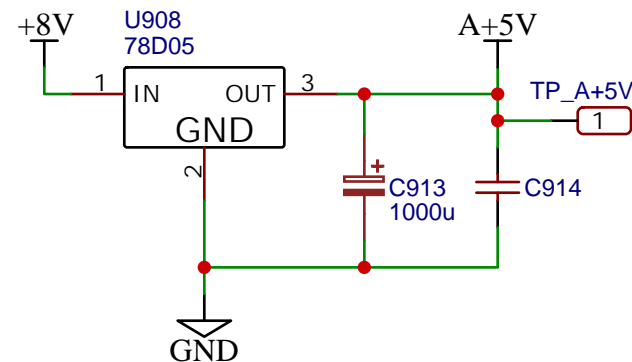
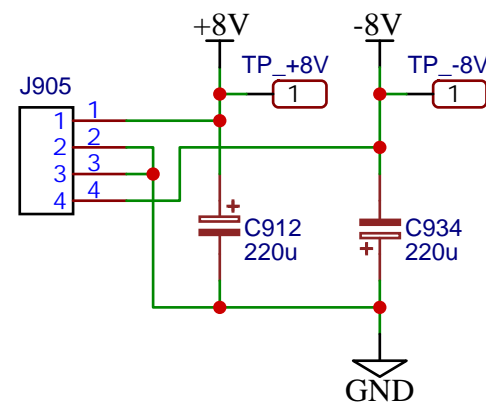
Power Heart Beat Circuit

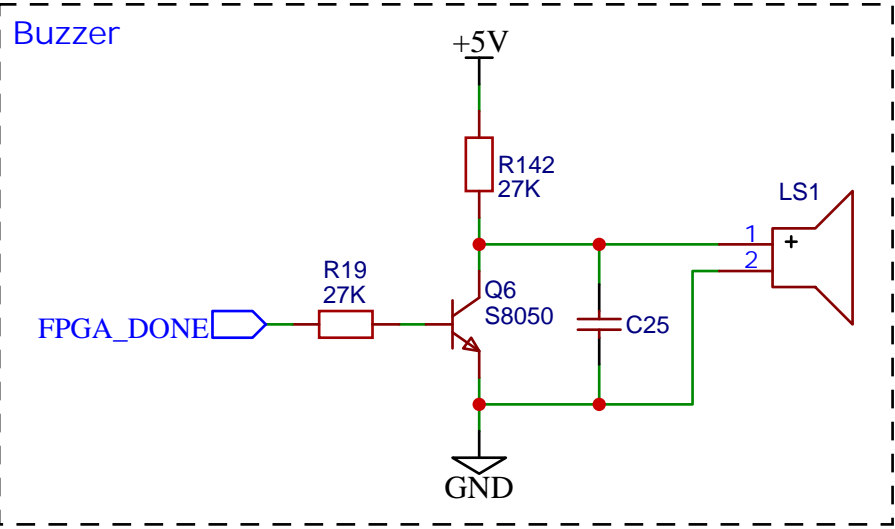
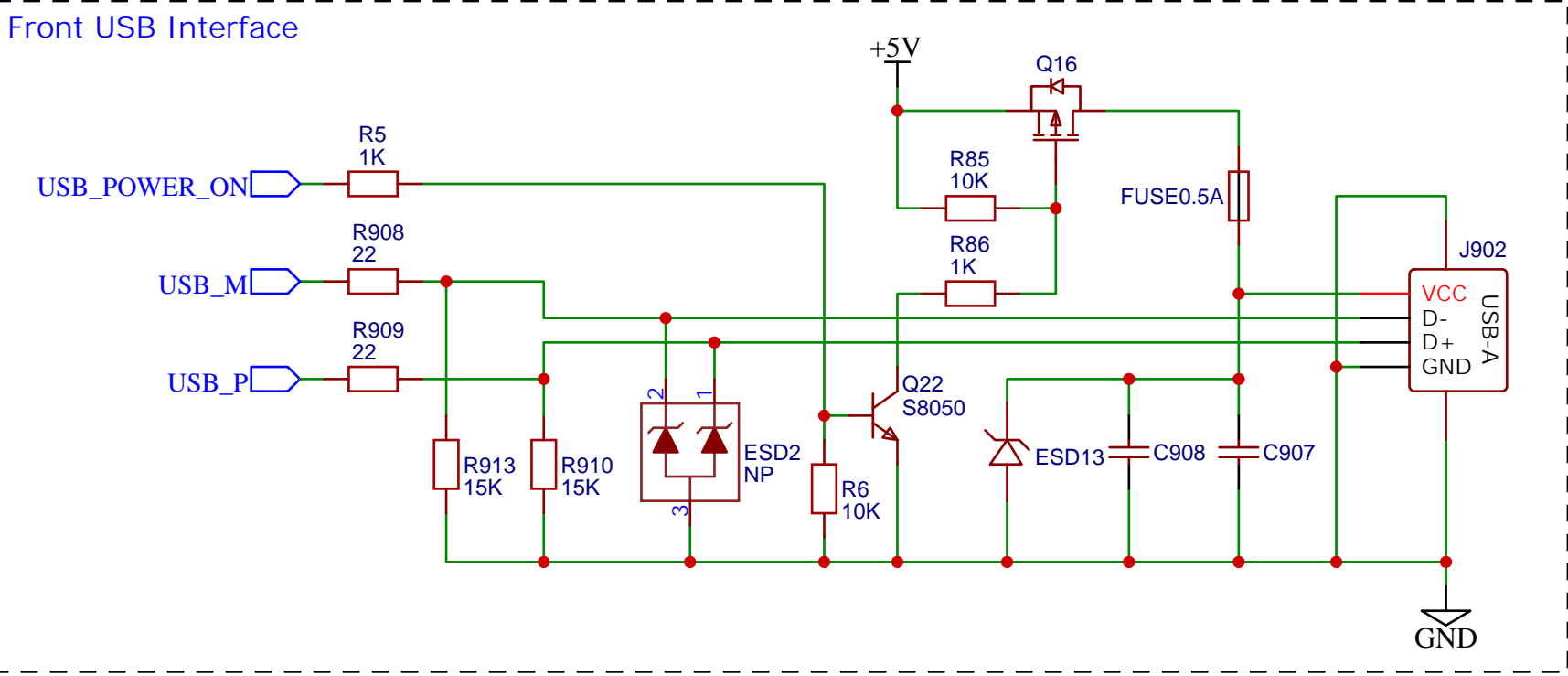
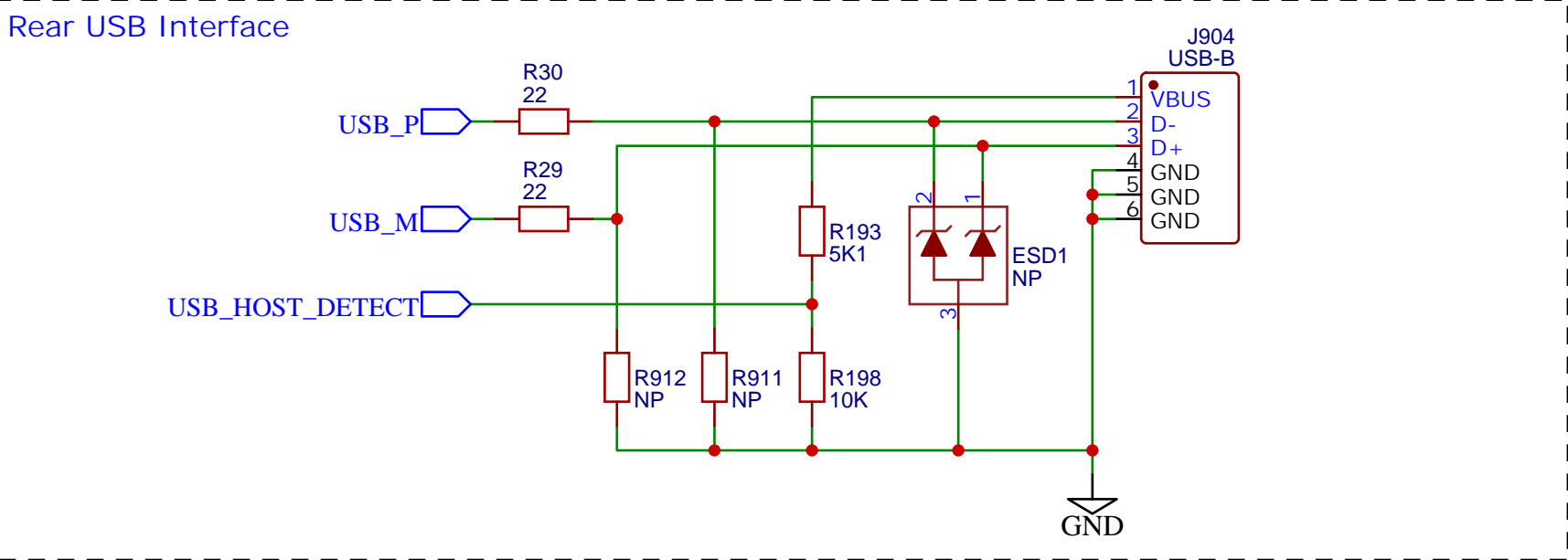
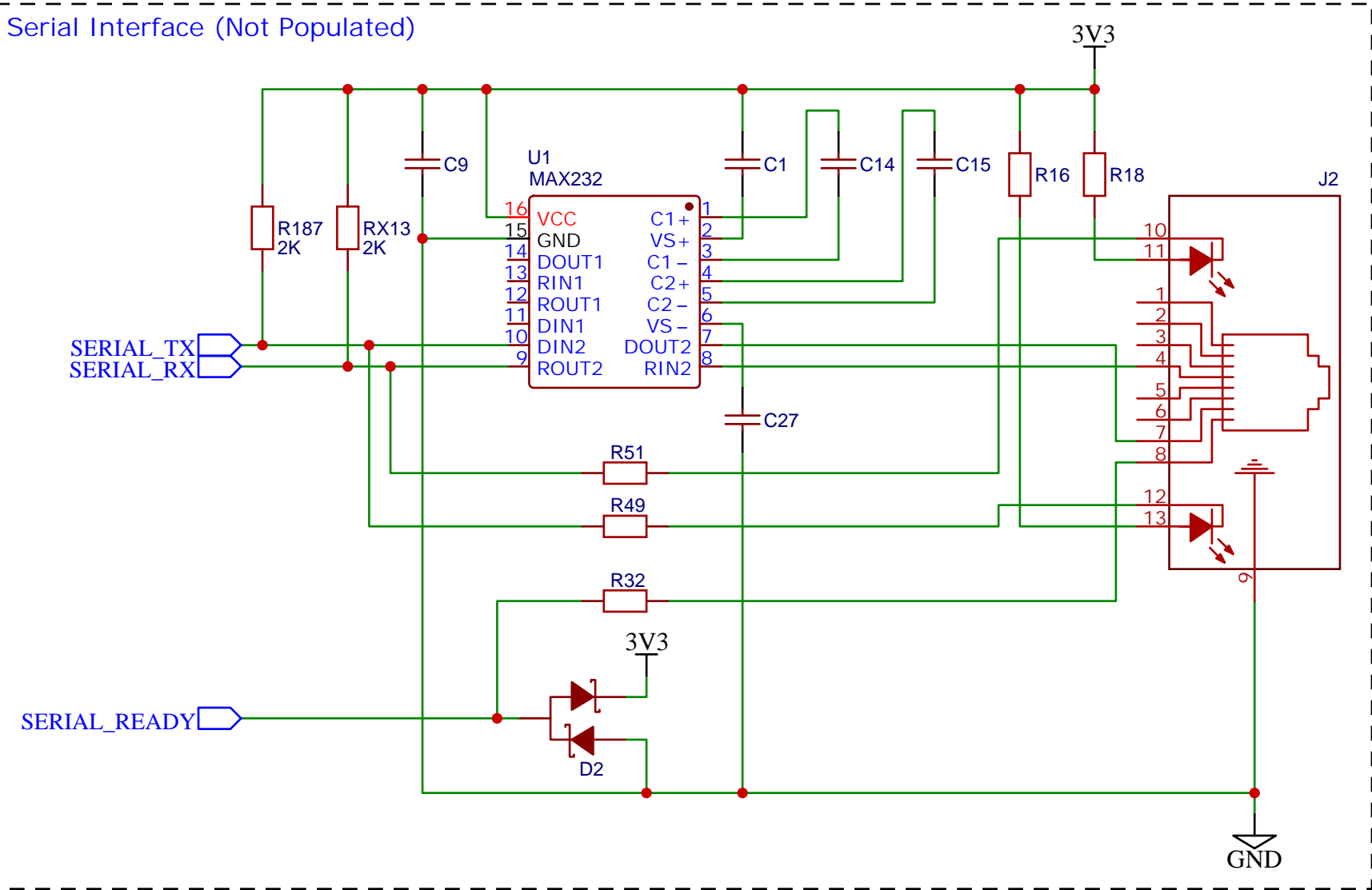
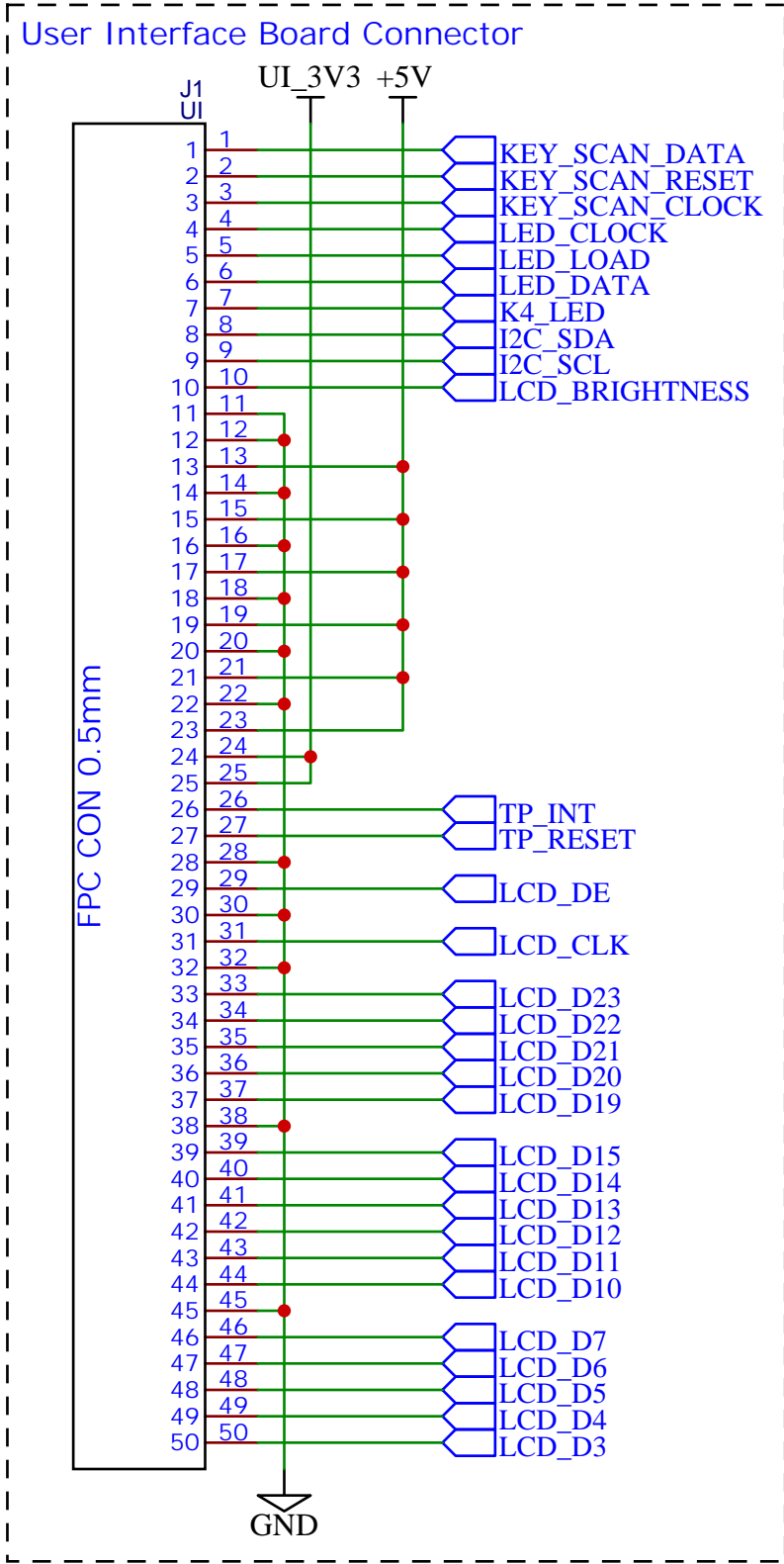


FPGA Power Filtering



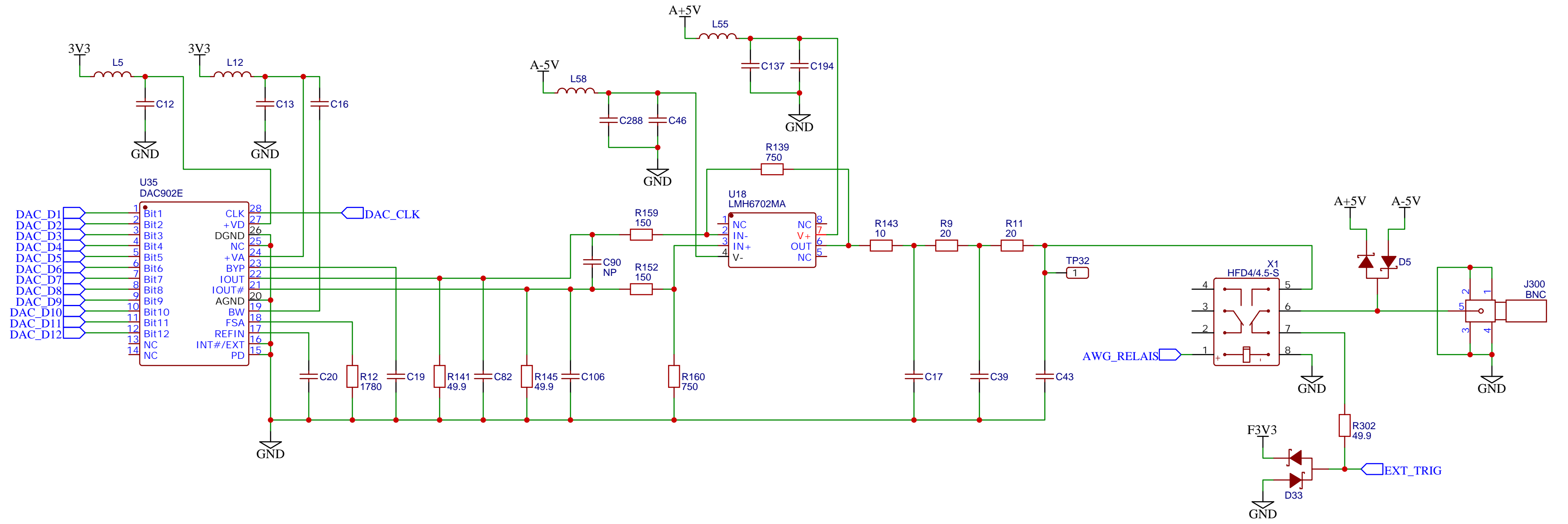
Analog Power Supply



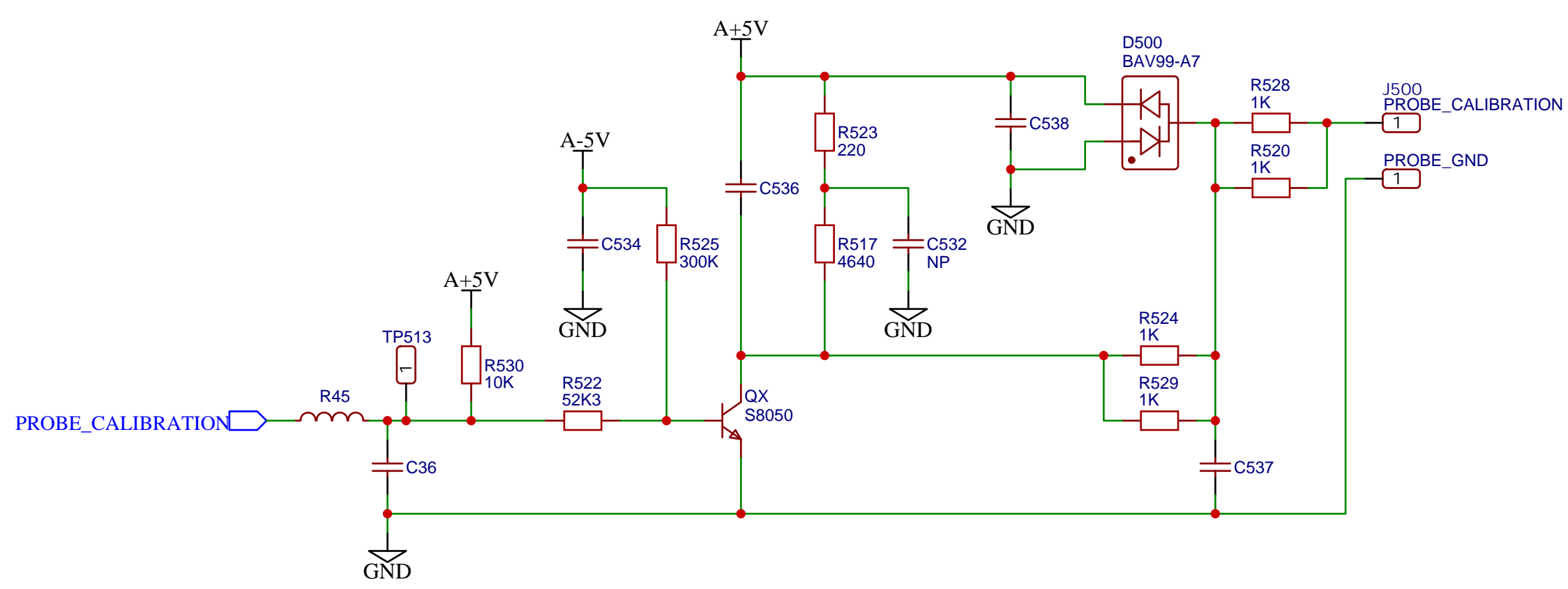


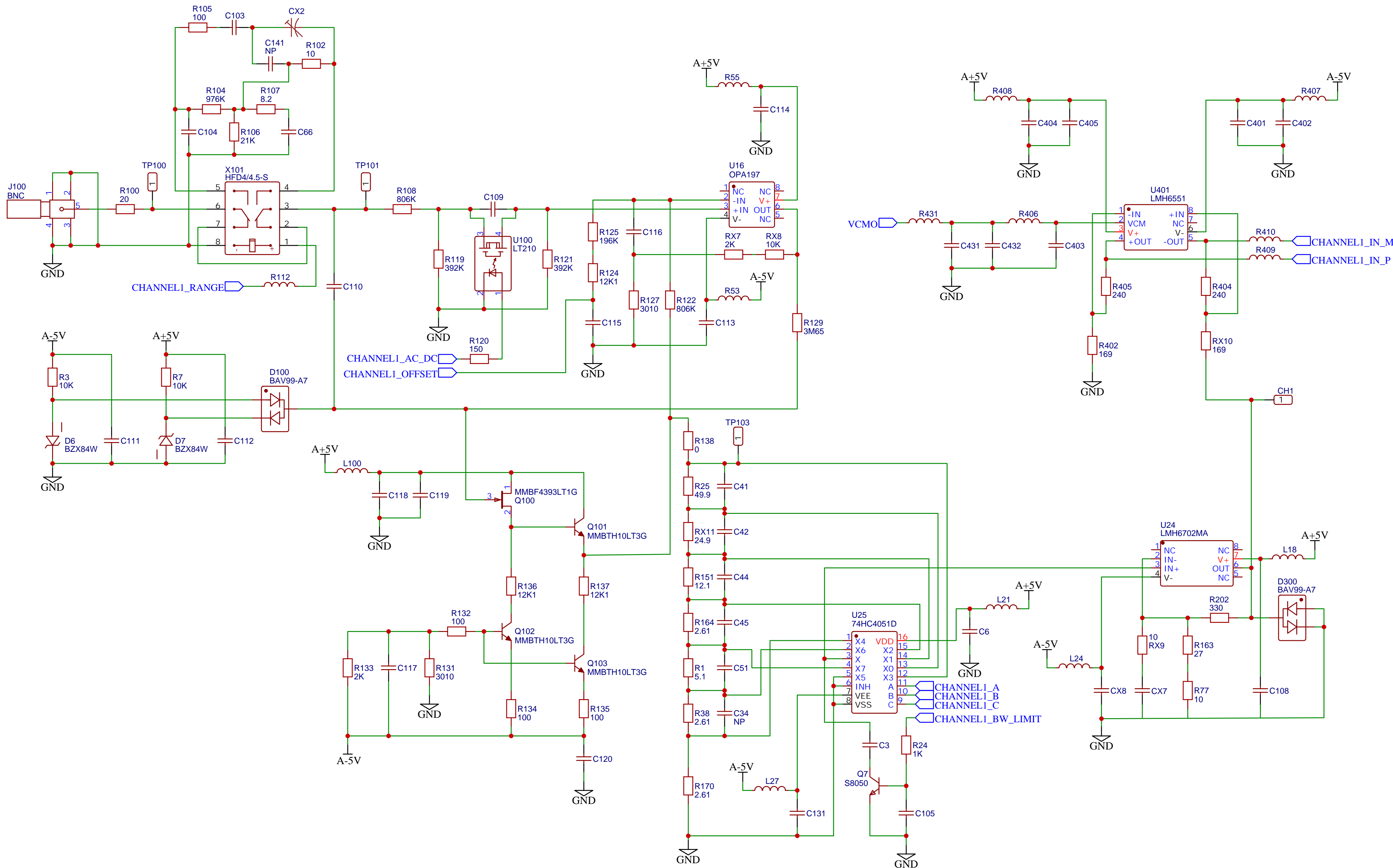
TITLE: Main Board (External Interfaces)		REV: 1.0
Company:		Sheet: 7/8
Date: 2022-06-29		Drawn By:

Waveform Generator And Trigger Input



Probe Calibration Signal





TITLE: Main Board (Channel 1)		REV: 1.0
Company:		Sheet: 4/8
Date: 2022-06-30		Drawn By:

