500 mA Negative Voltage Regulators

The MC79M00 series of fixed output negative voltage regulators are intended as complements to the popular MC78M00 series devices.

Available in fixed output voltage options of -5.0 V, -8.0 V, -12 V and -15 V, these regulators employ current limiting, thermal shutdown, and safe–area compensation, making them remarkably rugged under most operating conditions. With adequate heatsinking they can deliver output currents in excess of 0.5 A.

- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Also Available in Surface Mount DPAK (DT) Package
- Pb-Free Packages are Available

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

Device	Nominal Output Voltage
MC79M05	−5.0 V
MC79M08	−8.0 V
MC79M12	−12 V
MC79M15	−15 V

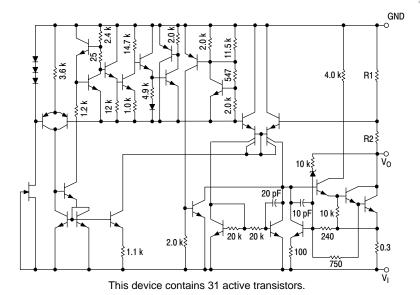


Figure 1. Representative Schematic Diagram

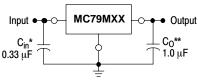


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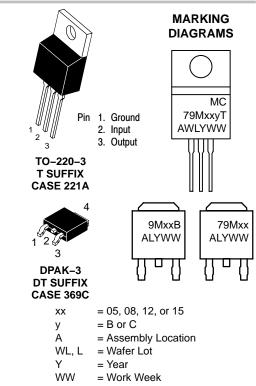
THREE-TERMINAL NEGATIVE FIXED VOLTAGE REGULATORS

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 1.1 V more negative even during the high point of the input ripple voltage. XX These two digits of the type number indicate nominal voltage.

- * C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O improve stability and transient response.



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

MAXIMUM RATINGS ($T_A = 25^{\circ}C$, unless otherwise noted.)

Rating	Syml	bol	Value	Unit
Input Voltage	VI		-35	Vdc
Power Dissipation				
Case 221A (TO-220-3)				
$T_A = 25^{\circ}C$	PD)	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	θυμ	Ą	65	°C/W
Thermal Resistance, Junction-to-Case	θυσ		5.0	°C/W
Case 369C (DPAK-3)				
$T_A = 25^{\circ}C$	PD)	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	$\theta_{J,\!\ell}$	Ą	92	°C/W
Thermal Resistance, Junction-to-Case	θυσ		6.0	°C/W
Storage Junction Temperature	T _{ste}	g	-65 to +150	°C
Junction Temperature	T _J		150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

NOTE: ESD data available upon request.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	65	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5.0	°C/W

MC79M05B, C **ELECTRICAL CHARACTERISTICS** ($V_I = -10 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 2), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	-4.8	-5.0	-5.2	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 1) $-7.0 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}$ $-8.0 \text{ Vdc} \ge V_I \ge -18 \text{ Vdc}$	Reg _{line}	_ _	7.0 2.0	50 30	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 1) 5.0 mA $\leq I_O \leq 500$ mA	Reg _{load}	-	30	100	mV
Output Voltage $-7.0 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}, 5.0 \text{ mA} \le I_O \le 350 \text{ mA}$	Vo	-4.75	_	-5.25	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	4.3	8.0	mA
Input Bias Current Change $-8.0 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}, I_O = 350 \text{ mA}$ $5.0 \text{ mA} \le I_O \le 350 \text{ mA}, V_I = -10 \text{ V}$	ΔI_{IB}		- -	0.4 0.4	mA
Output Noise Voltage, $T_A = 25^{\circ}C$, 10 Hz \leq f \leq 100 kHz	V _n	-	40	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	66	_	dB
Dropout Voltage I _O = 500 mA, T _J = 25°C	V _I –V _O	_	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ mA}, 0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	ΔV _O /ΔΤ	-	0.2	_	mV/°C

^{1.} Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

2. B = T_{low} to T_{high}, -40°C < T_J < 125°C. C = T_{low} to T_{high}, 0°C < T_J < 125°C.

MC79M08B, C ELECTRICAL CHARACTERISTICS ($V_I = -10 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 4), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-7.7	-8.0	-8.3	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 3) $-7.0 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}$ $-8.0 \text{ Vdc} \ge V_I \ge -18 \text{ Vdc}$	Reg _{line}	1 1	5.0 3.0	80 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 3) $5.0 \text{ mA} \le I_O \le 500 \text{ mA}$	Reg _{load}	-	30	100	mV
Output Voltage $-7.0 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -25 \text{ Vdc}, 5.0 \text{ mA} \le \text{I}_{\text{O}} \le 350 \text{ mA}$	Vo	-7.6	-8.0	-8.4	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	-	8.0	mA
Input Bias Current Change $-8.0 \text{ Vdc} \ge \text{V}_{\text{I}} \ge -25 \text{ Vdc}, \text{ I}_{\text{O}} = 350 \text{ mA} \\ 5.0 \text{ mA} \le \text{I}_{\text{O}} \le 350 \text{ mA}, \text{ V}_{\text{I}} = -10 \text{ V}$	ΔI_{IB}	- -	- -	0.4 0.4	mA
Output Noise Voltage, T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz	V _n	-	60	-	μV
Ripple Rejection (f = 120 Hz)	RR	54	63	-	dB
Dropout Voltage I _O = 500 mA, T _J = 25°C	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0$ mA, $0^{\circ}C \le T_J \le 125^{\circ}C$	$\Delta V_{O}/\Delta T$	-	0.4	_	mV/°C

Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately.
 Pulse testing with low duty cycle is used.

MC79M12B, C ELECTRICAL CHARACTERISTICS ($V_I = -19 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 6), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-11.5	-12	-12.5	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 5) $-14.5 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}$ $-15 \text{ Vdc} \ge V_I \ge -25 \text{ Vdc}$	Reg _{line}	- -	5.0 3.0	80 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 5) 5.0 mA $\leq I_O \leq 500$ mA	Reg _{load}	_	30	240	mV
Output Voltage -14.5 Vdc \geq V _I \geq -30 Vdc, 5.0 mA \leq I _O \leq 350 mA	Vo	-11.4	-	-12.6	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	_	4.4	8.0	mA
Input Bias Current Change $-14.5 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}, I_O = 350 \text{ mA}$ $5.0 \text{ mA} \le I_O \le 350 \text{ mA}, V_I = -19 \text{ V}$	ΔI_{IB}	- -	- -	0.4 0.4	mA
Output Noise Voltage, T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz	V _n	_	75	_	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	_	dB
Dropout Voltage I _O = 500 mA, T _J = 25°C	V _I –V _O	_	1.1	_	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ mA}, 0^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}$	$\Delta V_{O}/\Delta T$	_	-0.8	_	mV/°C

Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately.
 Pulse testing with low duty cycle is used.

Pulse testing with low duty cycle is used.

4. B = T_{low} to T_{high}, -40°C < T_J < 125°C
C = T_{low} to T_{high}, 0°C < T_J < 125°C

Pulse testing with low duty cycle is used.

6. B = T_{low} to T_{high}, -40°C < T_J < 125°C
C = T_{low} to T_{high}, 0°C < T_J < 125°C

MC79M15B, C ELECTRICAL CHARACTERISTICS ($V_I = -23 \text{ V}$, $I_O = 350 \text{ mA}$, T_{low} to T_{high} (Note 8), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	V _O	-14.4	-15	-15.6	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 7) $-17.5 \text{ Vdc} \ge V_I \ge -30 \text{ Vdc}$ $-18 \text{ Vdc} \ge V_I \ge -28 \text{ Vdc}$	Reg _{line}	_ _	5.0 3.0	80 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 7) 5.0 mA $\leq I_O \leq 500$ mA	Reg _{load}	-	30	240	mV
Output Voltage $-17.5 \text{ Vdc} \ge V_{\text{I}} \ge -30 \text{ Vdc}, 5.0 \text{ mA} \le I_{\text{O}} \le 350 \text{ mA}$	Vo	-14.25	-	-15.75	Vdc
Input Bias Current (T _J = 25°C)	I _{IB}	-	4.4	8.0	mA
Input Bias Current Change -17.5 Vdc \geq V _I \geq -30 Vdc, I _O = 350 mA 5.0 mA \leq I _O \leq 350 mA, V _I = -23 V	$\Delta l_{ m IB}$	_ _	- -	0.4 0.4	mA
Output Noise Voltage, T _A = 25°C, 10 Hz ≤ f ≤ 100 kHz	V _n	-	90	_	μV
Ripple Rejection (f = 120 Hz)	RR	54	60	-	dB
Dropout Voltage $I_O = 500$ mA, $T_J = 25$ °C	V _I –V _O	-	1.1	-	Vdc
Average Temperature Coefficient of Output Voltage $I_O = 5.0 \text{ mA}, 0^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}$	$\Delta V_{O}/\Delta T$	-	-1.0	_	mV/°C

Load and line regulation are specified at constant temperature. Change in V_O due to heating effects must be taken into account separately.
 Pulse testing with low duty cycle is used.

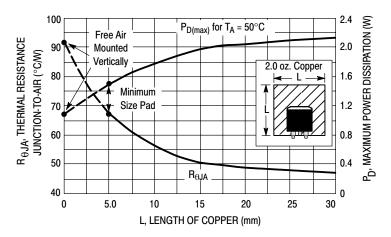


Figure 1. DPAK-3 Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

Pulse testing with low duty cycle is used.

8. B = T_{low} to T_{high}, -40°C < T_J < 125°C
C = T_{low} to T_{high}, 0°C < T_J < 125°C

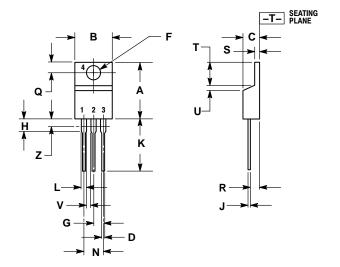
ORDERING INFORMATION

Device	Output Voltage Tolerance	Operating Temperature Range	Package	Shipping [†]
MC79M05BDT			DPAK-3	75 Units / Rail
MC79M05BDTRK		$T_J = -40^{\circ} \text{ to } +125^{\circ}\text{C}$		2500 / Tape & Reel
MC79M05BT			TO-220-3	50 Units / Rail
MC79M05CDT			DPAK-3	75 Units / Rail
MC79M05CDTRK				2500 / Tape & Reel
MC79M05CT		$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-220-3	50 Units / Rail
MC79M05CTG			TO-220-3 (Pb-Free)	50 Units / Rail
MC79M08BDT	1		DPAK-3	75 Units / Rail
MC79M08BDTRK		$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$		2500 / Tape & Reel
MC79M08BT			TO-220-3	50 Units / Rail
MC79M08CDT			DPAK-3	75 Units / Rail
MC79M08CDTRK		$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$		2500 / Tape & Reel
MC79M08CT			TO-220-3	50 Units / Rail
MC79M12BDT			DPAK-3	75 Units / Rail
MC79M12BDTG		T _J = -40° to +125°C	DPAK-3 (Pb-Free)	75 Units / Rail
MC79M12BDTRK		o a	DPAK-3	2500 / Tape & Reel
MC79M12BT	4.0%		TO-220-3	50 Units / Rail
MC79M12CDT	1		DPAK-3	75 Units / Rail
MC79M12CDTG			DPAK-3 (Pb-Free)	75 Units / Rail
MC79M12CDTRK		$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$	DPAK-3	2500 / Tape & Reel
MC79M12CT	1		TO-220-3	50 Units / Rail
MC79M12CTG			TO-220-3 (Pb-Free)	50 Units / Rail
MC79M15BDT			DPAK-3	75 Units / Rail
MC79M15BDTRK		$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$		2500 / Tape & Reel
MC79M15BT			TO-220-3	50 Units / Rail
MC79M15CDT	1		DPAK-3	75 Units / Rail
MC79M15CDTG			DPAK-3 (Pb-Free)	75 Units / Rail
MC79M15CDTRK	1		DPAK-3	2500 / Tape & Reel
MC79M15CDTRKG		$T_{J} = 0^{\circ} \text{ to } +125^{\circ}\text{C}$	DPAK-3 (Pb-Free)	2500 / Tape & Reel
MC79M15CT			DPAK-3	50 Units / Rail
MC79M15CTG			DPAK-3 (Pb-Free)	50 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

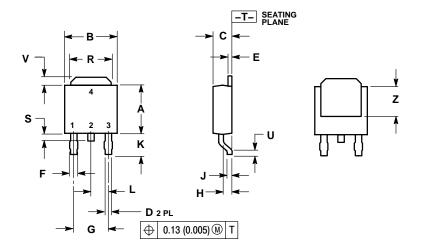
TO-220 PLASTIC PACKAGE T SUFFIX CASE 221A-09 ISSUE AA



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.570	0.620	14.48	15.75	
В	0.380	0.405	9.66	10.28	
C	0.160	0.190	4.07	4.82	
D	0.025	0.035	0.64	0.88	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.42	2.66	
Н	0.110	0.155	2.80	3.93	
J	0.018	0.025	0.46	0.64	
K	0.500	0.562	12.70	14.27	
L	0.045	0.060	1.15	1.52	
N	0.190	0.210	4.83	5.33	
Q	0.100	0.120	2.54	3.04	
R	0.080	0.110	2.04	2.79	
S	0.045	0.055	1.15	1.39	
T	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
٧	0.045		1.15		
7		0.080		2 04	

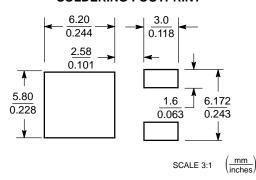
DPAK-3 DT SUFFIX CASE 369C-01 ISSUE O



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.22
В	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	88.0
Е	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180	BSC	4.58	BSC
Н	0.034	0.040	0.87	1.01
7	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090	BSC	2.29	BSC
R	0.180	0.215	4.57	5.45
s	0.025	0.040	0.63	1.01
U	0.020		0.51	
٧	0.035	0.050	0.89	1.27
Z	0.155		3.93	

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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