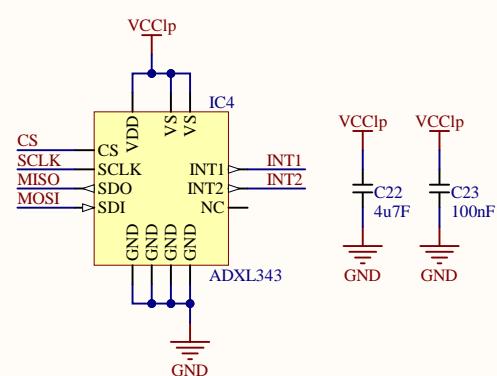
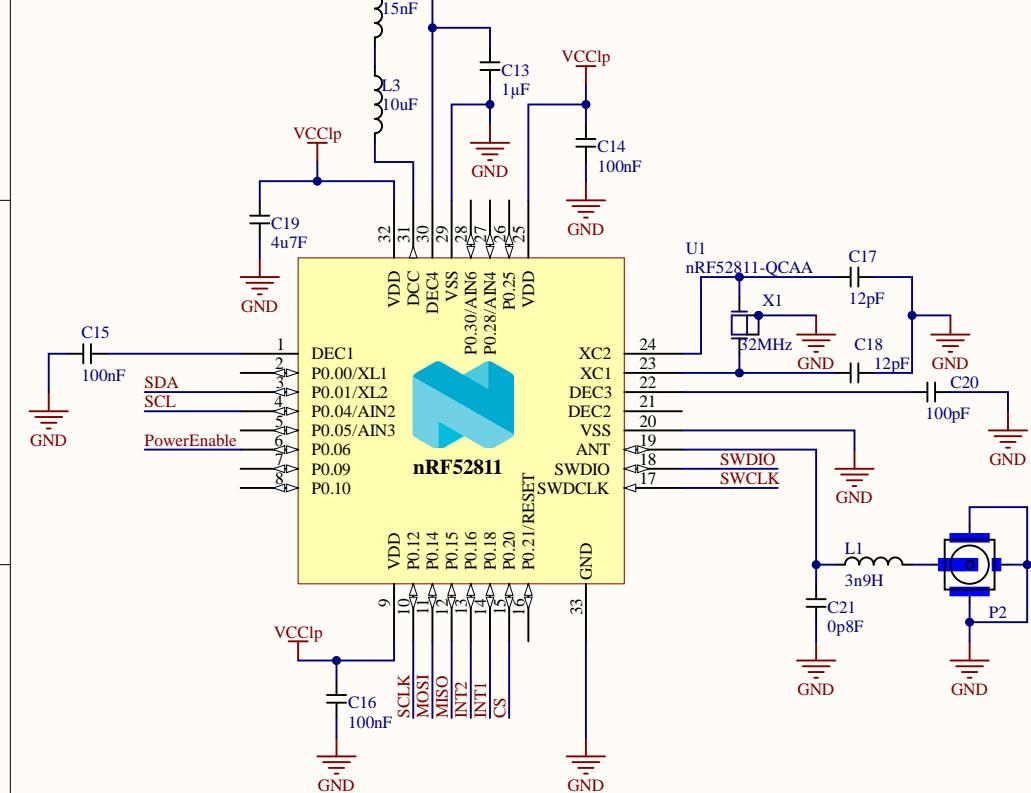
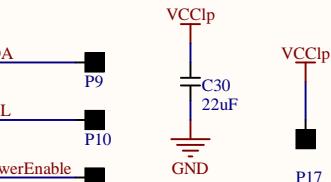
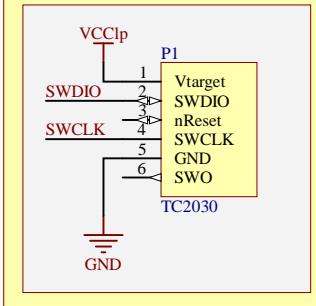


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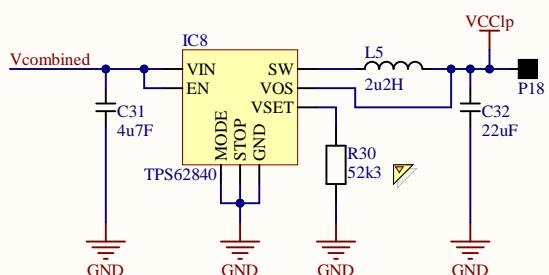
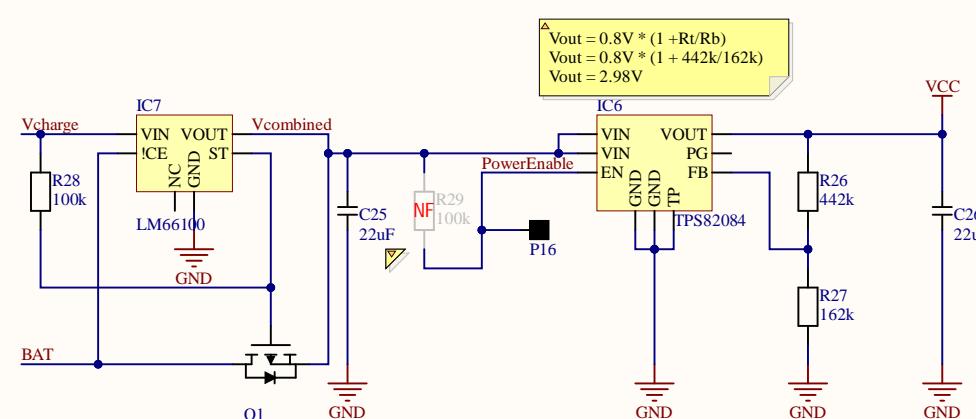
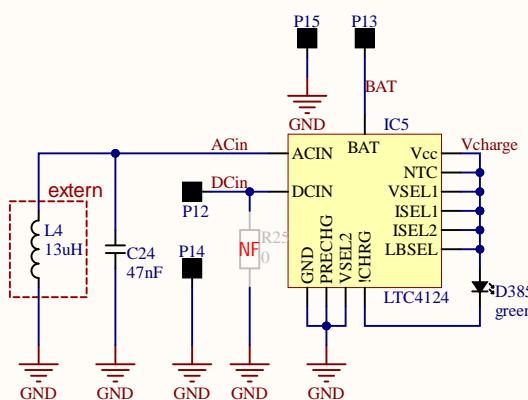
## Programming



## Title: Fair Dice

Project: FlexV1.PprjPcb	TU Darmstadt
File: Microcontroller.SchDoc	IES
Variant: Standard	Merckstraße 25
Author: Dominik G.	64283 Darmstadt
	Germany

A


**Title: Fair Dice**

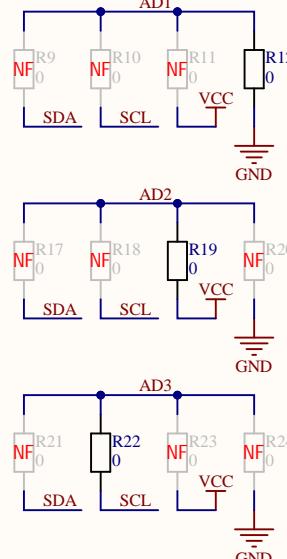
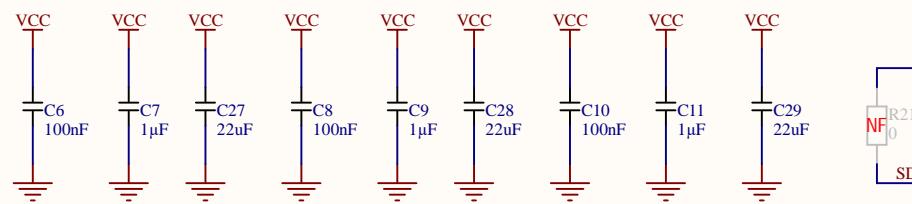
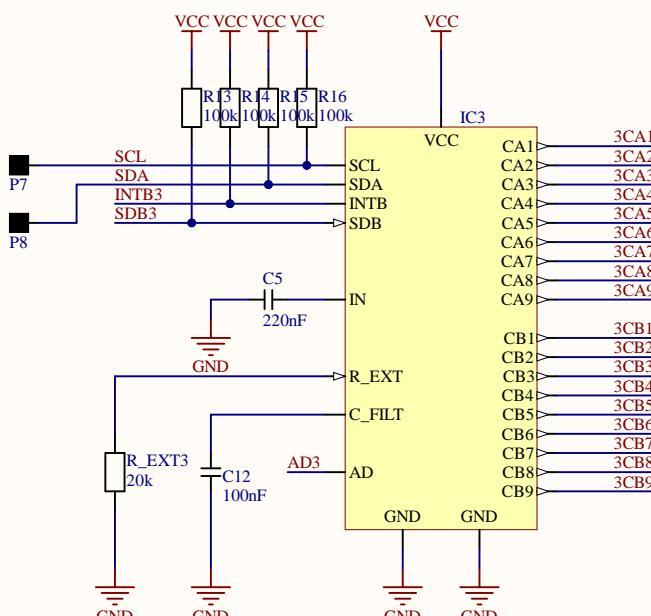
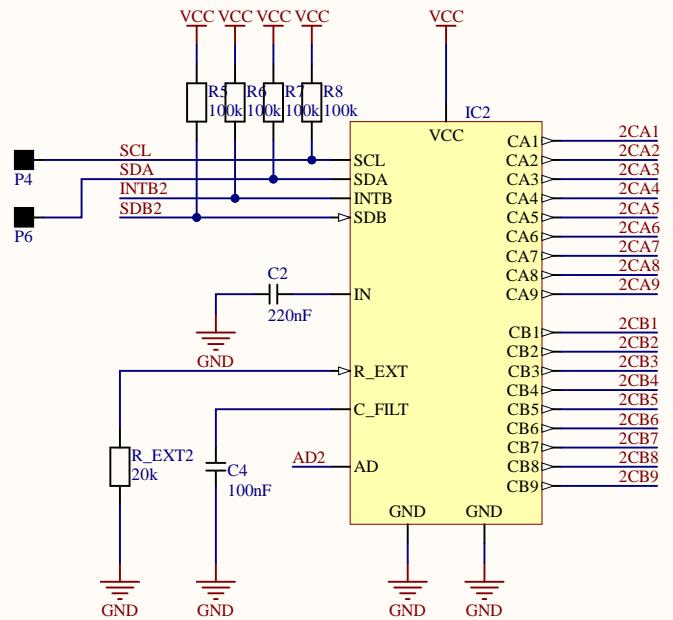
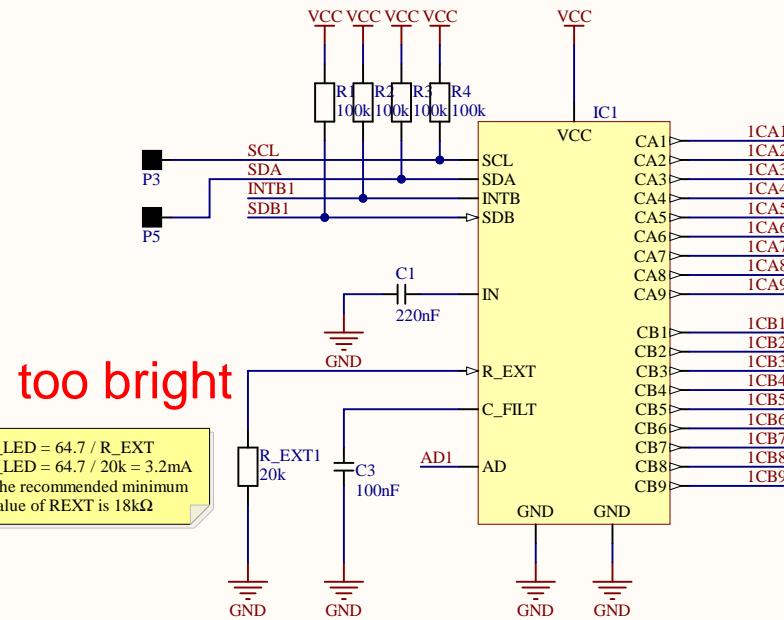
Project: FlexVI.PnjPcb

File: PowerMM.SchDoc

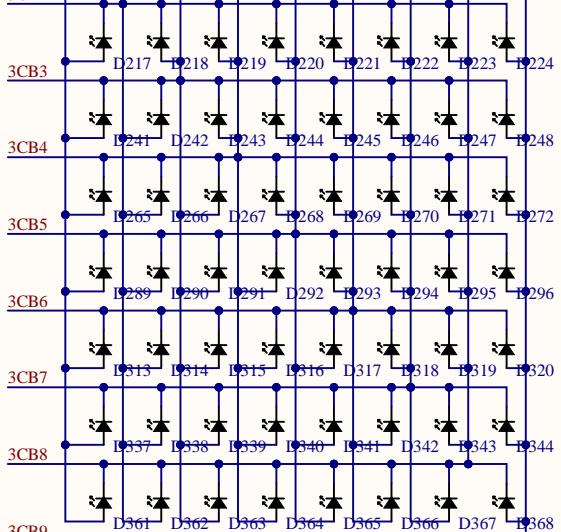
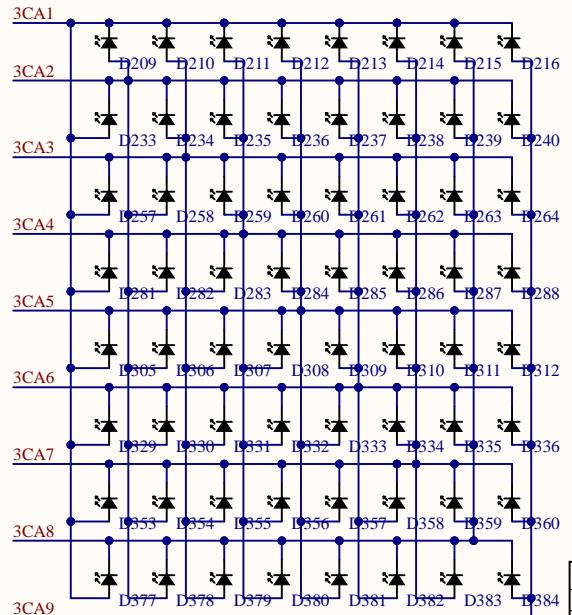
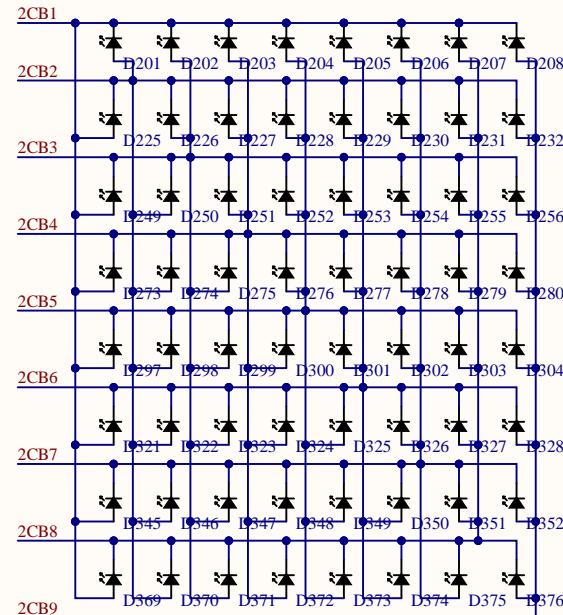
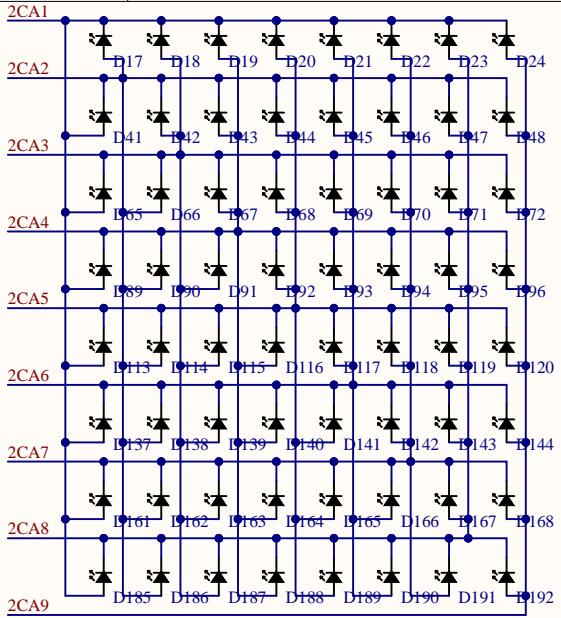
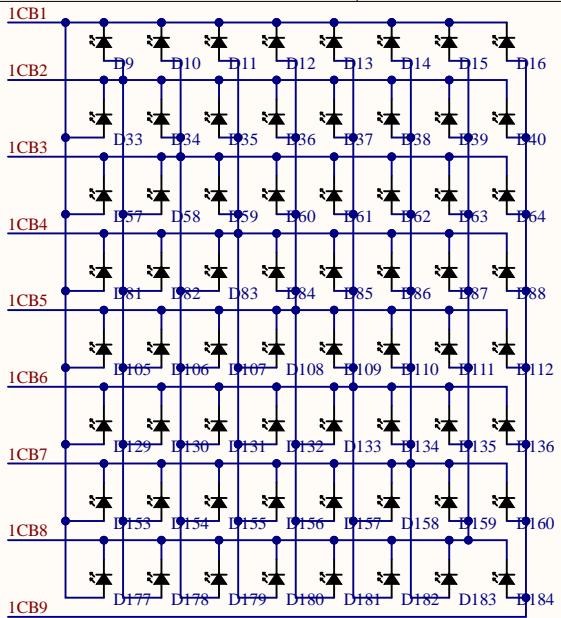
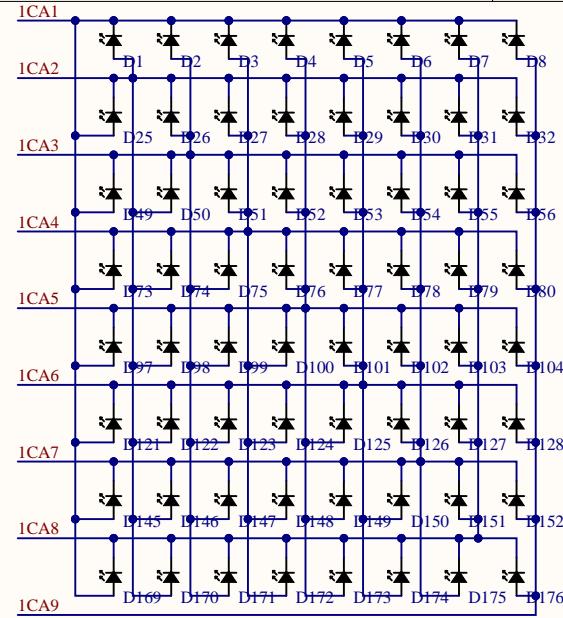
Variant: Standard

Author: Dominik G.

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**Title: Fair Dice**

Project: FlexV1.PjPcb	TU Darmstadt
File: LEDController.SchDoc	IES
Variant: Standard	Merckstraße 25
Author: Dominik G.	64283 Darmstadt
	Germany
	Sheet: of
	Revision: 0.2



**Title: Fair Dice**

Project: FlexV1.PjrPcb  
File: LEDs.SchDoc  
Variant: Standard  
Author: Dominik G.

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Date: 18.06.2020  
Revision: 0.2  
Sheet: of

