



University of Pannonia  
Department of Electrical Engineering and Information Systems  
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# OPTIMAL CURRENT CONTROL FOR DOMESTIC POWER CONVERTERS

DOI:.....

Doctoral (PhD) Thesis  
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Made at University of Pannonia Doctoral School of Information Science

Veszprém  
2019

# OPTIMAL CURRENT CONTROL FOR DOMESTIC POWER CONVERTERS

Értekezés doktori (PhD) fokozat elnyerése érdekében

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# Acknowledgement

I wish everybody the best!

# Abstract

(max 8-10 sor) Voltage unbalance is a major yet often overlooked power quality problem in low voltage residential feeders due to the random location and rating of single-phase renewable sources and uneven distribution of household loads. This paper proposes a new indicator of voltage **deviation** that may serve as a basis of analysis and compensation methods in this dimension of power quality. The paper proposes three main results. First of all a novel voltage norm capable of indicating unbalance and **under-voltage in a single value**. **Afterwards**, a three phase unbalance reduction controller structure is given. As the third main result, the proposed controller structure is integrated with an optimization based control algorithm that uses asynchronous parallel pattern search as its engine. The suggested structure and the underlying three phase power grid model has been implemented in a dynamical simulation environment and tested against engineering expectations. The simulation based experiments served as a proof of concept for the proposed complex control structure. The experiments included performance and robustness analysis, both of them concluded that the proposed control and inverter structure is promising. The proposed three phase inverter structure together with the control algorithm connected with a renewable source (photovoltaic panel or wind turbine) is capable of an asymmetric power injection **or rerouting the energy flow** to the grid so that the voltage unbalance decrease. This is also important from the environmental point of view since the achieved power loss reduction can easily be translated to CO<sub>2</sub> emission reduction and carbon footprint - these indicators has also been calculated.

===== This Thesis is consisted about optimal control of power converters. The first part is consisting of a constrained optimal control of a current source rectifier (CSR) is presented, based on a mathematical model developed in Park's frame. To comply with the system constraints an explicit model-based predictive controller was established. To simplify the control design, a disjointed model was utilised due to the significant time constant differences between the AC and DC side dynamics. As a result, active damping was used on the AC side, and explicit Model Predictive Control (MPC) on the DC side. The results are compared by simulation with the performance of a state feedback control. This is followed by developing a cost function for mitigating voltage asymmetry on a domestic network, which requires only measuring the voltage whilst only current interventions are required. Lastly an asymmetric inverter structure was developed to serve the cost function minimizing compensational needs. Results were implemented

and validated with Matlab simulations.

# List of contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
1.1	Motivation . . . . .	5
<b>2</b>	<b>Baic notions</b>	<b>6</b>
2.1	Basic notions for voltage unbalance reduction . . . . .	6
2.2	Basic notions for current source rectifier and EMPC . . . . .	6
<b>3</b>	<b>Constrained, explicit predictive control for current source buck-type rectifiers</b>	<b>7</b>
3.1	Mathematical Modeling of the CSR . . . . .	7
3.2	Model simplification . . . . .	8
3.3	Control structure . . . . .	9
3.4	DC-side explicit model predictive control . . . . .	11
3.5	Active AC-side damping . . . . .	13
3.6	Space vector modulation strategy . . . . .	14
3.7	Performance evaluation . . . . .	17
3.7.1	Computational effort . . . . .	17
3.7.2	Horizon performance . . . . .	18
3.7.3	Simulation results . . . . .	19
3.7.4	Comparison with a state feedback control . . . . .	19
3.8	Conclusion . . . . .	22
<b>4</b>	<b>Geometrical indicator for voltage asymmetry in three phase networks</b>	<b>23</b>
4.1	Network structure . . . . .	23
4.2	<b>Voltage deviations</b> . . . . .	23
4.2.1	Standard indicators of unbalance . . . . .	24
4.2.2	Proposed geometrical indicator . . . . .	25
<b>5</b>	<b>Voltage unbalance compensation with optimization based control algorithm and asymmetrical inverter structure</b>	<b>30</b>
5.1	Unbalance compensation . . . . .	30
5.1.1	Problem statement . . . . .	30
5.1.2	Control problem . . . . .	31
5.1.3	Asymmetrical inverter structure . . . . .	31
5.1.4	Optimization based control algorithm . . . . .	34
5.2	Discussion . . . . .	36

5.2.1	Dynamical simulation based experiments . . . . .	36
5.2.2	Performance analysis . . . . .	36
5.2.3	Environmental effect . . . . .	38
5.3	Conclusion . . . . .	39
<b>6</b>	<b>Summary</b>	<b>41</b>
<b>7</b>	<b>Thesis</b>	<b>42</b>
7.1	Playground . . . . .	42



# Chapter 1

## Introduction

Recently, due to the growth of distributed generation from renewable energy sources the nature of the electrical power grid tends to alter from a passive network to an active one. In this new environment, the importance and difficulty of maintaining the security and operational stability of the distribution system are increasing together. As opposed to this expectation most of the household renewable sources and loads are unevenly distributed single phase power converters, some of them representing high power consumption. Ideal generators supply symmetrical three-phase sinusoidal positive sequence voltages, which are balanced in terms of their amplitudes phase differences at a single frequency. The terminology of unbalance can be divided into amplitude unbalance, phase difference unbalance, and unbalanced harmonic disturbance. The occurrence of at least one of these features is enough for a distribution network to become unbalanced. The situation is further exacerbated by the stochastic on/off switching of the different types of loads which causes stochastic disturbing unbalance in the load currents which causes unbalanced load of the low voltage transformer, and causes amplitude and phase unbalance in the voltage phasor through the serial impedance of the low voltage transportation line wires and connecting devices cables. Many countries have changed their regulating laws about power supply to allow for grid-tie inverter systems to provide spare power from renewable sources to local low voltage grids. The unbalance of the grid is further increased by using single phase grid tie inverter systems in the size of typical small household power plants (1 - 50 kW) and the produced electrical power originating from renewable power source (wind and solar) also admits stochastic behavior. This unbalance yields to a suboptimal operation of low voltage three phase transformers to generate undesirable additional power loss and increase in the probability of malfunction of the low voltage energy transportation system. The effective current unbalance causes additional power loss of the transportation line resistances too.

Single phase power injections to the grid are mainly generated by domestic photovoltaic-(PV) and wind power plants. Many current references about the renewable energy sources integration to the low voltage grid at short time and long time storage ready for connection of this electrical energy in the literature can be found. For off-grid solutions sometimes more complex solu-

tions integrating diesel generators, PV and wind generators are proposed, e.g. in [46]. [10] were presented the economical aspects of a PV system. The economic results are strongly influenced by the annual average insolation value, which encourages the areas most exposed to the sun and the southern areas. The consumption of consumers is not critically important, but the design principle used has as significant effect on the maximization of the performance of PV plants. [22] notify, that autonomous photovoltaic systems are strongly responsible of their reactive energy requirements. To support photovoltaic systems with battery banks sustainable character one should be able to establish that their reactive energy requirement share fairly compensated by the corresponding energy yield. Additionally, [39] emphasize that PV systems are increasingly being deployed in all over the world, and this is the source of a wide range of power quality problems. With a view to consistently measuring and assessing the power quality characteristics of PV systems, they had presented an in-depth overview and discussion of this topic. The study by [44] explored integration issues of electric vehicle battery packs. They suggest that high voltage battery packs with large format cells has advantages in assembly, thermal management, monitoring and control, services and maintenance. On the other hand, quality, reliability and limited specific energy of large format cells are obstacles need to overcome. Solving these problems will further affect the cost, performance, reliability and safety of the electric vehicles. Smart energy systems in specially in urban areas are discussed in [30] where a design methodology has been suggested.

There are different consequences of unbalance. [27] were discussing the effects of unbalanced voltage on a three-phase induction motor, one has to consider not only negative-sequence voltage but also the positive-sequence voltage. With the same voltage unbalance factor, the status of voltage unbalance could be judged by the magnitude of positive sequence voltage. Also the effect of voltage unbalance has been studied on three-phase four-wire distribution networks for different control strategies for three-phase inverter-connected distributed generation units on voltage unbalance in distribution networks ([32]). Here the negative-sequence component and the zero sequence component were studied where unbalance conditions could lower stability margin and increasing the power losses. On the other hand, the adaptive coordination of distribution systems included distributed generation is also an emerging problem as it was discussed by [5]. A small voltage unbalance might lead to a significant current unbalance because of low negative sequence impedance ([6]) too. There are different approaches of lowering the unbalance with different control techniques. Additionally, new computationally efficient control techniques have been presented by [28] to estimate and compensate input voltage-unbalance disturbances for a voltage source converter. These tools are designed to be effective with high power systems with slower PWM switching frequencies of 5 kHz or lower and limited current-controller bandwidth. About the unbalance compensation control aspect, a three-phase IGBT-based static synchronous compensator were proposed for voltage and/or current unbalance compensation by [54]. An instantaneous power theory was used for real-time calculation

and control. Three control schemes current control, voltage control and integrated control were proposed to compensate unbalanced voltage, unbalanced current or both. The unbalance phenomena and power quality can be examined with modeling too. A particular modeling method was presented by [29] where a three-phase four-wire grid-interfacing power quality compensator were modeled. During grid voltage unbalance, the compensator, used a shunt and a series four phase inverter, and could enhance both the quality of power within the microgrid and the quality of currents flowing between the microgrid and utility system. The shunt four-leg inverter were controlled to maintain a set of balanced distortion free voltages to regulate power sharing among the parallel-connected distributed generation systems. Simulation studies were carried out by [21] where one of the aims was to develop and test the feasibility of a decoupled three-phase on-load tap charger in the distribution system with the objective of improving the distribution network power quality. Further control methods were applied for the solution for balancing of the most sensitive with regard to electric energy quality part of power system by [25], minimizing the active power losses, stabilization of three-phase voltages, enhancement of asynchronous machine performance stability and reduction of errors occurring in power consumption measuring circuits. Further control methods were applied for the solution for balancing of the most sensitive with regard to electric energy quality part of power system in [25], minimizing the active power losses, stabilization of three-phase voltages, enhancement of asynchronous machine performance stability and reduction of errors occurring in power consumption measuring circuits. In our Department we have antecedents of research of power quality in low voltage distribution grid. A previous work of [17] a complex control unit has been proposed that is capable of lowering extant harmonic distortion. In the work of [16] the effect of a small domestic (photovoltaic) power plant on the power quality, mainly the total harmonic distortion has been examined. The aim of this work is to **examine and compensate three phase voltage asymmetry of the electrical network based on the extended simulation model proposed by [17]**.

In many articles the authors presents a different viewpoint of calculating unbalance on the network. [31] showed to assess the harmonic distortion and the unbalance introduced by the different loads connected to the same point of common coupling have been applied to an experimental distribution network. By [24] the focus was to bring out the ambiguity that crops up when we refer to a particular value of voltage unbalance that exists in the system. By making use of the complex nature of voltage unbalance, the voltage combinations that lead to the calculation of complex voltage unbalance factor could be narrowed down to a great extent. A fast and accurate algorithm for calculating unbalance has been presented by [53]. The magnitudes of zero, positive, and negative sequences are obtained through simple algebraic equations based on the geometric figure, which is also called as 4 and 8 geometric partitions. Also a three-phase optimal power flow calculation methodology has been presented by [3], that is suitable for unbalanced power systems. The optimal algorithm uses the primal-dual interior point method as an optimization tool in associa-

tion with the three-phase current injection method in rectangular coordinates.

=====

Current source rectifiers (CSR) are widely used in front-end power electronic converter for the uncontrollable or controllable DC-bus in industrial and commercial applications. They have maintained their position through many applications, with uses such as medium-voltage high-power drives [50], [14] STATCOMs [18] and renewable systems [9], [11]. They have a plain and reliable circuit structure, which makes them attractive for simple control design. The CSRs are traditionally controlled by classic cascaded linear control loops such as PI controllers. These simple control applications are suitable for induction motor control [8], and other electromechanical actuators [43], and unusual topologies [36]. Also, worth mentioning of self-tuning variants of PI controllers [48]. In the past, the modulation methods used were trapezoidal pulse width modulation techniques (TPWM), or application of pulse patterns calculated offline for selective harmonic elimination (SHE). More recently, current space vector modulation (SVM) has been used for the synthesis of the transistor control signals [13]. Even so, AC-side harmonic elimination could still be an issue at lower switching frequencies where LCL filtering would be advised [20]. In order to keep switching frequencies low and to minimize switching losses, new topologies and hybrid modulations are used, mixing TPWM and SHE depending on the grid frequency [51]. In terms of the amplitude of the grid and DC-link voltages, CSRs exhibit a step-down conversion. When used as DC voltage source, the rectifier can output a lower DC voltage without the need of a grid-side transformer, as is usually employed in voltage source rectifiers (VSR). Because of their current source behaviour, CSRs can be easily paralleled and provide inherent short-circuit protection, representing an excellent potential in DC power supply applications [12], [55]. There are several control strategies in addition to classical PI control for applications in this domain. Self-adapting control methods are on the rise with more sophisticated algorithms in the field of fuzzy logic [49]. They are capable of handling increasingly more complicated models and systems with high dynamics and accuracy [7], [19], and even without establishing and validating classical state-space models [52]. The other field is the sliding mode control, which can achieve good dynamic performance and handle non-linearity. Still, they might also introduce chattering, which can be very undesirable when applied to real-life systems like in [41] and [47]. In the linear domain implicit model predictive control (IMPC or just MPC) is a fair solution due its effectiveness in power electronics due to its configurable cost function and such scalable nature [23], [2]. In this field also finite-state solutions are present which can be considered also predictive control, where the modulation scheme's defined states serve as optimization potential [42], [15]. As a further step adaptive application was established to tackle parameter estimation problems for better performance [33] Recently, beside implicit, finite-state, and adaptive predictive control, explicit model predictive control has emerged in the field of power electronics [26]. Establishing the MPC cost function can range widely depending

on the expected dynamics, degree of noise cancellation, and model complexity. Additionally, the current limitation can also be implemented introducing constraints in the modulation algorithm. In [2] the validity of an MPC-based, digital pulse width modulation control strategy for single-phase voltage source rectifiers is discussed, further confirming the validity of this method in control systems.

## 1.1 Motivation

This stuff is so important and interesting! (max 2500 karakter)  
Outline of the thesis.

# Chapter 2

## Baic notions

### 2.1 Basic notions for voltage unbalance reduction

Basic notions for voltage unbalance reduction

### 2.2 Basic notions for current source rectifier and EMPC

Basic notions for current source rectifier and EMPC.

## Chapter 3

# Constrained, explicit predictive control for current source buck-type rectifiers

### 3.1 Mathematical Modeling of the CSR

The structure of the classical three phase buck-type current source rectifier (CSR) is presented in 3.1. In continuous current mode, the differential equations corresponding to the CRS's inductor currents and capacitor voltages are the following:

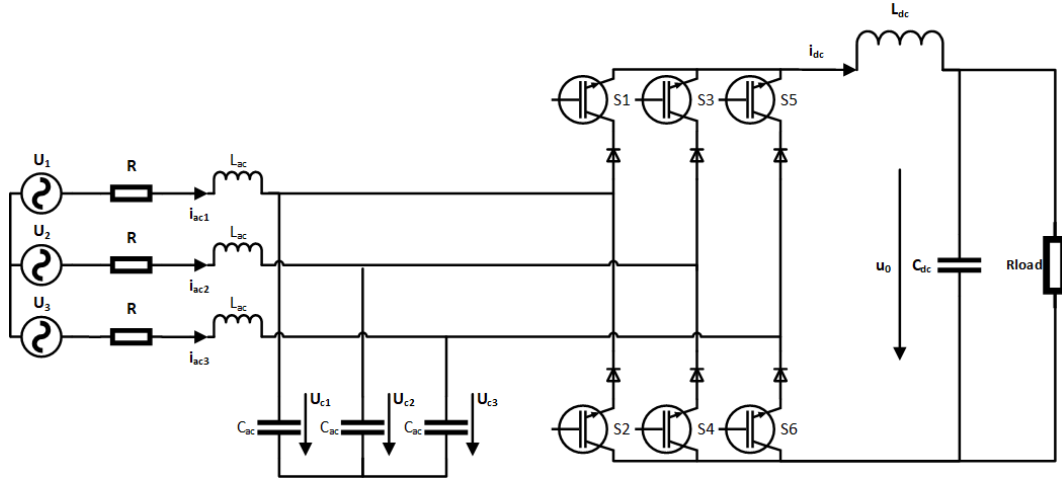


Figure 3.1: Circuit diagram of the three-phase buck-type rectifier with insulated gate bipolar transistors (IGBTs).

$$\begin{aligned}
 L_{ac} \dot{i}_{acp} &= u_p - u_{c_p} - R i_{acp} \\
 C_{ac} \dot{u}_{c_p} &= i_{acp} - \delta_p i_{dc} \\
 L_{dc} \dot{i}_{dc} &= (\sum_{p=1}^3 \delta_p u_{c_p}) - u_0 \\
 C_{dc} \dot{u}_0 &= i_{dc} - \frac{u_0}{R_{load}}
 \end{aligned} \tag{3.1}$$

where  $p \in \{1, 2, 3\}$  is the index of three phases and  $\delta_p$  describes the conduction state of the rectifier leg  $p$ .

$$\delta_p = \begin{cases} 1 & \text{if the upper transistor is ON} \\ -1 & \text{if the lower transistor is ON} \\ 0 & \text{if both are ON or OFF} \end{cases} \quad (3.2)$$

Using the components in the stationary frame of the space phasors of the three-phase quantities, from 3.1 it results:

$$\begin{aligned} L_{ac}\dot{i}_{ac_\alpha} &= u_\alpha - u_{c_\alpha} - Ri_{ac_\alpha} \\ L_{ac}\dot{i}_{ac_\beta} &= u_\beta - u_{c_\beta} - Ri_{ac_\beta} \\ C_{ac}\dot{u}_{c_\alpha} &= i_{ac_\alpha} - \delta_\alpha i_{dc} \\ C_{ac}\dot{u}_{c_\beta} &= i_{ac_\beta} - \delta_\beta i_{dc} \\ L_{dc}\dot{i}_{dc} &= 1.5(\delta_\alpha u_{c_\alpha} + \delta_\beta u_{c_\beta}) - u_0 \\ C_{dc}\dot{u}_0 &= i_{dc} - \frac{u_0}{R_{load}} \end{aligned} \quad (3.3)$$

Equation 3.3 is transformed to the synchronous reference frame rotating with the  $u_{c_d}$  capacitor voltage space vector. The resulting mathematical model is thus:

$$\begin{aligned} L_{ac}\dot{i}_{ac_d} &= u_d - u_{c_d} - Ri_{ac_d} + \omega L_{ac}i_{ac_q} \\ L_{ac}\dot{i}_{ac_q} &= u_q - u_{c_q} - Ri_{ac_q} - \omega L_{ac}i_{ac_d} \\ C_{ac}\dot{u}_{c_d} &= i_{ac_d} - \delta_d i_{dc} + \omega C_{ac}u_{c_q} \\ C_{ac}\dot{u}_{c_q} &= i_{ac_q} - \delta_q i_{dc} - \omega C_{ac}u_{c_d} \\ L_{dc}\dot{i}_{dc} &= 1.5(\delta_d u_{c_d} + \delta_q u_{c_q}) - u_0 \\ C_{dc}\dot{u}_0 &= i_{dc} - \frac{u_0}{R_{load}} \end{aligned} \quad (3.4)$$

where  $\omega_s$  represents the network voltage vector's angular velocity.

## 3.2 Model simplification

Notice, that the sixth-order ODE model (4) is bilinear in its states and inputs because of the product terms (e.g.:  $\delta_d i_{dc}$ ). As such, using design methods for linear systems is not straightforward. The high complexity given by the system's order is another problem to tackle. For designing classic MPC, linear, low-order equation systems are favorable. Hence simplification of the model would bring noteworthy benefits, making the MPC design more straightforward, when a linear system resulted. Since the AC and DC side's time constants differ significantly (as in the AC:  $\omega_{ac} = \frac{1}{\sqrt{L_{ac}C_{ac}}} \cong 5.7 \cdot 10^3$  [rad/s], and on the DC:  $\omega_{dc} = \frac{1}{\sqrt{L_{dc}C_{dc}}} \cong 2.8 \cdot 10^2$  [rad/s], see Table X. for reference). Thus, the differential equations can be separated into two sets, and the control of the AC and DC sides can be decoupled as described in [2]. The AC side model results as follows:



$$\begin{bmatrix} \dot{i}_{ac_d} \\ \dot{i}_{ac_q} \\ \dot{u}_{c_d} \\ \dot{u}_{c_q} \end{bmatrix} = \begin{bmatrix} -\frac{R}{L_{ac}} & \omega & -\frac{1}{L_{ac}} & 0 \\ -\omega & -\frac{R}{L_{ac}} & 0 & -\frac{1}{L_{ac}} \\ \frac{1}{C_{ac}} & 0 & 0 & \omega \\ 0 & \frac{1}{C_{ac}} & -\omega & 0 \end{bmatrix} \begin{bmatrix} i_{ac_d} \\ i_{ac_q} \\ u_{c_d} \\ u_{c_q} \end{bmatrix} + \begin{bmatrix} \frac{u_d}{L_{ac}} \\ \frac{u_q}{L_{ac}} \\ -\frac{\delta_d i_{dc}}{C_{ac}} \\ -\frac{\delta_q i_{dc}}{C_{ac}} \end{bmatrix} \quad (3.5)$$

Looking at the state matrix it can be further stated that there are only weak couplings between the  $d$  and  $q$  components. This allows to handle them separately, and later to design separate control for each. The equation system describing the DC side dynamics is the following:

$$\begin{bmatrix} \dot{i}_{dc} \\ \dot{u}_0 \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L_{dc}} \\ \frac{1}{C_{dc}} & -\frac{1}{R_{load}C_{dc}} \end{bmatrix} \begin{bmatrix} i_{dc} \\ u_0 \end{bmatrix} + \begin{bmatrix} \frac{1.5}{L_{dc}}(\delta_d u_{c_d} + \delta_q u_{c_q}) \\ 0 \end{bmatrix} \quad (3.6)$$

It can be noticed that, with the AC and DC model separation, bilinearity disappears, since the binding coefficients are present only in the input ( $\mathbf{u}$ ) of the DC state space model. Consequently, all equations are linear and with a considerably lower order, making control design much easier and allowing for the application of linear design methods. For the DC side dynamics, the linear time invariant differential equation system's matrices can be identified for predictive control design purposes:

$$\begin{aligned} \mathbf{x} &= \begin{bmatrix} i_{dc} \\ u_0 \end{bmatrix}, \\ \mathbf{u} &= (\delta_d u_{c_d} + \delta_q u_{c_q}), \\ \mathbf{y} &= u_0, \\ \mathbf{A} &= \begin{bmatrix} 0 & -\frac{1}{L_{dc}} \\ \frac{1}{C_{dc}} & -\frac{1}{R_{load}C_{dc}} \end{bmatrix}, \\ \mathbf{B} &= \begin{bmatrix} i_{dc} \\ u_0 \end{bmatrix}, \\ \mathbf{C} &= [0 \ 1]. \end{aligned} \quad (3.7)$$

where  $\mathbf{x}$ ,  $\mathbf{u}$  and  $\mathbf{y}$  are the state, input and output vectors of the DC-side system, and  $\mathbf{A}$ ,  $\mathbf{B}$  and  $\mathbf{C}$  are the state, input and output matrices. The circuit parameters used for the implementation of the control structure based on this model are presented in Table 3.1.

### 3.3 Control structure

Relying on the possibility of separation of the AC-side and DC-side controllers, the control structure from Fig. 3.2. is proposed.

The controllers operate in the synchronous frame of the AC filter capacitor voltages  $u_{c(1,2,3)}$ , and the rectifier input currents  $i_{r(1,2,3)}$  are in phase with the capacitor voltages. The current reference  $i_{\alpha\beta}^*$  supplied to the space vector modulation unit in the stationary frame, is obtained by coordinate transformation  $[D(-\Theta)]$  of the current reference 3.8 delivered by the current controllers in the synchronous frame.



### 3.4 DC-side explicit model predictive control

Model predictive control (MPC) is an efficient and systematic method for solving complex multi-variable constrained optimal control problems [50]. The MPC control law is based on the “receding horizon formulation”, where the model’s assumed behavior is calculated for a number of  $N$  steps, where  $N$  stands for the horizon’s length. Only the first step of the computed optimal input is applied in each iteration. The remaining steps of the optimal control input are discarded and a new optimal control problem is solved at the next sample time. Using this approach, the receding horizon policy provides the controller with the desired feedback characteristics, although with high order systems the computational effort is considerably demanding since all the steps should be taken in to account on the specified horizon in every iteration. With Explicit MPC (EMPC), the discrete time constrained optimal control problem is reformulated as multi-parametric linear or quadratic programming. Using this approach, the problem of optimization can be solved offline, making it much more feasible from the perspective of the optimal control task. The optimum control law is a piecewise affine function of the states, and the resulting solution is stored in a pre-calculated lookup table. The parameter space, or the state-space is partitioned into critical regions. The real-time implementation consists in searching for the active critical region, where the measured state variables lie, and in applying the corresponding piecewise affine control law to achieve the desired dynamics. In order to introduce the MPC implementation from this paper, let us consider a linear discrete time system 3.9 derived with the discretisation of system 3.6 with zero-order hold method, where control inputs are assumed piecewise constant over the simulation sample time  $T_s = \frac{1}{f_s}$  :

$$\begin{aligned} \mathbf{x}(t+1) &= \mathbf{A}_d \mathbf{x}(t) + \mathbf{B}_d \mathbf{u}(t) \\ \mathbf{y}(t) &= \mathbf{C}_d \mathbf{x}(t) \end{aligned} \quad (3.9)$$

where  $\mathbf{A}_d$ ,  $\mathbf{B}_d$ ,  $\mathbf{C}_d$  are the matrices of the discretised system derived from 3.7. With system 3.9 appears to be linear time invariant, MPC design can be followed. The following constraints have to be satisfied:

$$\begin{aligned} \mathbf{y}_{min} &\leq \mathbf{y}(t) \leq \mathbf{y}_{max}, \\ \mathbf{u}_{min} &\leq \mathbf{u}(t) \leq \mathbf{u}_{max} \end{aligned} \quad (3.10)$$

where  $t > 0$ ,  $\mathbf{x} \in \mathbb{R}^n$ ,  $\mathbf{u} \in \mathbb{R}^m$ ,  $\mathbf{y} \in \mathbb{R}^p$ . The MPC solves the following constrained optimization problem [42]:

$$\begin{aligned} \min_{U=\{u_t \dots u_{t+N_u-1}\}} J(\mathbf{u}, \mathbf{x}(t)) &= \sum_{k=0}^{N_y-1} (\mathbf{x}_{t+N_y|t}^T \mathbf{Q}_w \mathbf{x}_{t+N_y|t} + \mathbf{u}_{t+k}^T \mathbf{R}_w \mathbf{u}_{t+k}) \\ \text{subject to:} & \end{aligned} \quad (3.11)$$

$$\begin{aligned}
 \mathbf{x}_{min} &\leq \mathbf{x}_{t+k|t} \leq \mathbf{x}_{max}, \quad k = 1, \dots, N_c - 1 \\
 \mathbf{u}_{min} &\leq \mathbf{u}_{t+k|t} \leq \mathbf{u}_{max}, \quad k = 1, \dots, N_c - 1 \\
 \mathbf{x}_{t|t} &= \mathbf{x}(t), \quad \mathbf{u}_{t|t} = \mathbf{u}(t) \\
 \mathbf{x}_{t+k+1|t} &= \mathbf{A}_d \mathbf{x}_{t+k|t} + \mathbf{B}_d \mathbf{u}_{t+k|t} \\
 \mathbf{y}_{t+k|t} &= \mathbf{C}_d \mathbf{x}_{t+k|t} \\
 \mathbf{u}_{t+k|t} &= -\mathbf{K} \mathbf{x}_{t+k|t}, \quad k \geq 0
 \end{aligned} \tag{3.12}$$

This problem is solved at each time instant  $t$ , where  $\mathbf{x}_{t+k|t}$  denotes the state vector predicted at time  $t+k$ , obtained by applying the input sequence  $\mathbf{u}_{t|t} \dots \mathbf{u}_{t+t+1|t}$  to model 3.15, starting from the state  $\mathbf{x}_{t|t}$ . Further, it is assumed that  $\mathbf{Q}$  and  $\mathbf{R}$ , are symmetric positive semidefinite ( $\mathbf{Q}_w = \mathbf{Q}_w^t \geq 0, \mathbf{R}_w = \mathbf{R}_w^t > 0$ ) and  $\mathbf{K}$  is a feedback gain. Further,  $N_y, N_u, N_c$  are the output, input and constraint horizons, respectively. Using the model for predicting the future behavior of the system and with some appropriate substitution and variable manipulation, the problem 3.11,3.12 can be transformed to the standard multi parametric quadratic programming (mp-QP) form, as described in [42]:

$$V_z = \min \frac{1}{2} z^t H z \tag{3.13}$$

subject to:

$$Gz \leq W + S\mathbf{x}(t) \tag{3.14}$$

where the matrices  $H, G, W, S$  result directly from the coordinate transformations and variable manipulations. The solution of the mp-QP problem for each critical region has the form:

$$\mathbf{u}^* = f_i \mathbf{x} + g_i \tag{3.15}$$

and the critical region is described by:

$$\mathcal{C}_{reg_i} = \{\mathbf{x} \in \mathbb{R}^n | H_i \mathbf{x} \leq K_i\} \tag{3.16}$$

Thus, the explicit MPC controller is completely characterized by the set of parameters:

$$\{f_i, g_i, H_i, K_i\}_{i=1 \dots N} \tag{3.17}$$

In case of the discrete time system resulting from 3.7, for sampling time equal with the switching period  $T_s = 5 \cdot 10^{-5}$  s, the problem defined to be solved by MPC is the minimization of the quadratic cost function 3.9 for:

$$\mathbf{R}_w = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}, \mathbf{Q}_w = \begin{bmatrix} 10^{-6} & 0 \\ 0 & 10^{-6} \end{bmatrix}, N_y = N_u = N_c = 2 \tag{3.18}$$

Since  $N_y, N_u, N_c$  take the same value, they will be substituted by  $N$ . The constraints defined based on the rated power of the CSR  $P_n = 2500$  W, are:

$$\begin{aligned}
 0 &\leq i_{dc} \leq 50A \\
 0 &\leq u_0 \leq 500V
 \end{aligned} \tag{3.19}$$

The state space partition resulting from this problem has 13 critical regions, which can be observed in Fig. 3.3.

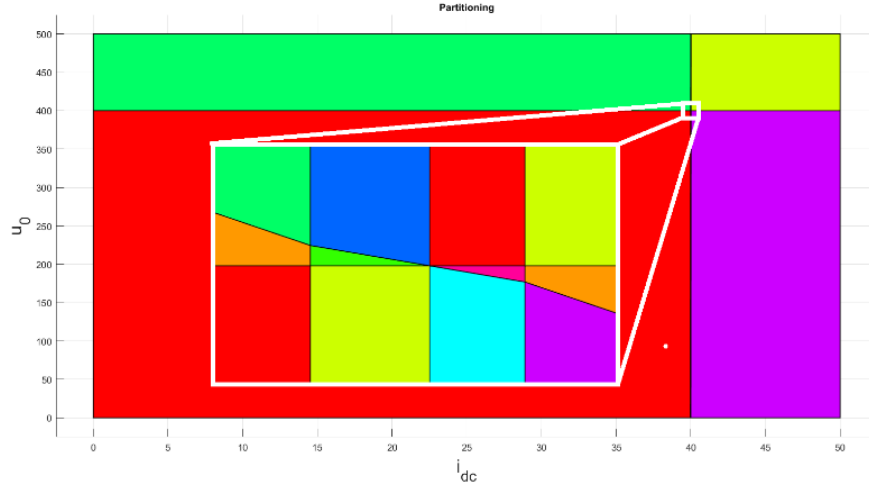


Figure 3.3: State space partitioning.

From the basis of the discredited model 3.9, the given constraints, and horizon 3.19 the cost function 3.11 is established via the MPT toolbox [30] and used in the generated controller for the EMPC design [29], [31]. The controller is created as a compliant S-function in the Matlab/Simulink environment and its place in the control structure can be observed in Fig. 3.4. as the EMPC controller. The output of the MPC controller is the control variable obtained via solving 3.12 and  $u_{MPC} = (\delta_d u_{cd} + \delta_q u_{cq})$ , from which the current reference can be calculated using 3.19. The quadrature component  $u_{cq}$  is zero in the synchronous frame of the filter capacitor voltage.

$$i_{rMPCd} = \frac{u_{MPC}}{u_{cd}} \cdot i_{dc} \quad (3.20)$$

### 3.5 Active AC-side damping

The CSR requires a voltage supply on the AC side. Taking into consideration the inductive character of the mains, the presence of a three-phase capacitor tank at the input of the CSR is a must. The most convenient is to use three-phase LC filtering with inductors on the lines and star connected capacitors resembling those in Fig. 3.1, although the resonance phenomena between these components can still cause difficult problems. The simplest way to dampen the resonance on the AC side LC filter is to add a damping resistor across the capacitor [41]. Because these resistors result in high losses, active damping methods have been proposed, which emulate damping resistors by control. This makes the CSR bridge produce an additional high frequency current, equivalent to the presence of virtual damping resistors connected in parallel with the AC capacitors. The resonance of the AC side LC filter produces harmonics in the capacitor voltage with frequency close to  $\omega_{ac} = \frac{1}{\sqrt{L_{ac}C_{ac}}}$ ,

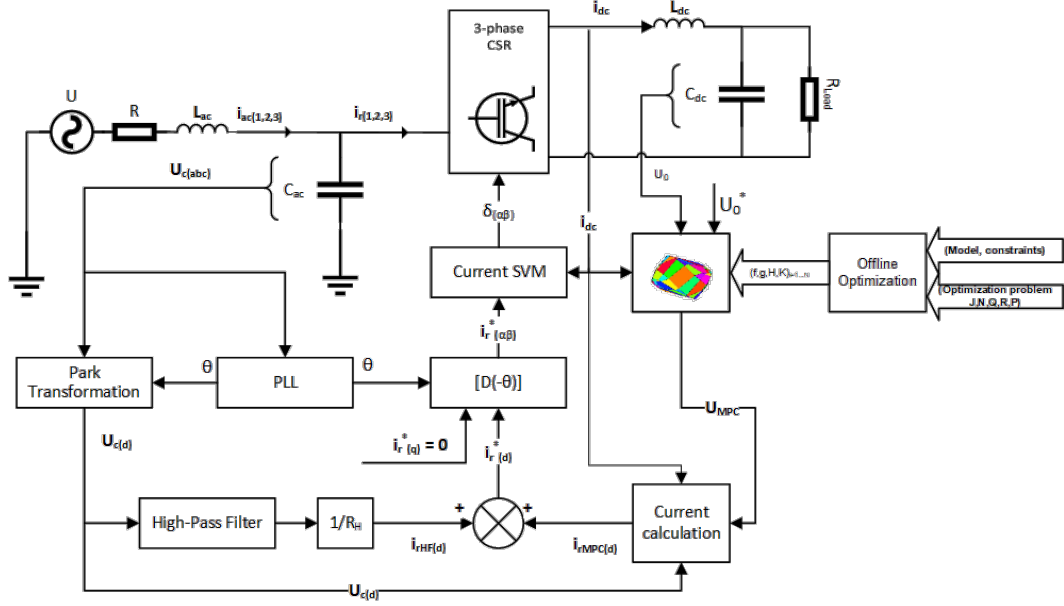


Figure 3.4: The control structure of the CSR, with MPC controller on the DC side.

which appears as  $\omega_{ac} - \omega$  component in  $u_{cd}$ , where  $\omega = 2\pi f$ . The fundamental component of the capacitor voltage represents a DC component in the synchronous reference frame. Therefore, a high-pass filter (HPF) is applied to filter out this DC component, with the transfer function:

$$HPF(s) = \frac{s}{s + 0.1 \cdot (\omega_{ac} - \omega)} \quad (3.21)$$

A virtual damping resistance  $R_H$  has been defined for calculation of the damping current component  $i_{HPF}$  from the HPF component of the capacitor voltage.

### 3.6 Space vector modulation strategy

The chosen modulation strategy is developed in the " $\alpha\beta$ " stationary reference frame. The structure requires simultaneous conduction of the upper and lower transistors of the bridge, since the current of the  $L_{dc}$  choke must not be interrupted. Additionally, the switching devices are considered as ideal.

According to this, one of the upper and one of the lower switches must be closed at all times. This allows nine states, six of which are active. There are three "zero" vectors, corresponding to the switching states, when both devices of one of the bridge legs are in conduction. These current vectors are shown in Table 3.2.

The neighboring space phasors can be formulated as:

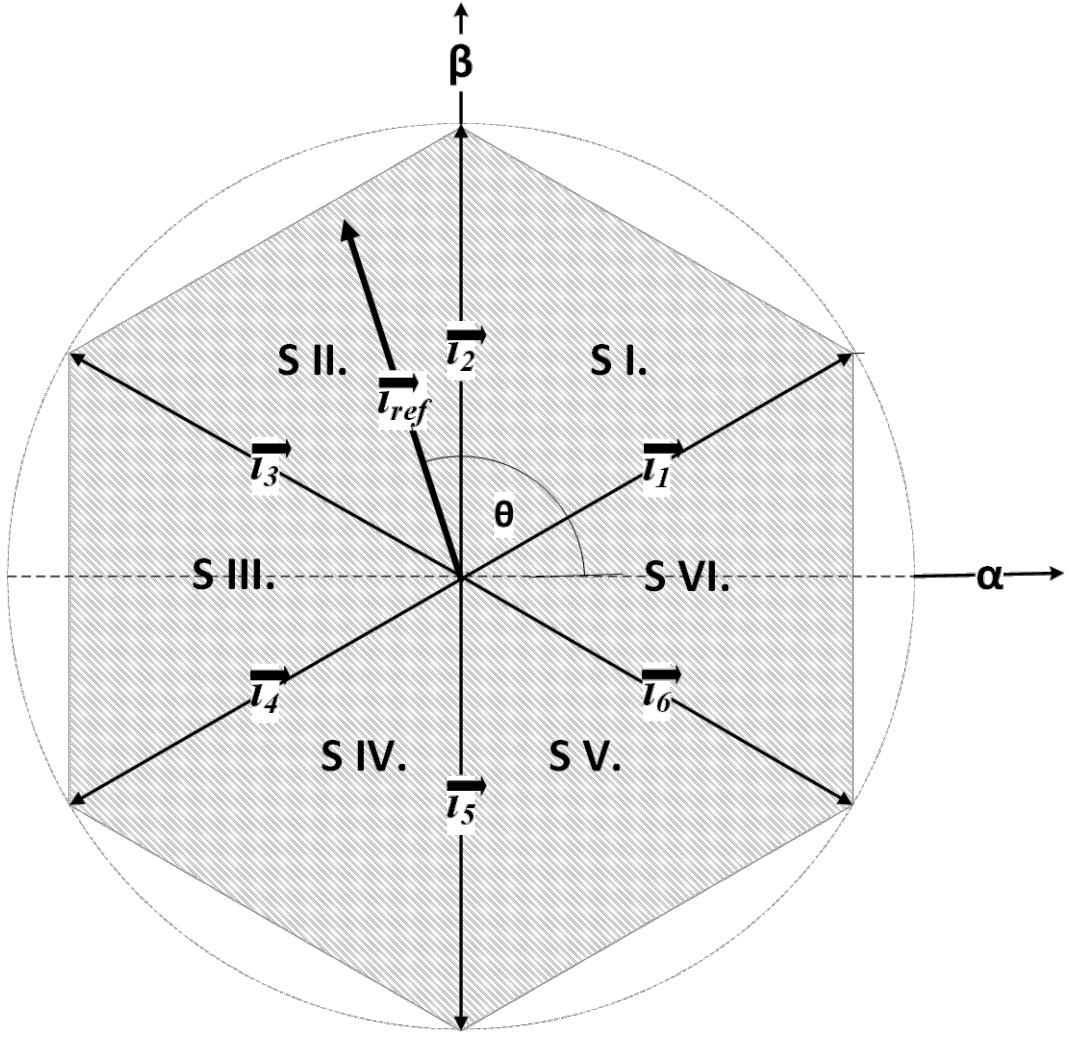


Figure 3.5: The fundamental input current vectors corresponding to the active switching states of the CSR.

$$\begin{aligned}\vec{i}_n &= \frac{2}{\sqrt{3}} i_{dc} e^{j(\frac{n\pi}{3} - \frac{\pi}{6})} \\ \vec{i}_{n+1} &= \frac{2}{\sqrt{3}} i_{dc} e^{j(\frac{n\pi}{3} + \frac{\pi}{6})} \\ n &= 1, 2, \dots, 6\end{aligned}\tag{3.22}$$

The reference current vector is sampled with fixed sampling period  $T_s$ . The sampled value of  $\vec{i}_{ref}$  is synthesized as the time average of two neighbouring space phasors adjacent to the reference current:

$$T_n \vec{i}_n + T_{n+1} \vec{i}_{n+1} = T_s \vec{i}_{ref}\tag{3.23}$$

$T_n$  and  $T_{n+1}$  represent the individual durations of the switching states corresponding to the neighboring vectors. For example, in case of a current reference vector situated in the first sector,  $T_1$ ,  $T_2$  and  $T_0$  can be calculated using 3.24.

Name	Switching State						Phase currents			Vector representation
	1	2	3	4	5	6	ia	ib	ic	
$\vec{i}_1$	1	0	0	0	0	1	$i_{dc}$	0	$-i_{dc}$	$2i_{dc}e^{j\pi/6}/\sqrt{3}$
$\vec{i}_2$	0	0	1	0	0	1	0	$i_{dc}$	$-i_{dc}$	$2i_{dc}e^{j\pi/2}/\sqrt{3}$
$\vec{i}_3$	0	1	1	0	0	0	$-i_{dc}$	$i_{dc}$	0	$2i_{dc}e^{j5\pi/6}/\sqrt{3}$
$\vec{i}_4$	0	1	0	0	1	0	$-i_{dc}$	0	$i_{dc}$	$2i_{dc}e^{j7\pi/6}/\sqrt{3}$
$\vec{i}_5$	0	0	0	1	1	0	0	$-i_{dc}$	$i_{dc}$	$2i_{dc}e^{j3\pi/2}/\sqrt{3}$
$\vec{i}_6$	1	0	0	1	0	0	$i_{dc}$	$-i_{dc}$	0	$2i_{dc}e^{j11\pi/6}/\sqrt{3}$
$\vec{i}_7$	1	1	0	0	0	0	0	0	0	0
$\vec{i}_8$	0	0	1	1	0	0	0	0	0	0
$\vec{i}_9$	0	0	0	0	1	1	0	0	0	0

Table 3.2: The fundamental input current vectors corresponding to the active switching states of the CSR.

$$\begin{aligned}
T_1 &= T_s \frac{i_{ref\alpha}}{i_{dc}} \\
T_2 &= T_s \frac{\sqrt{3}}{2i_{dc}} (i_{ref\beta} - \frac{i_{ref\alpha}}{\sqrt{3}}) \\
t_0 &= T_s - T_n - T_{n-1} = T_{7,8,9}
\end{aligned} \tag{3.24}$$

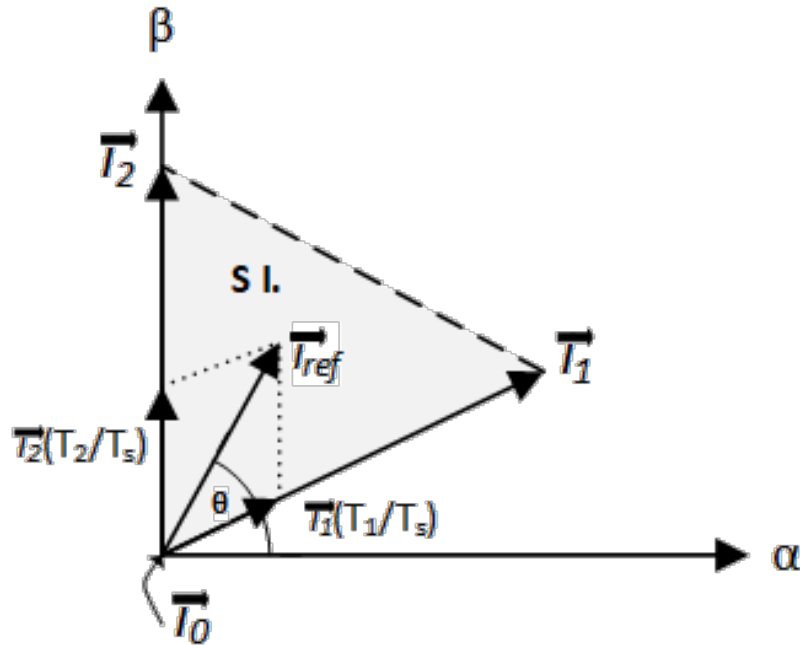


Figure 3.6: Synthesis of  $\vec{i}_{ref}$  by  $\vec{i}_1$ ,  $\vec{i}_2$ , and  $\vec{i}_0$

The complex plane is naturally divided by the fundamental space vectors into six areas, named "sectors".



	$\vec{i}_1$	$\vec{i}_2$	$\vec{i}_9$	$\vec{i}_9$	$\vec{i}_2$	$\vec{i}_1$
S1	1	1	1	1	1	1
S2	0	0	0	0	0	0
S3	0	1	0	0	1	0
S4	0	0	0	0	0	0
S5	0	0	1	1	0	0
S6	0	0	0	0	0	0
	$T_s$			$\vdots$	$T_s$	

Table 3.3: Representation of switching sequences for SECTOR I.

$$\begin{aligned} x\frac{\pi}{6} + \frac{(n-1)\pi}{3} \leq \theta_n \leq \frac{\pi}{6} + \frac{n\pi}{3} \\ n = 1, 2, \dots, 6 \end{aligned} \quad (3.25)$$

The non-zero space vectors are selected based on the phase angle  $\theta$  between  $\vec{i}_{ref}$  and the real axis. Table 3.3 presents an example of switching pattern in case of a current reference vector situated in Sector I.

The switching scheme represented in Table 3.2. is aimed at reducing the number of commutations in a switching cycle, resulting in the reduction of the switching losses [32]. Additionally, the constraint (3.26) resulting from the available magnitudes of the current vectors, is applied to the current reference.

$$0 \leq |i_{ref}| \leq \frac{\sqrt{6}i_{dc}}{\cos\theta + \sqrt{3}\sin\theta} \quad (3.26)$$

## 3.7 Performance evaluation

From the continuous AC 3.5, and DC 3.6 model equations described in Ch.X., the controller is formulated from discretised system 3.9, and it is described via the cost function and control problem of 3.11, and 3.12 in Ch.X+1. The evaluated model and control structure are shown on Fig.4. In the following section said EMPC's computational requirements are evaluated, and the Matlab/Simulink simulation results are compared to a classic state feedback controller's dynamic performance.

### 3.7.1 Computational effort

The binary search tree generated for the control problem presented in Fig. 3.7. The depth of the search tree is 5 and it has a total number of 29 nodes. It is utilized with the MPT toolbox [33], [26], and it can be used for the computationally optimal real-time implementation of the proposed algorithm on low-cost hardware.

The search for an active critical region starts from the first level and represents the evaluation in each adjacent node of an inequality of the form:  $x \leq K$ .

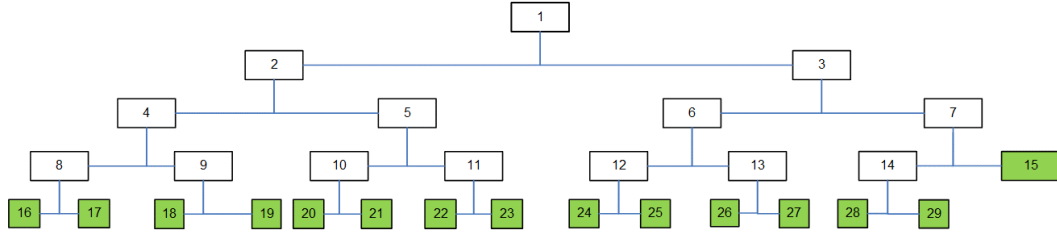


Figure 3.7: Binary search tree of the controller for a horizon of  $N = 4$ . The leaf nodes are depicted with filled squares. The depth of the tree is 5.

Thus, in this case a maximum number of 4 inequalities have to be evaluated to reach the active critical region. Implementing the presented algorithm is straightforward on a DSP processor, for instance from the dsPIC33 family by Microchip. Using the mac (multiply and accumulate) instruction the inequality is evaluated for each node using 4 instructions, thus in 80ns on a 50MIPS processor (Fig. 3.8). The active critical region can be reached in a maximum of 400ns. Compared to the typical sample rate of 10us in the case of a CSR, the real-time implementation on a DSP processor is possible.

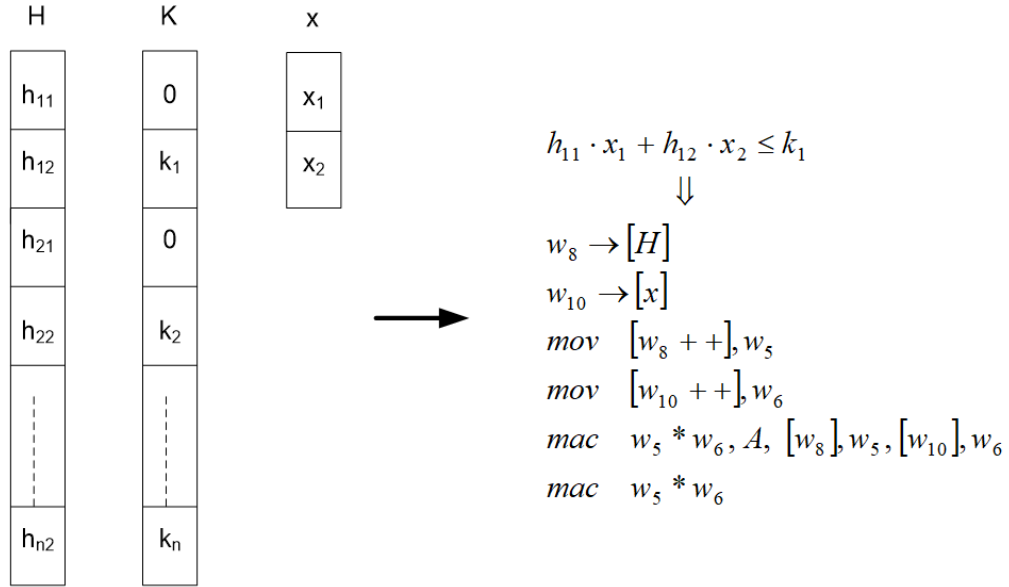


Figure 3.8: Data organization in the data memory of a single core DSP and the evaluation of a 2-dimensional inequality

### 3.7.2 Horizon performance

With the cost function (3.11) employed using (3.18), changing the length of the horizon ( $N$ ) affects the system's complexity illustrated by the partition in the state space shown in Fig. 3.3., and Fig. 3.9 presents the step response of the controlled system for different lengths of the horizon. It shows, that the

response is not affected by the increase of the horizon above  $N = 2$ , supporting the choice of this value for Matlab Simulink implementation.

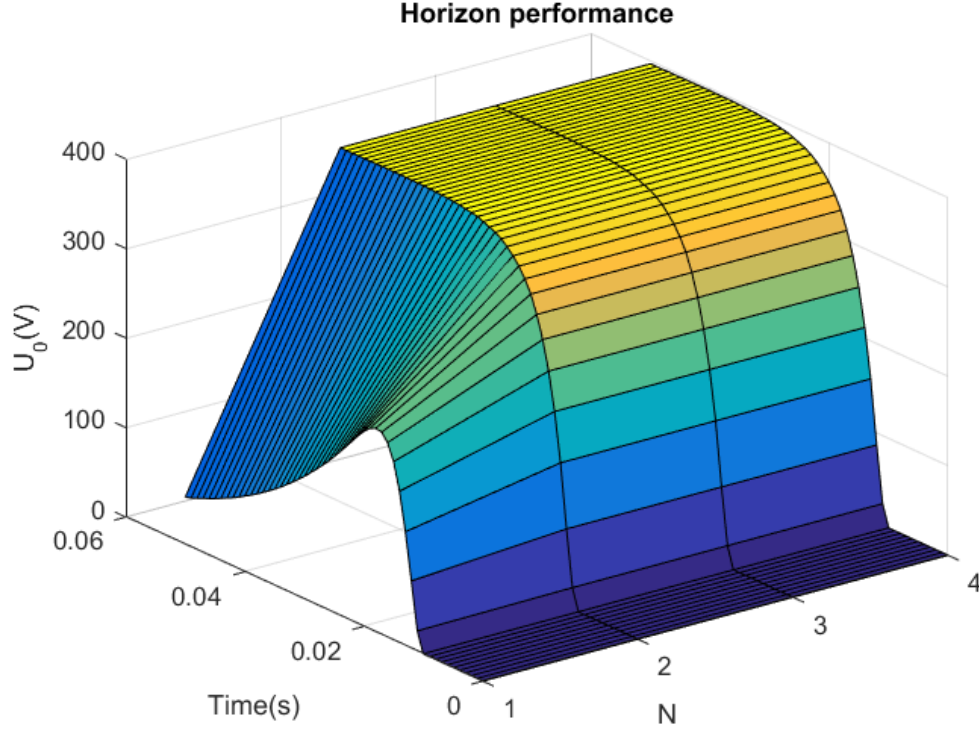


Figure 3.9: Step response of the system as a function of the horizon length ( $N$ ).

### 3.7.3 Simulation results

The simulation results are produced with Matlab/Simulink. The discrete model's (3.9) simulation frequency was 1 MHz, with the model parameters represented in Table 3.1., and with the control structure shown on Fig. 3.2. The EMPC performance is shown below:

More details about the Matlab simulation are presented in [37].

### 3.7.4 Comparison with a state feedback control

On the DC side, not only the output voltage  $u_0$  but also the inductor current  $i_{dc}$  needs to be controlled. Described in [28], a state feedback control with optimal parameters can be used as a reference based on the model properties listed in Table 3.1, with output voltage  $u_0$  and DC bus current  $i_{dc}$  chosen as the state variables. Since  $u_0$  is a DC quantity in steady state, an integrator signal is introduced to diminish the steady-state error. The structure of the controller is represented in Fig. 3.12.

The tuning constants applied and calculated according to [15] are:

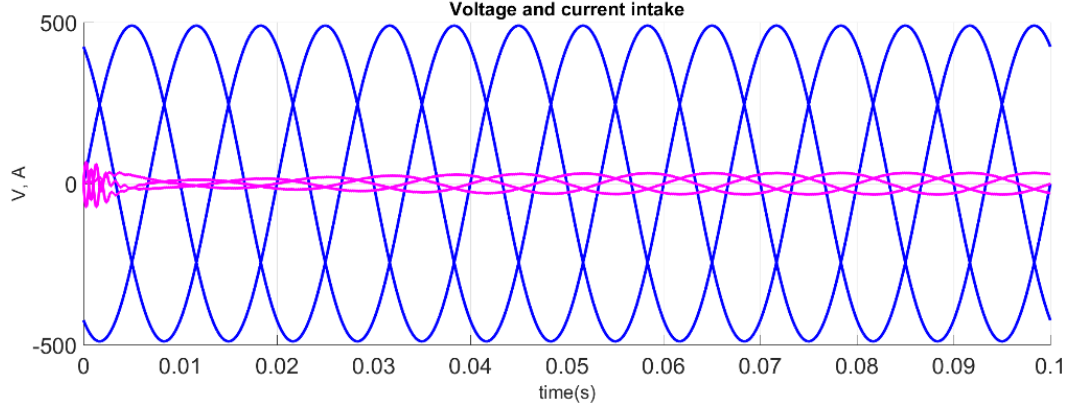


Figure 3.10: Three-phase voltage and current intake of the CSR with EMPC

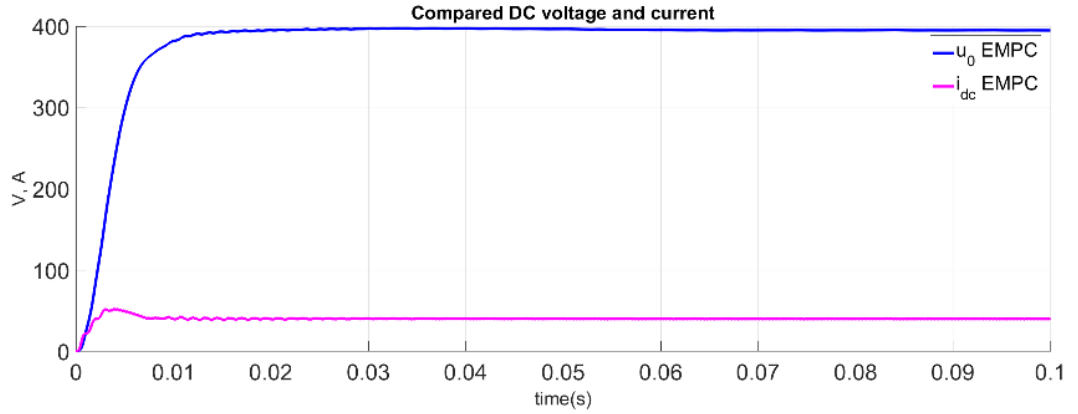


Figure 3.11: Resulting current and voltage trajectories of the CSR with (EMPC).

$$\begin{aligned}
 k1 &= \frac{\omega_n^3}{1.5U_n\omega_{dc}^2}, \quad k2 = \frac{1.9\omega_n L_{dc}}{1.5U_n}, \quad k3 = \frac{2.2\omega_n^2}{1.5U_n(\omega_{dc}^2-1)}, \\
 \text{where,} \\
 \omega_n &= 1.1, \quad \omega_{ac} = \frac{1}{\sqrt{L_{ac}C_{ac}}}, \quad \omega_{dc} = \frac{1}{\sqrt{L_{dc}C_{dc}}}
 \end{aligned} \tag{3.27}$$

The state feedback controllers block on the diagram is taking the controller's place, shown on Fig. 3.2. The independent outputs are the high pass filter's output  $i_{rHPF(d)}$  and the controller's output  $i_{rcontrol(d)}$ . The sum of the independent current values is converted to Clarke frame to be able to govern the switching states of the IGBT's. This can be done because  $i_{rHPF(d)}$  has only high frequency components and  $i_{rcontrol(d)}$  has low frequency components due to the differences in LC time constants, as discussed in the second section.



## 3.8 Conclusion

The constrained, model-based optimal control of a current source rectifier has been presented in this paper. The dynamic model of a three-phase current source rectifier has been developed in Park frame. The proposed model has been examined from the design and implementation points of view with the purpose of explicit model-based predictive control. It proved to be the case that the regular set of differential equations of the CSR appears to be too complex, and contains non-linearity for such a design approach. To address this issue the usage of separated AC and DC equation sets was suggested to avoid linearization and complexity reduction. This solution eliminates bilinearity and enables the application of linear control design techniques. Current-based SVPWM of the three-phase converter has been used with an emphasis on the reduction of switching losses. Throughout the article the explicit model predictive control method is described and the method's effectiveness compared to conventional state feedback control is shown. The implementation and simulation experiments have been performed in Matlab/Simulink environment. Moreover, the proper implementation of the system in a modern DSP chip will result in real-time operation.

# Chapter 4

## Geometrical indicator for voltage asymmetry in three phase networks

### 4.1 Network structure

The examined network is supposed to be the low voltage local transformer area with regular households, depicted in Figure 4.1. The network structure of Figure 4.1 has been implemented in Simulink™ environment for simulation based experiments. Most of the households represent a single phase load with resistive inductive and capacitive properties while some of them are symmetric three phase ones. There might also be some households with domestic powerplant, they are not only loads but also represents distributed generators. Furthermore, it is assumed that the households located on a domestic size grid tie inverter are also provided with battery storage capacities. Commercially available inverters are capable of optimizing the working point and charging current of the system, while the ability of power quality improvement is far not typical - it is in experimental phase in some cases, e.g. [17].

### 4.2 Voltage deviations

The paper focuses on three types of voltage deviations. The first fall in to the category of unbalance, namely any kind of phase deviations, and unbalanced amplitude deviations, and balanced amplitude deviations, like under-voltage. There are many different technological causes with more or less practical importance. The following conditions are examined and tested in the sequel:

**Single phase under-voltage unbalance** If there is a single phase uncompensated overload in the system, the voltage in the overloaded phase will be lower than the other two.

**Two phase under-voltage unbalance** Two of the three phases are overloaded without compensation, the two overloaded phases will have higher

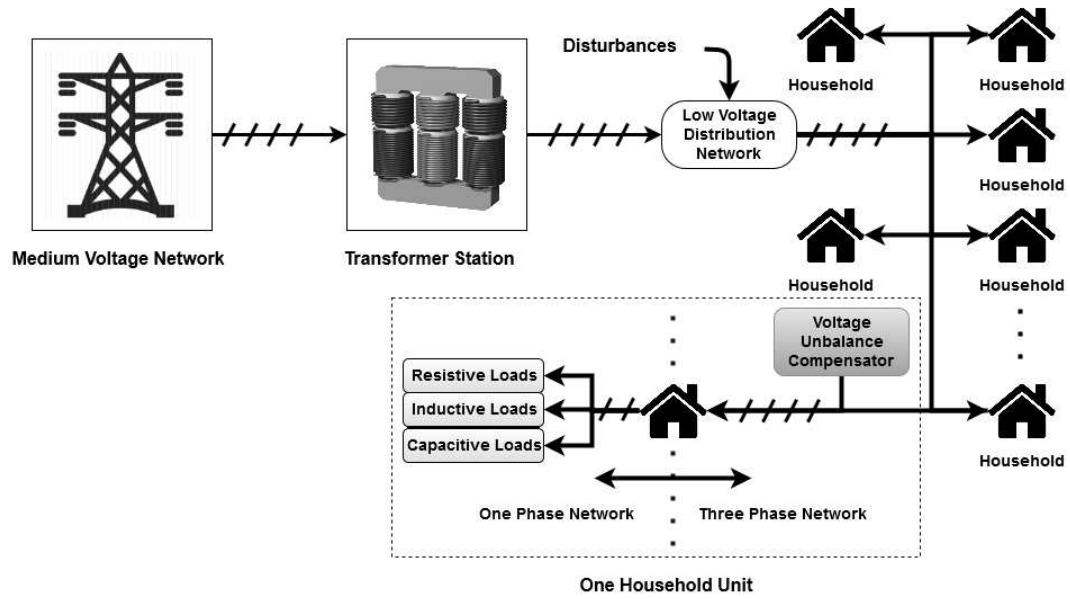


Figure 4.1: The simplified structure of a three phase four wire low voltage network. The transformer station acts as transition from the medium voltage power grid to the low voltage network. The several regular households are representing the main loads of the network. The transformer and the loads are connected with power line sections infected by inductive and resistive disturbances and capacitive couplings. Domestic powerplants can connect to any connection point within the low voltage network, via an appropriate inverter - either to the three phase sections using a three phase inverter or to a single phase using a single phase inverter.

voltage drop than the third phase. Balanced three phase under-voltage]  
 The loads of all three phases are overloaded in an unbalanced manner.

**Unbalanced single phase angle** If the three phase voltage amplitudes are balanced but the relative angles between them (ideally it should be equal to  $\pm 120$  degree). It is assumed, that phase A would be the reference. If one of the other two phase angles is deflected, unequal displacement.

**Unbalanced two phase angles displacement** Similar to the single phase angle unbalance, if the other two phase angles are both deflected, then unequal angle displacement in two phase angles occurs.

An indicator of the voltage unbalance is supposed to measure the extent of unbalance but it is not expected to classify between the above types.

#### 4.2.1 Standard indicators of unbalance

Although the term voltage unbalance is unambiguous, the root phenomenon may be various as well as the standard norms used to measure unbalance. All of these different indicators measure voltage unbalance but each of them does it in a different way (4.1).



$$\begin{aligned}
 V_{936} &= \frac{\max\{V_{an}, V_{bn}, V_{cn}\} - \min\{V_{an}, V_{bn}, V_{cn}\}}{\text{mean}\{V_{an}, V_{bn}, V_{cn}\}} \times 100 \\
 V_{112} &= \frac{\max \text{ deviation from mean of } \{V_{an}, V_{bn}, V_{cn}\}}{\text{mean}\{V_{an}, V_{bn}, V_{cn}\}} \times 100 \\
 MDV &= \frac{\max \text{ deviation from mean of } \{V_{ab}, V_{bc}, V_{ca}\}}{\text{mean}\{V_{ab}, V_{bc}, V_{ca}\}} \times 100 \\
 TDV &= \frac{\text{negative seq. voltage}}{\text{positive seq. voltage}} \times 100
 \end{aligned} \tag{4.1}$$

The voltages  $\{V_{an}, V_{bn}, V_{cn}\}$  denotes the phase-to-neutral voltages, while  $\{V_{ab}, V_{bc}, V_{ca}\}$  are the line-to-line voltages,  $V_1$  is equal to the voltage amplitude of the fundamental frequency and  $V_n$  is the voltage amplitude of the  $n$ -th upper harmonic voltage [6] (current unbalance formulations are simply obtained by replacing voltage with current components). Although the harmonic distortion does not relate closely to the asymmetry phenomenon, it might cause a variety of problems like dropping efficiency of electric motors.

The above four norms measure different values for a single case. The first two standard indicators,  $V_{936}$  and  $V_{112}$ , ignore the  $\pm 120$  degree phase difference unbalance and only take the amplitudes into account. The last two definitions,  $MDV$  and  $TDV$ , are sensitive to the phase difference unbalance. Nevertheless, these definitions ignore zero sequence components and harmonic distortion that are always present in three-phase four-wire systems [6].

#### 4.2.2 Proposed geometrical indicator

It can be stated that every difference between the ideal and the measured voltage in both amplitude phase and sub-harmonics causing a form of **voltage deviation**. The problem can also be investigated from a geometrical point of view as it is depicted in Figure 4.2. The three-phase voltage system's phasor diagram contains three phase-to-neutral voltage vectors which can be regarded as the points of a triangle (similarly, the three line-to-line vectors can play the role of the edges of the triangle). The two triangles (i.e. the ideal and the actual ones) always intersect except from very extreme and physically meaningless cases. The area where the two triangles do not cover each other (i.e. the difference of their union and intersection) can be used as a norm of voltage **quality**. In fact it is computationally more demanding compared to the previous methods, but takes every deviation into consideration [34], [35]. The calculation of error is given by (4.2).

$$G = \text{Area of } (\triangle_{Ideal} \cup \triangle_{Real} - \triangle_{Ideal} \cap \triangle_{Real}), \tag{4.2}$$

$\triangle_{Ideal}$  indicates the triangle spanned by the ideal voltage vectors and  $\triangle_{Real}$  the triangle of real voltage vectors. Difference of the ideal and the real triangle's union and intersection defines the norm  $G$ . Basically, the algorithm calculates the symmetrical difference of the triangles, stretched from three phase ideal and real voltage vectors.

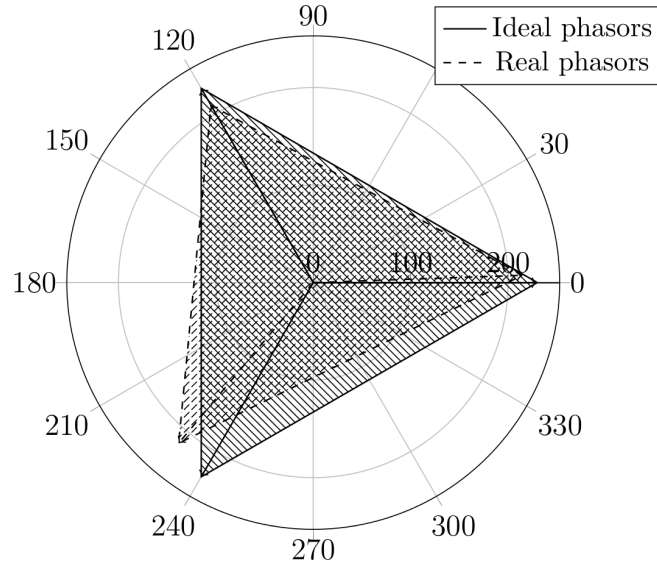


Figure 4.2: The triangles spanned by the ideal and the actual voltage phasors. The extent of voltage deviation on the network can be measured by the sum of areas where the two triangles are not overlapping.

### The geometrical method's additional content compared to the regulated method

When using a new method for calculation and cost function it is reasonable to test its usability against the prevalent or regulated method. In this case the geometrical norm's utility (4.2) against the TDV (4.1) value. The geometrical norm was validated experimentally, by investigating the correlation between the regulated (4.1) and geometrical (4.2) norms subjected to random, uniformly distributed unbalance on the voltage vector amplitude and phase values with 20 V amplitude and  $1/300 \cdot \pi$  rad phase variance (Fig. 4.3, and Fig. 4.4).

Although there is correlation between the two norm values in the general case, but for some situations the regular method indicates low, while geometrical norm still indicates high value.

On Figure 4.5a dominant phase deviation can be observed, while Figure 4.5b shows amplitude deviation but with opposite direction. When there is such deviation on the grid both indicators present almost identical results. On 4.5c there is still observable unbalance (two phase deviate stronger than the third in terms of amplitude), but the correlation is significantly lower. In the last case in the lowest correlation area, amplitude deviation is present, but the deviation direction is identical on all phases (balanced over-voltage or under-voltage)(Figure 4.5d). The regular method indicates very low values. In this case other methods are utilised in parallel in terms of network diagnostics to detect the under-voltage phenomena. [4].

To clarify this, the regular norm's calculation method needs to be investigated. The symmetrical component mutual impedance matrix on a three phase connection point is given by (4.3),

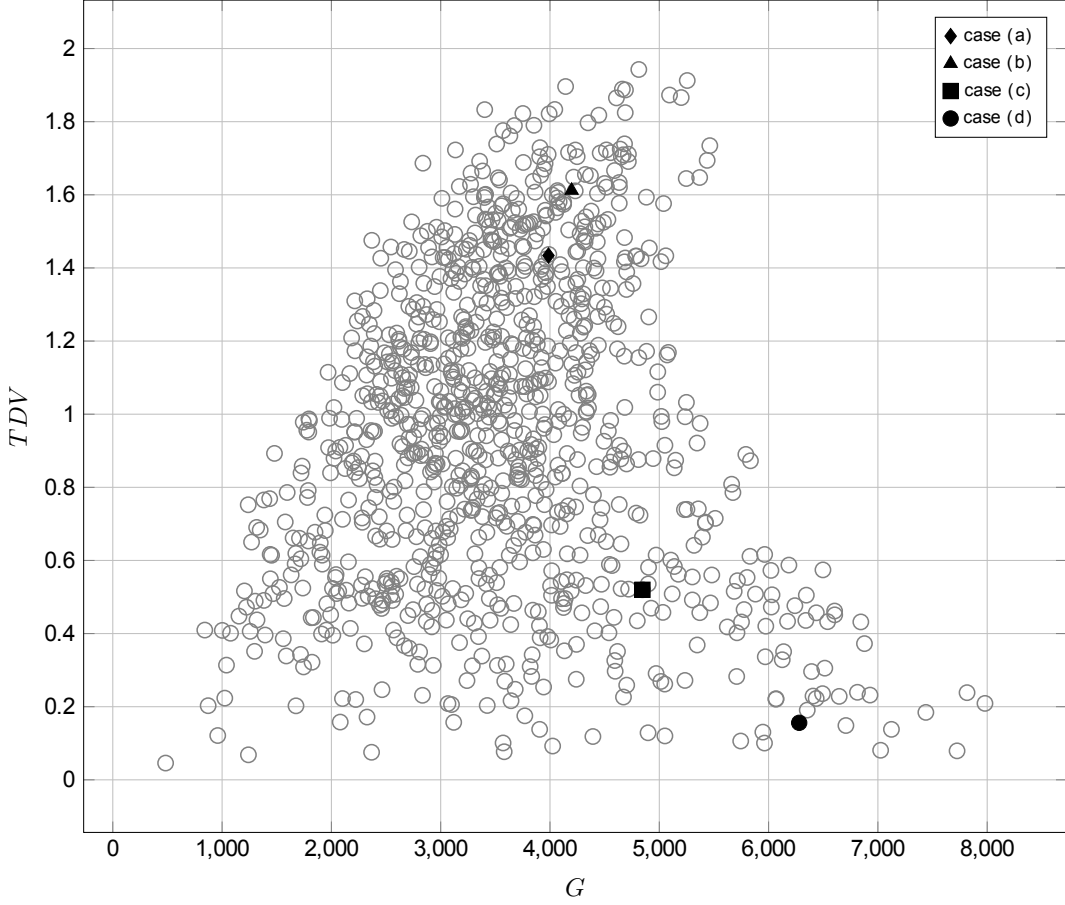


Figure 4.3: Correlation between the geometrical voltage unbalance indicator  $G$  and the regulated voltage unbalance indicator  $TDV$  using 1000 samples. In every iteration each three phase voltage vector's amplitude and phase values changed randomly, according to uniform distributions with  $\pm 20$  V and  $\pm \frac{1}{3}\pi \cdot 10^{-2}$  rad variance. It can be seen, that the geometric norm contains more information than the classical one. The four asymmetry cases of Figure 4.5 are denoted by black symbols on the picture. It is apparent, that in case (c), and (d) the  $G$  norm holds additional information than the  $TDV$ .

$$\begin{aligned}
 Z_s &= \frac{1}{3} \begin{bmatrix} 1 & 1 & 1 \\ 1 & \alpha & \alpha^2 \\ 1 & \alpha^2 & \alpha \end{bmatrix} \cdot \begin{bmatrix} Z_{aa} & Z_{ab} & Z_{ac} \\ Z_{ba} & Z_{bb} & Z_{bc} \\ Z_{ca} & Z_{cb} & Z_{cc} \end{bmatrix} \cdot \begin{bmatrix} 1 & 1 & 1 \\ 1 & \alpha^2 & \alpha \\ 1 & \alpha & \alpha^2 \end{bmatrix} = \\
 &= \begin{bmatrix} Z_{00} & Z_{01} & Z_{02} \\ Z_{10} & Z_{11} & Z_{12} \\ Z_{20} & Z_{21} & Z_{22} \end{bmatrix}, \tag{4.3}
 \end{aligned}$$

where  $Z_s$  is the symmetrical component mutual impedance matrix, and  $\alpha = e^{j\frac{2}{3}\pi}$ . If there are both negative and zero sequence symmetrical components present on the network, the dominant part of the voltage drop's negative and zero sequence can be calculated as follows (4.4).

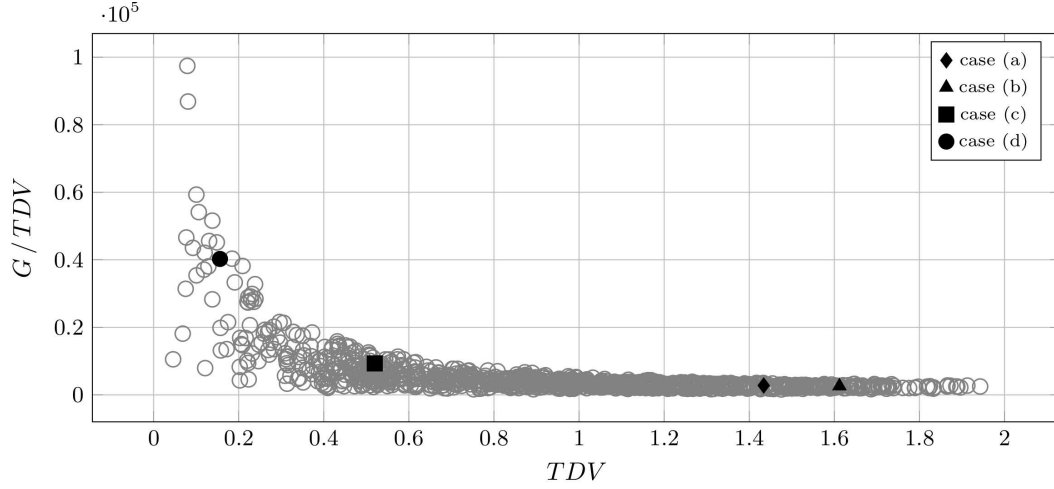


Figure 4.4: **Itt a .eps converzio nem lett valami jo, javitani kell!** Correlation between the regulated unbalance indicator and the fraction of geometrical and regulated indicator. It can be seen that there is a functional connection between the two values.

$$\begin{aligned}\Delta U_2 &\approx Z_{21}I_1 + Z_{22}I_2 \\ \Delta U_0 &\approx Z_{01}I_1 + Z_{00}I_0,\end{aligned}\tag{4.4}$$

$\Delta U_0, \Delta U_1, \Delta U_2$  are the voltage drop's zero positive and negative sequence components,  $I_0, I_1, I_2$  are the current's drop's zero positive and negative sequence components, and  $Z_{00}, Z_{01}, Z_{21}, Z_{22}$  are mutual impedances, respectively. (If there is only positive and negative sequence present, then the right hand side's second term is zero.) As such, the indication of negative and zero sequence present the network calculates (4.5):

$$\begin{aligned}m_{21} &= \left| \frac{Z_{21}}{Z_{11}} \right| \times 100 \\ m_{01} &= \left| \frac{Z_{01}}{Z_{11}} \right| \times 100,\end{aligned}\tag{4.5}$$

where  $m_{21}$  is the negative sequence factor which is identical to the TDV (4.1), and  $m_{01}$  is the zero sequence factor.

At the previously described balanced over- or under-voltage case the positive sequence value is dominant, so the regular indicator will take considerably lower value. In other words, aside from indicating voltage unbalance, the geometrical method incorporates the balanced deviations as well. In a control design perspective, a general case, where notably highly unbalance values may appear, using  $TDV$  as cost function could introduce hidden errors in control due error cancellation. Additionally the geometrical solution checks electrical asymmetry, i.e. the norm of a  $\pm 120$  degree rotated version of the ideal three-phase phasor is zero in the geometrical sense. Moreover, the geometrical norm is more sensitive for small scale unbalance, as opposed to the TDV. To summarise, the geometrical indicator a more suitable solution for a more general case indicator, and a good candidate for cost function in optimal control design.

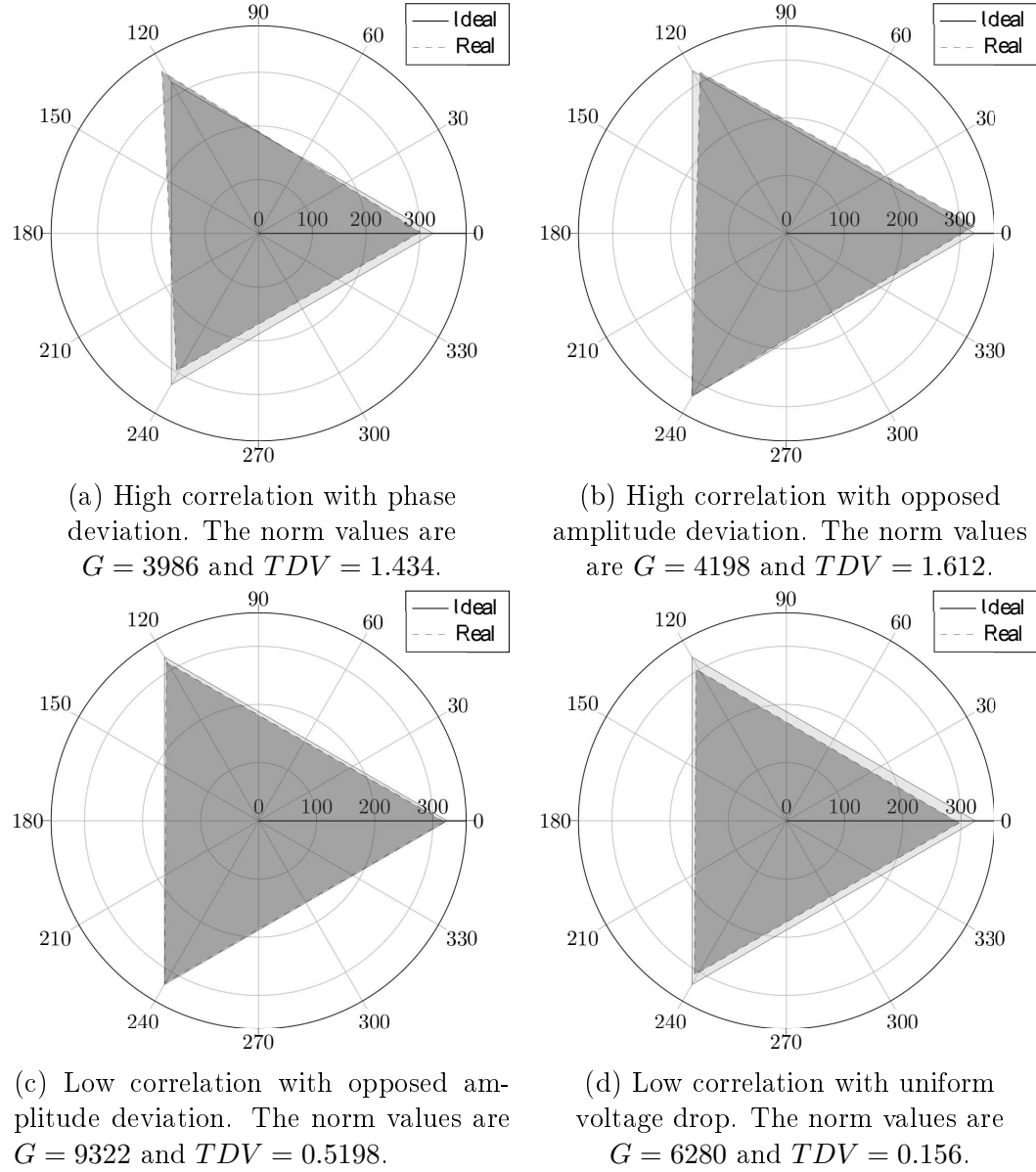


Figure 4.5: Four distinct cases of voltage triangles examining correlation between the regular  $TDV$  and geometrical  $G$  method.

# Chapter 5

## Voltage unbalance compensation with optimization based control algorithm and asymmetrical inverter structure

### 5.1 Unbalance compensation

Based on the proposed measures of voltage unbalance it is possible to formulate some power quality related aims, or demands for the domestic size generator units as follows.

#### 5.1.1 Problem statement

The voltage and current unbalance presents in the three phase low voltage transformer area causes additional power loss inside the medium voltage/low voltage transformer and in the transportation line wires too. It also has undesired effects in certain three phase loads, mainly rotating machines where it causes torque reduction and pulsating torque effect. Large scale unbalance can activate automatic protection functions of electricity dispatch system causes power outage. This is unpleasant for the customers and adds maintenance cost to the service provider. These negative effects lower the electric power quality and rises the cost of electrical energy and rises the carbon footprint of our everyday life. **The paper's aim to propose a model and control for** a three phase instrumentation, which can compensate these undesirable effects, lower or eliminate the voltage and current unbalance to lower the power losses and the CO<sub>2</sub> emission and increase power quality not only at the connection point but in the whole low voltage transformer area. *It is important to note, that this power quality improvement can be achieved without any significant added cost.* The **future** aim is to integrate this function into an existing three phase photovoltaic inverter device connected to the low voltage grid, and a complex energetic system is able to inject the renewable energy to the transportation network, can store the electrical energy from stochastic renewable sources in

electrical vehicle batteries or feed the grid from the charged batteries in energy deficit and high demand simultaneous situations. Our new added value is the integrated control algorithm which can highly lower or eliminate the observed unbalance of the network.

### 5.1.2 Control problem

The above problem statement partially specifies the solution space together with the solution method. The system of interest is the power grid with all the stochastic and nonlinear phenomena present in it. The input to the system are current signals (one current in the single phase case and three in the three phase setup), which are naturally constrained by the available energy of the household - stored in a battery pack or momentarily generated by the wind or solar generator unit. The response of the system can be either the current or the voltage measured at the connection point of the inverter unit, however, the general legal regulations only allow voltage measurement for consumers. The difficulty of the control problem comes from the fact, that there are no mathematically tractable models of the network can be generated because of its unpredictable and nonlinear features. This means, that in these terms only black-box methods can be applied for this system.

For the control aim it is a natural choice to minimize the actual voltage unbalance of the low voltage local transformer area measured (or calculated) at the connection point of the inverter. Several optimization based methods are available for such kind of optimal control problems, e.g. [17] where the only bottleneck is the computational efficiency since the implemented controller has to run on the commercial inverter's hardware (digital signal processor unit).

### 5.1.3 Asymmetrical inverter structure

For the sake of completeness an asymmetrical inverter was developed in simulation environment, which is capable of carrying out the specified control task. The renewable energy injection is realized increasingly, and applied directly to the three phase low voltage grid with a domestic size photovoltaic power plant as source of power. This can reduce the voltage and current unbalance caused by the stochastic power production of wind and solar sources. More and more manufacturers produce three phase grid synchronized inverters from 5 kW size. These equipments implement accurate symmetrical current feed with a standard three phase full bridge structure, consists of six Isolated Gate Bipolar Transistors (IGBT). This is a cost effective standard structure suitable for symmetric harmonic current injection. It has limited capacities to inject not totally symmetric 3 phase current time functions, but Kirchhoff's current law permits only constant zero-sum current time functions injected with this structure. There are examples with this type of asymmetric current injections in the literature [?].

This type of current injections has limited compensation capacity and this is not enough in most asymmetric production and load cases. In our case we

need more general, not specific asymmetric current waveforms, because the proposed control aim assumes the ability of injecting non zero-sum currents. This needs special inverter design structure. We need zero line connection for the differential current. One of the possible solution is to use 3 different full bridge single phase current inverters to supply each phase of low voltage transportation lines [40]. But in this case we galvanic isolations are needed for these single phase inverters on the output, or on the DC input size.

This isolation can be reached with using isolation transformers in the supply side. But we prefer to use it a complex energetic system with specific inside true DC bus system feeded from Photovoltaic panel or batteries. We have to isolate at least 2 full bridges with two way DC-DC converters. This can complicate the physical realization but easy to simulate with two controlled power source. Other possible easy to realize solution to isolate the full bridge outputs connected to three phase lines with isolating transformers. It is recommended due to electric shock protection reasons. Our distant aim to compensate other operational type line failures, such zero current appearing. This isolation method doesn't allow to produce DC current components, thats why we are looking for other design. Possible elegant solution to supplement the standard three phase inverter design with a fourth half bridge for Zero line, building a specific four leg inverter design [38].

Only drawback of this solution is the complex difficult control method of the half bridges, to keep the current sum in zero values in each moment, and to provide the correct current paths inside the inverter. This structure has the lowest production cost, but in the phase of proofing the asymmetry compensation we chose the DC-DC isolated full bridge design for simulation purpose because of the simple control during simulation.

As a further generalization step, the injection of no harmonic current shapes will be necessary in order to decrease the extant Total Harmonic Distortion (THD) of the network. These expectations yield an inverter with new structure suitable for arbitrary current injections without limitations. The design lends similar elements like in [17] by means of battery charge, renewable power point tracking, intermediate voltage, and IGBT bridge control, but in this case the problem requires a three phase solution for the voltage unbalance reduction. The applied structure based on a full bridge IGBT structure used in single phase current injection. Three different IGBT full bridge were connected at the output point, thus our structure has three phase and neutral connection too, to carry out any current form. The disadvantage of this structure is that it needs 12 IGBTs in the output stage as opposed to the 6 IGBTs needed for a classical full bridge structure and needs three galvanically isolated direct current (DC) voltage source for feeding. The other standard elements, that the inverter design consists:

- Standard maximum power point tracking (MPPT) input stage, to inject the maximum available power from the renewable source to the intermediate voltage capacitor with a simple controlled boost converter
- A half bridge current controller to charge or deploy the battery pack



CHAPTER 5. VOLTAGE UNBALANCE COMPENSATION WITH  
OPTIMIZATION BASED CONTROL ALGORITHM AND  
5.1. UNBALANCE COMPENSATION METRIC INVERTER STRUCTURE

connected to the complex energetic system for energy storage and energy unbalance compensation

- Intermediate voltage controller
- Universal three phase output stage with 3 single phase full bridge IGBT current injector and 2 high current DC-DC converter

This is suitable to inject any necessary current shape to the low voltage three phase grid connection even DC currents too. Later a power loss and production cost analysis will be necessary if the built structure will be suitable for asymmetric compensation of low voltage transformer area.

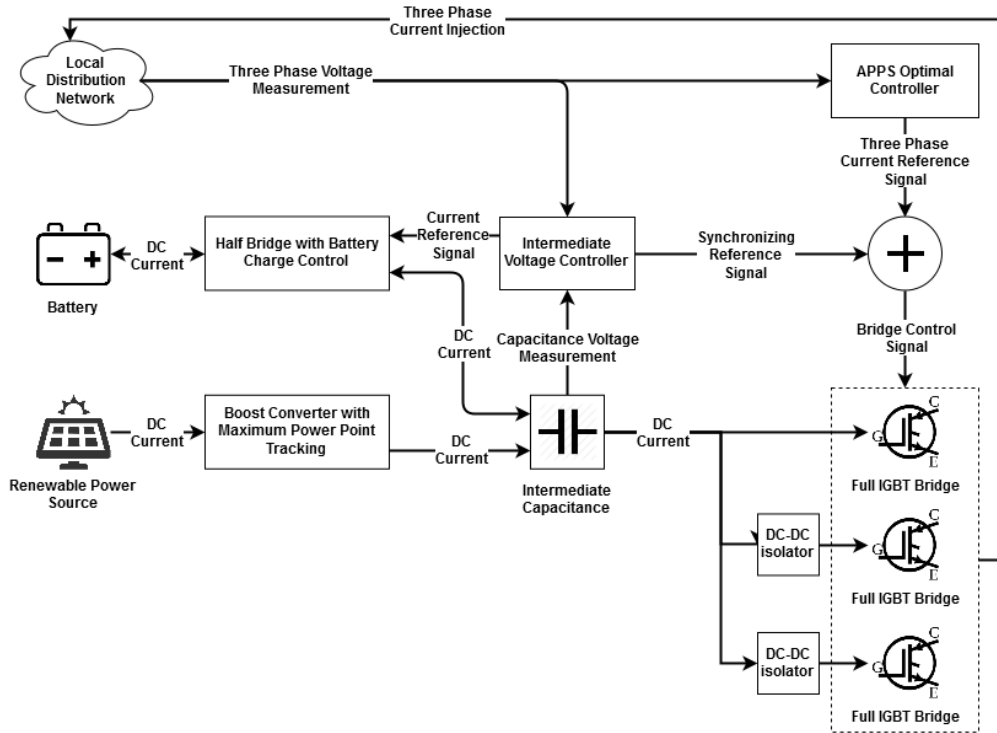


Figure 5.1: The asymmetrical inverter design, which applies three single phase full bridge IGBT current injector to create the injected asymmetrical current shapes for voltage unbalance compensation.

Of course there is a possibility that there is no renewable power available for a longer period of time and the battery completely loses its charge. In this case the system should work merely with the power of the connection point but with zero energy balance. This states to operate two controller with semi-opposite control goals. The optimization based controller requires current injection while the intermediate voltage controller (Figure 5.1) keeps the inverters energy balance. Although for this operation some of the control's performance should be sacrificed, unbalance compensation could be achieved even without external renewable power, and energy storage at a minimum power requirement.

### Measurements from a real unbalanced network

The measurements took place at the Faculty's building, where a common 400 V connection point was investigated as the behaviour of the network. The three phase 230 V line-to-ground voltages has been transformed to 6 V to be effectively measurable time domain with high performance NI-USB DAQ on 10 ksample/s. Because of the limited computational capacity only a 10 second measurement was made in every hour. The measurements then has been merged and smoothed to eliminate the inter-measurement transients. Afterwards, the measurement data has been used as the **input** of a micro-grid segment of the Matlab/Simulink model, to test the controller and inverters structure's performance in quasi-realistic circumstances. The controllers performance on the simulated microgrid's network loss reduction can be observed on Figure 5.6 and Figure 5.7. The measurement output is connected to a modeled three phase load and network system, consisting of symmetrical loads and network segments between them. Further artificial load unbalance is not necessary since the network's unbalance is already present. This structure enables to show that any point the inverter is connected could serve as quality restoration such unbalance compensation at this case. Our future plan is to set up multiple devices on different connection points.

#### 5.1.4 Optimization based control algorithm

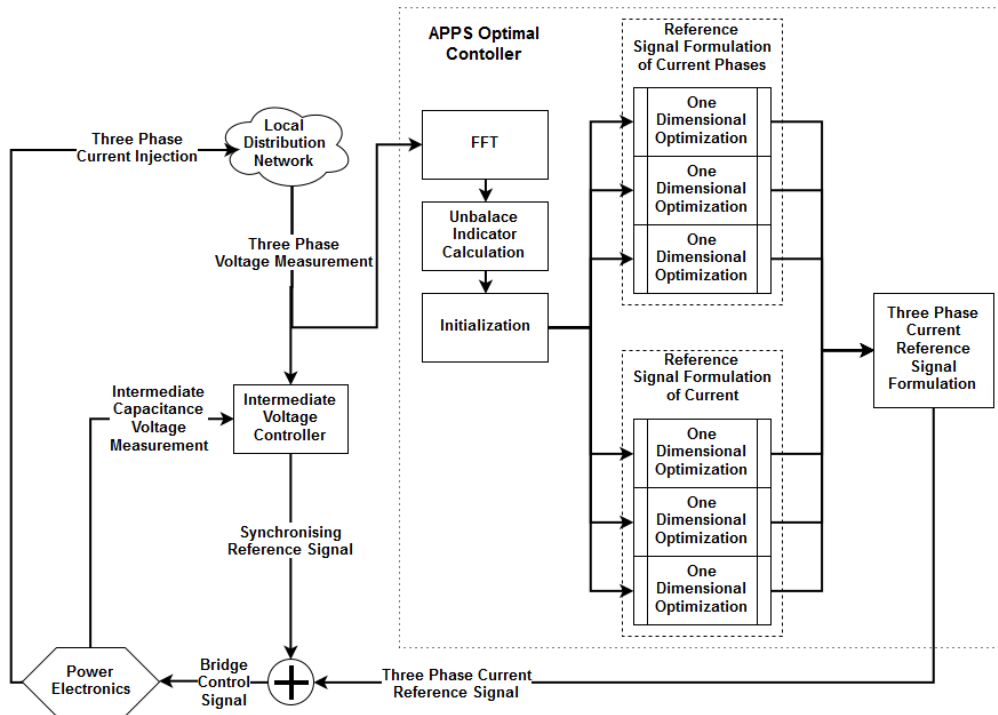


Figure 5.2: The optimization algorithm implemented for current control. A one dimensional linear optimization step is being solved in each dimension of the six dimensional parameter space, iteratively.

The problem is that, the exact mathematical relation is nonlinear because the nonlinear, and highly time variant loads of the network, we should use a control strategy to cope this nonlinear and time variant energy system. For this purpose we chose an asynchronous parallel pattern search method (APPS) which could be able to control our scenario. We applied a variant of the gradient method that is a first-order optimization (minimization) algorithm for a multivariate function  $f(x)$ . The point  $x(k)$  corresponding to the local minimum can be calculated from the negative gradient  $\nabla f(x)$ , that gives the value and direction of the corresponding step in the parameter space. The next step is made in the direction of gradient with the proper sign. This sequence of steps, ideally, converges to local multivariate extreme value  $x(k)$  of the function (5.1).

$$\begin{aligned} x^{(k)} &= x^{(k-1)} - t_k \nabla f(x^{(k-1)}) \\ k &\in \mathbb{N} \end{aligned} \quad (5.1)$$

The controlled electrical system is described by multivariate non-linear differential equations, the optimization of which is infeasible to derive using the differentiation of an error function. Therefore, the optimization methods based on direct differentiation are not applicable. In such cases, when high computational power is needed for performing long time-consuming simulations, the APPS method can be utilized. The search pattern  $p$  is based on the sampling of the error function (selected norm) on a "grid", and it corresponds to variables or subsets of variables in each point in the independent variable or parameter space easily. At the same time, the norm values at these points can be calculated independently if  $\Delta k > 0$ , using (5.2).

$$\begin{aligned} x^{(k+1)} &= x^{(k)} + \Delta_k d_i \\ \text{if } f(x^{(k)} + \Delta_k d_i) &\leq f(x^{(k)}) \\ k &\in \mathbb{N} \end{aligned} \quad (5.2)$$

The parameter is  $x(k) \in R_n$ , and the search pattern  $p \in D = d_1, \dots, d_n$  is taken from a predefined finite set. In this case, the error function values should be calculated for each pattern  $p$  in the set  $D$ . If the error function is not decreasing in any of the directions, then the step size should be reduced (e.g. by half). As the competing directions are different, if there is not enough computing power available for direction vector  $p$ , synchronization should not be maintained. In this case we are talking about the asynchronous case. In the case of our controller, an individual  $p$  vector is defined for each output variable, and the optimization was performed in each direction asynchronously and shifted in time. Most likely, the error function has a single local minimum as a symmetric amplitude and phase values. Approaching the minimal value of norm, the controller uses adaptive increments that are proportional to the norm itself. Because of the complex interactions between the components of the controller, only one parameter is changed at a time, even if the values of the amplitude and phase components in specific time slot changes. The algorithm moves along the six axes of six separate time slots close to the local minimum of the error function.

Unlike other similar approaches, e.g. [45], the explained optimal controller does not rely on a measured current signal (which varies according where the measurement took place on the grid and renders the global optimisation unreliable) but rather measuring and analysing the voltage unbalance via the proposed indicator and optimizes the voltage shape, the latter of which depends on the nonlinear distortion of the whole low-voltage transformer area and determines additional power losses. The controller's performance was compared to a non compensated network, and a network consisting synchronised symmetric power intake from a regular inverter.

In each iteration only one physical value is changing on the six dimensional parameter field, which consists of the three amplitude and three phase values. If the change effects with cost function reduction (the reference norm's normalised value), the controller holds the new value of amplitude or phase for the controlled current sources (Figure 5.2). The advantage of this controller structure that is not necessary to know the controlled value's behaviour well, like we could not determine the number and type of the other loads on the network [34]. There are however two disadvantages. First is the low speed of control, due to the several necessary iterations (depending on the circumstances) to find the optimal directions in the parameter space, and the serial nature of interventions and norm calculations. The second comes from the method itself since the controller may stuck in local minima.

## 5.2 Discussion

### 5.2.1 Dynamical simulation based experiments

In order to be able to investigate the proposed optimization based unbalance reduction control structure with the three phase inverter on a low voltage local grid, all the elements of this complex electrical system (including the photovoltaic source, the inverter, the battery and the nonlinear local grid with different types of loads) has been modeled in Matlab/Simulink environment. The primary aim of the simulation based experiments were to serve as a proof of concept for the proposed complex control structure.

### 5.2.2 Performance analysis

The aim of performance analysis is twofold. First of all, the proposed voltage unbalance indicator has to be investigated in the control structure as the cost function of the optimization based controller, and on the other hand, the control structure itself has to be exposed against engineering expectations.

The results of the first experiment can be seen in Figure 5.3 where the geometrical norm 4.2 has been used as the voltage unbalance indicator and the cost function for the optimizer. The dashed line represents the examined low voltage local network's unbalance norm ( $G$ ) without the proposed controller implemented in the inverter unit of the domestic powerplant while the solid line represents the compensated network's norm value. The performance of

the controller with this norm is apparent, it was able to decrease the network voltage unbalance by approximately 85 %. In this experimental setup the controller has enough input energy due to the batteries and the available solar power.

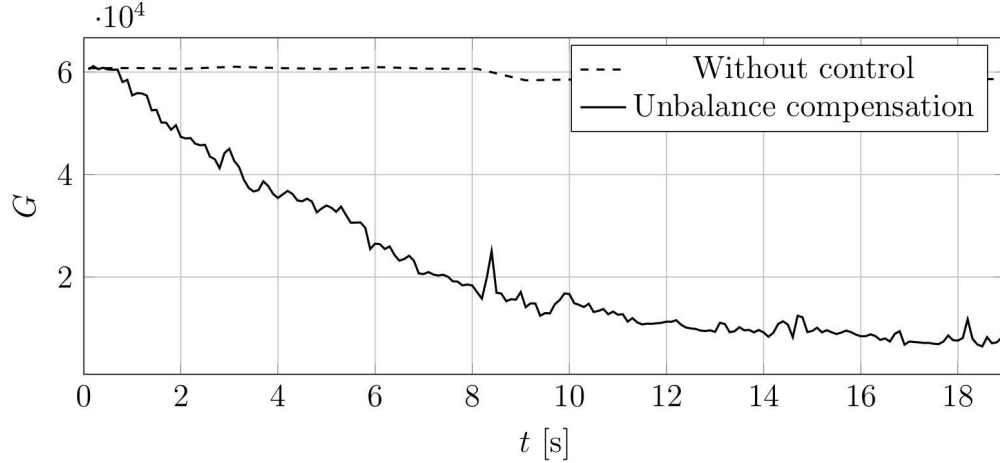


Figure 5.3: Unbalance reduction control system performance with half charged battery and photovoltaic power source available. The underlying unbalance norm is the geometrical one ( $G$ ) in this experiment. After starting the controller at  $t = 0.1s$  the unbalance measure  $G$  of the network significantly decrease.

A slightly more challenging situation is investigated in Figure 5.4 where the controller had to operate without photovoltaic source and batteries. This is called zero balance operation mode when the energy obtained from the network is reinjected in such a way that the unbalance indicators decrease. It can be seen that the performance of the controller is modest than that of Figure 5.3, but it is still acceptable.

### Robustness analysis

The robustness of the proposed control structure is an important qualitative property with respect to the time dependent loads present on the network. The robustness of the proposed controller had to be tested via simulation when different types of loads (inductive, capacitive, resistive) had been varied in step changes representing representing the on/off switching the different types of household appliances (motors, switching mode power supplies, electric heaters, etc.). In the experiment depicted in Figure 5.5, a load change has been introduced to the network in every 15 seconds causing the voltage unbalance to jump to a different value (measured in the geometrical norm (4.2)). As it can be seen in the figure the controller successfully compensates the unbalance after each transient.

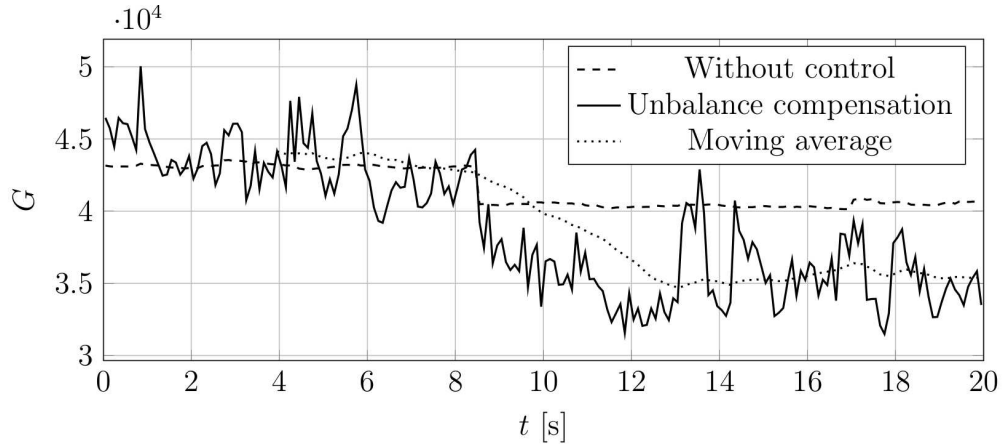


Figure 5.4: Unbalance reduction control system performance without battery and renewable source (zero energy balance operation). The performance reduction is clearly observable compared to the case when external power source is available (Figure 5.3), but as result the voltage unbalance indicator  $G$  reduced by the average value of 14.78%.

### 5.2.3 Environmental effect

The favorable effects of the proposed unbalance reduction control algorithm, i.e. increase power quality not only at the connection point but in the whole low voltage transformer area, which causes a reduction of the effective power loss and the reduction in the CO<sub>2</sub> emission.

#### Power loss reduction on the network

Network loss reduction due to the unbalance reduction compensation control is investigated on Figure 5.6 where the simulation experiment was carried out in the circumstance when the renewable source was not shut down (e.g. insufficient amount of sunlight) and additionally the battery was drained completely [34], [35].

The results show that despite of the negative cross effects of the intermediate voltage controller and the unbalance reduction controller it was possible to find the trade-off between the control goals of the different controllers (maintain zero energy balance for the inverter and decrease the unbalance on the network). The estimated loss reduction in the experimental setup is 6.5%.

#### CO<sub>2</sub> footprint

The fact that this controller enables the reactive power reduction has a favourable consequence, i.e. the power loss or equivalently CO<sub>2</sub> emission and the carbon footprint can also be decreased. The estimated environmental effects of voltage asymmetry compensation can be calculated. Let us assume 3000 kWh for the yearly electric energy consumption an average household and 9.173% for the loss of the distribution network [1]. With the controller

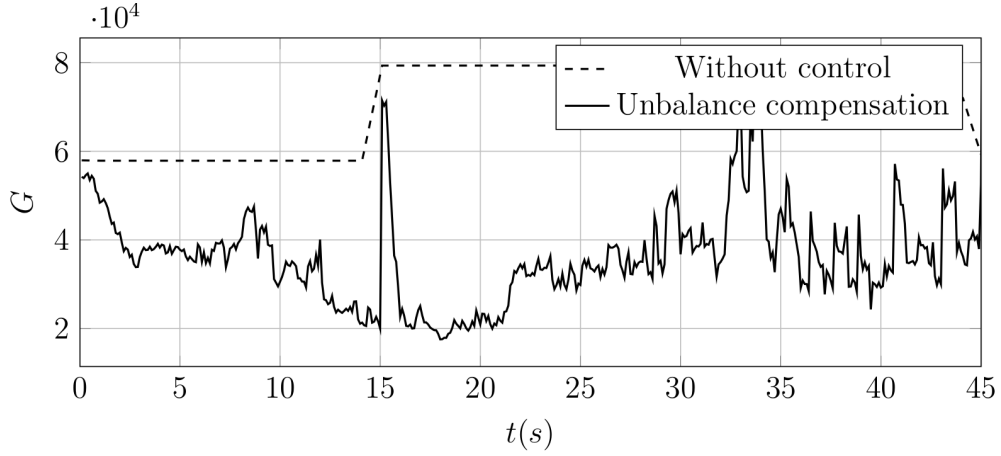


Figure 5.5: Robustness analysis with respect to step type changes in the network load (and voltage unbalance). The unbalance reduction controller successfully compensates the changes in the network voltage unbalance norm ( $G$ ) value.

the losses on the simulated network are reduced by 6.5%. The calculation follows (5.3)

$$\begin{aligned} P_{loss} &= 3000 \text{ kWh} \cdot 9.173\% \\ P_{loss}^{comp} &= 3000 \text{ kWh} \cdot (9.173 \cdot 0.93)\% \\ \Delta P_{loss} &= P_{loss} - P_{loss}^{comp} \end{aligned} \quad (5.3)$$

where  $P_{loss}$  is the assumed network loss per household and  $P_{loss}^{comp}$  is the assumed network loss with unbalance compensation control and  $\Delta P_{loss}$  is the saved energy. According to (5.3), unbalance compensation results in an energy savings of 19.26 kWh. Taking into account the proportion of power currently generated by fossil fuels (coal 17.3 %, gas 38.3% [1], [17]) and the rate of CO<sub>2</sub> emission during electric energy production (1,000 g/kWh from coal and 430 g/kWh from gas), it can be concluded that voltage unbalance compensation could reduce CO<sub>2</sub> emissions by 6504.9 g a year, in an average household.

## 5.3 Conclusion

The currently used measures of voltage unbalance has been extended in this work with a norm candidate. It is more demanding from the computational point of view but has an interesting feature namely it checks electrical asymmetry, i.e. the norm of a  $\pm 120$  degree rotated version of the ideal three-phase phasor is zero in the geometrical sense.

The defined norm is applied as a cost function in the asymmetry reducing controller structure also presented in the paper. Simulations show that the geometrical based indicator can serve as a basis of further research. The suggested controller structure enables the residential users owning a grid synchronized domestic power plant to reduce voltage unbalance measurable at the connection point. The fundamental element of the system is a modified three phase

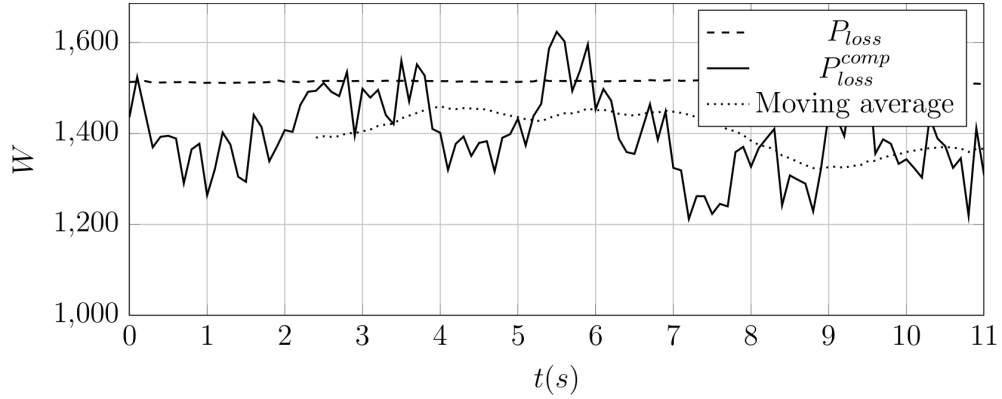


Figure 5.6: Compensation control's loss reduction during zero energy balance operation on the modeled network, where  $P_{loss}$  indicates the effective power losses and  $P_{loss}^{comp}$  effective power losses during control of the network. As result the network losses reduced by mean 6.5%.

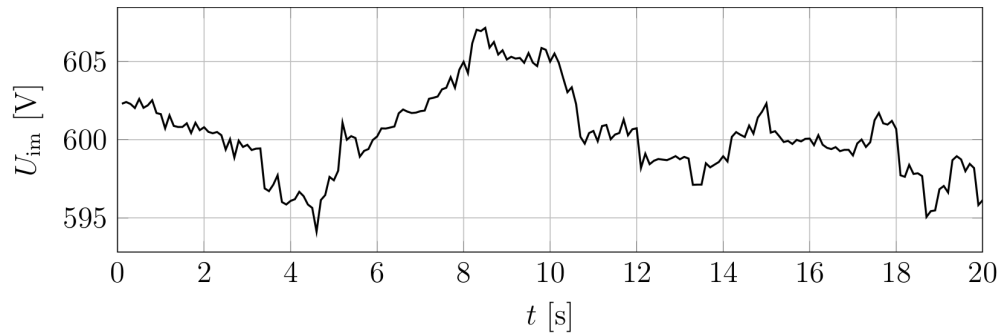


Figure 5.7: Intermediate puffer capacitance's voltage within boundaries ( $600 \pm 10$  V), during zero energy balance operation mode of the voltage unbalance compensation controller.  $U_{im}$  indicates intermediate the capacitance's voltage.

inverter that is capable of the asymmetric injection of any current waveforms to the network. The optimization based control algorithm injects the available energy (as current waveform) in such a way, that the voltage unbalance decreases. This optimization problem is usually constrained by the available renewable energy supplied by the power plant.

The control structure has been tested on a low voltage network model in a dynamical simulation environment consisting of the models of the electrical grid, a domestic power plant, asymmetrical inverter circuit, and different types of loads. Different simulation experiments has been run for each norm and for both the power constrained and unconstrained case. The preliminary results show that this structure can serve as a residential level voltage quality improvement method for the three phase low voltage network **also indirectly reduces the CO<sub>2</sub> emission due facilitating more effective energy usage.**



## Chapter 6

### Summary

# Chapter 7

## Thesis

### 7.1 Playground

1. Thesis: Thesis: Constrained, explicit predictive control for current source buck-type rectifiers.

The proposed CSR model has been examined from the design and implementation points of view with the purpose of explicit model-based predictive control, and It proved to be that regular set of differential equations of the CSR appears to be too complex for such a design approach. To address this issue the usage of separated AC and DC equation sets was implemented. This solution eliminates bilinearity and enables the application of linear control design techniques, and enables different solutions on AC and DC side. For the modulation, Current-based SVPWM has been used with an emphasis on the reduction of switching losses. For DC side control explicit model predictive control method is described and the method's effectiveness was compared to conventional state feedback control. The implementation was carried out in a Matlab/Simulink environment and the proposed control structure has been tested by simulation. Additionally, the proper implementation of the system in a modern DSP chip would result in real-time operation.

2. Thesis: Geometrical indicator for voltage unbalance in three phase networks.

The currently used measures of voltage unbalance has been extended with a norm candidate. It is more demanding from the computational point of view but has a new feature namely it checks electrical asymmetry, i.e. the norm of a  $\pm 120$  degree rotated version of the ideal three phase phasor is zero in the geometrical sense. The defined norm is applied as a cost function in the asymmetry reducing controller structure also presented in the paper. Simulations show that the geometrical based unbalance indicator can serve as a basis of further research.

3. Thesis: Voltage unbalance compensation with optimization based control algorithm and asymmetrical inverter structure.

The suggested controller structure enables the residential users owning a

grid synchronized domestic power plant to reduce voltage unbalance measurable at the connection point. The fundamental element of the system is a modified three phase inverter that is capable of the asymmetric injection of any current waveforms to the network. The optimization based control algorithm injects the available energy (as current waveform) in such a way, that the voltage unbalance decreases. This optimization problem is usually constrained by the available renewable energy supplied by the power plant.

The control structure has been tested on a low voltage network model in a dynamical simulation environment consisting of the models of the electrical grid, a domestic power plant, asymmetrical inverter circuit, and different types of loads. Different simulation experiments has been run for each norm and for both the power constrained and unconstrained case. The preliminary results show that this structure can serve as a residential level voltage quality improvement method for the three phase low voltage network.

# List of notations

## Chapter 4.1. notations

$\mathbf{A}$	State matrix of the DC side system
$\mathbf{A}_d$	Discretised state matrix of the DC side system
$\mathbf{B}$	Input matrix of the DC side system
$\mathbf{B}_d$	Discretised input matrix of the DC side system
$C_{ac}$	AC side inductance
$C_{dc}$	DC side inductance
$\mathbf{C}$	Output matrix of the DC side system
$\mathbf{C}_d$	Discretised output matrix of the DC side system
$C_{reg_i}$	Critical region
$D(-\Theta)$	Inverse Clarke transformation
$f$	Network voltage frequency
$f_{pwm}$	Rectifier switching frequency
$f_s$	Simulation frequency
$f_i$	?
$g_i$	?
$G$	?
$H$	?
$HPF(s)$	High pass filter transfer function
$i_{ac1,2,3}$	AC side inductance current
$i_{ac\alpha,\beta}$	AC side inductance current in Clarke frame
$i_{acd,q}$	AC side inductance current in Park frame
$i_{HPF}$	AC side damping current
$i_{r1,2,3}$	Rectifier current
$i_{rMPC_d}$	Direct component of the output of the EMPC controller
$i_{r1,2,3}^*$	Rectifier reference current
$i_{r\alpha,\beta}^*$	Rectifier reference current in Clarke frame
$i_{rcontrol_d}$	Direct component of the output of the DC voltage controller
$i_{rHF_d}$	Direct component of the damping current of AC noise
$i_{dc}$	DC side inductance current
$i_{ref\alpha,\beta}$	$\alpha$ , or $\beta$ component of the reference current vector respectively
$\vec{i}_{0,\dots,9}$	Current vector of the phasor
$\vec{i}_{ref}$	Reference current vector
$J$	Quadratic EMPC cost function
$k_{1,2,3}$	State feedback controller's coefficients
$K$	Feedback gain of EMPC controller

$L_{ac}$	AC side inductance
$L_{dc}$	DC side inductance
$n$	Current phasor sector indicator
$N$	Control horizon
$N_y, N_u, N_c$	Output, input and constraint horizons
$P_n$	Nominal power of the CRS
$\mathbf{Q}_w$	State weight matrix of quadratic MPC cost function
$R$	Phase resistance
$R_H$	Virtual damping resistance
$R_{load}$	Load resistance
$\mathbf{R}_w$	Input weight matrix of quadratic MPC cost function
$\mathbb{R}$	Set of real numbers
$S$	Current phasor sector
$\mathbf{S}$	?
$t$	Discrete timestep
$T_s$	Switching period
$T_{0,\dots,9}$	Dwell time in the corresponding sector
$\mathbf{u}$	Output vector of the DC side system
$u_{1,2,3}$	AC side phase voltage
$u_{\alpha,\beta}$	AC side phase voltage in Clarke frame
$u_{d,q}$	AC side phase voltage in Park frame
$u_{c1,2,3}$	AC side capacitance voltage
$u_{c\alpha,\beta}$	AC side capacitance voltage in Clarke frame
$u_{cd,q}$	AC side capacitance voltage in Park frame
$u_0$	DC side voltage on load
$u_0^*$	DC side voltage reference
$u_{MPC}$	MPC control variable
$\mathbf{u}^*$	?
$U$	Set of MPC inputs
$Un$	Network line-to-line voltage
$\mathbf{x}$	State vector of the DC side system
$\mathbf{y}$	Input vector of the DC side system
$V_z$	?
$\delta_{1,2,3}$	Conduction state leg in Clarke frame
$\delta_{\alpha,\beta}$	Conduction state leg in Park frame
$\delta_{d,q}$	Conduction state leg
$\theta$	Network voltage vector's angular displacement
$\omega$	Network voltage vector's angular velocity
$\omega_{ac}$	Ac side LC filter angular velocity
$\omega_n$	Damping angular velocity

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Chapter 4.2. notations

$IEEEv_{936}$	?
$IEEEv_{112}$	?
$V_{an}, V_{bn}, V_{cn}$	Line-to-neutral network voltage
$V_{ab}, V_{bc}, V_{ca}$	Line-to-line network voltage
$MDV$	?

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$VUF$	Voltage unbalance factor
$V_{1,\dots,n}$	Voltage of the $n^{th}$ harmonic
$\Delta R$	Vectorial deviation of the $R^{th}$ phase
$\Delta S$	Vectorial deviation of the $S^{th}$ phase
$\Delta T$	Vectorial deviation of the $T^{th}$ phase
$N$	Vectorial voltage deviation from the balanced state
$G$	Geometrical voltage deviation from the balanced state
$\Delta_{Ideal}$	Triangle of Ideal voltage vectors
$\Delta_{Real}$	Triangle of real voltage vectors

---

Chapter 4.3. notations	
$x^k$	Local multivariate extreme state at the $k^{th}$ timestep
$t_k$	Timestep at the $k$
$k$	Discrete timestep
$\Delta_k$	?
$d_i$	?
$D$	?
$p$	?
$P_{loss}$	Lost power per household due to network unbalance
$P_{loss}^{comp}$	Assumed network loss with unbalance compensation control
$\Delta P_{loss}$	Saved power with unbalance compensation control

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# Abbreviations

VSR:	Voltage source rectifier
CSR:	Current source rectifier
MPC:	Model predictive coltrol
MPT:	Model predictive control toolbox
EMPC:	Explicit model predictive control
HPF:	High pass filter
SVPWM	Space vector pulse width modulated
SFC:	State feedback control
MDV:	?
VUF:	Voltage unblance factor (i.e. TDV)
THD:	Total harmonic distortion
IGBT:	Insulated gate bipolar transistor
MPPT:	Maximum power point tracking

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