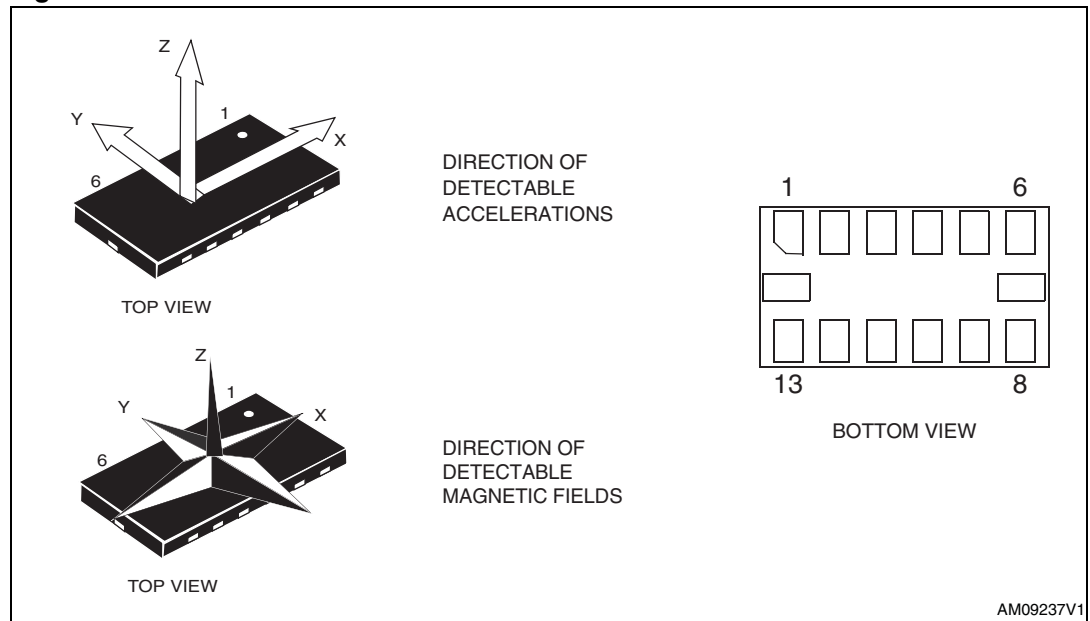


## 1.2 Pin description

**Figure 2. Pin connection**



**Table 2. Pin description**

Pin#	Name	Function
1	Vdd_IO	Power supply for I/O pins
2	SCL	Signal interface I <sup>2</sup> C serial clock (SCL)
3	SDA	Signal interface I <sup>2</sup> C serial data (SDA)
4	INT2	Inertial Interrupt 2
5	INT1	Inertial Interrupt 1
6	C1	Reserved capacitor connection (C1)
7	GND	0 V supply
8	Reserved	Leave unconnected
9	DRDY	Data ready
10	Reserved	Connect to GND
11	Reserved	Connect to GND
12	SETP	S/R capacitor connection (C2)
13	SETC	S/R capacitor connection (C2)
14	Vdd	Power supply