



# Analog Circuit Design For Process Variation-Resilient Systems-on-a-Chip

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**Summary :** Free analog circuit design for process variation-resilient systems-on-a-chip pdf download - this book describes several techniques to address variation-related design challenges for analog blocks in mixed-signal systems-on-chip the methods presented are results from recent research works involving receiver front-end circuits baseband filter linearization and data conversion these circuit-level techniques are described with their relationships to emerging system-level calibration approaches to tune the performances of analog circuits with digital assistance or control coverage also includes a strategy to utilize on-chip temperature sensors to measure the signal power and linearity characteristics of analog rf circuits as demonstrated by test chip measurements describes a variety of variation-tolerant analog circuit design examples including from rf front-ends high-performance adcs and baseband filters includes built-in testing techniques linked to current industrial trends balances digitally-assisted performance tuning with analog performance tuning and mismatch reduction approaches describes theoretical concepts as well as experimental results for test chips designed with variation-aware techniques

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