# TLA<sup>+</sup> Tools: практичный инструмент формальной верификации алгоритмов

(или что ещё разработчику на Golang точно надо изучить во время карантина)

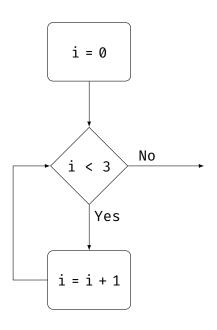


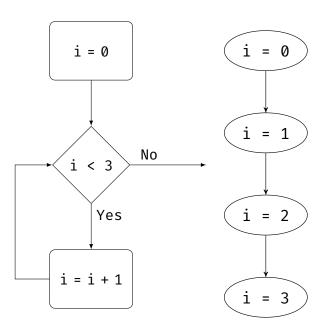
GopherCon Russia 2020 Alexey Naidyonov ITooLabs Mar 28 2020

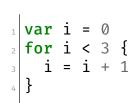


Что такое программа?

```
var i = 0
for i < 3 {
i = i + 1
}
```



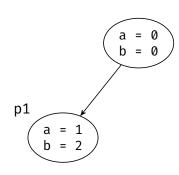




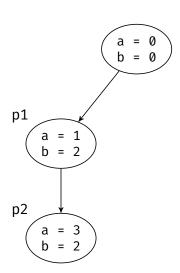


$$\begin{vmatrix} b & = & a & + & 1 \\ a & = & b & + & 1 \end{vmatrix}$$

$$\begin{bmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{bmatrix}$$

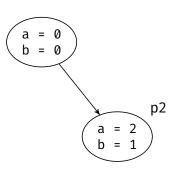


$$\begin{bmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{bmatrix}$$



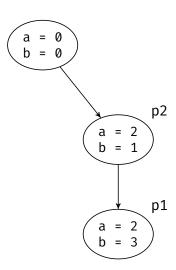
$$\begin{vmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{vmatrix}$$

$$\begin{vmatrix} b & = & a & + & 1 \\ a & = & b & + & 1 \end{vmatrix}$$



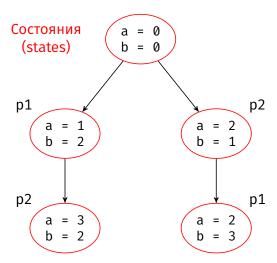
$$\begin{vmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{vmatrix}$$

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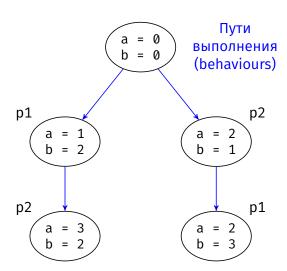


$$\begin{bmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{bmatrix}$$

$$\begin{vmatrix}
b & = a + 1 \\
a & = b + 1
\end{vmatrix}$$



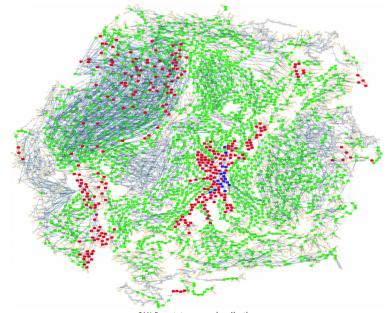
$$\begin{bmatrix} a & = & b & + & 1 \\ b & = & a & + & 1 \end{bmatrix}$$



$$(m \cdot n) \frac{(m \cdot n)!}{(n!)^m},$$

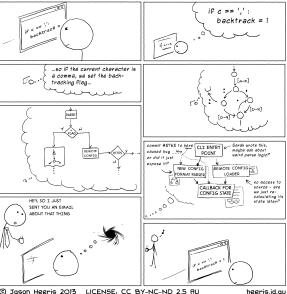
где

m - количество процессов n - длина пути выполнения



 $\textbf{CAN Bus state space visualization} \\ \texttt{https://www3.hhu.de/stups/prob/index.php/State\_space\_visualization\_examples} \\$ 

#### THIS IS WHY YOU SHOULDN'T INTERRUPT A PROGRAMMER



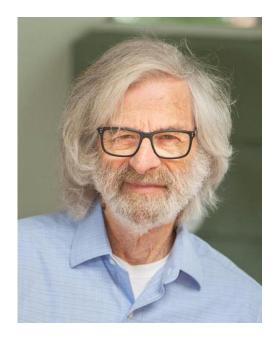
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"A distributed system is one in which the failure of a computer you didn't even know existed can render your own computer unusable."

— Leslie Lamport, May 1987

# Лесли Лэмпорт (Leslie Lamport)

- Lamport timestamps
- · Bakery algorithm
- PAXOS
- LaTeX
- TLA+



# **Temporal Logic of Actions**

 $A \wedge B$ 

Коньюнкция

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция

$A \wedge B$	Коньюнкция
$A \vee B$	Дизьюнкция
$A \to B$	Импликация

$A \wedge B$	Коньюнкция
$A \vee B$	Дизьюнкция
$A \to B$	Импликация
$\neg A$	Отрицание

$A \wedge B$	Коньюнкция
$A \vee B$	Дизьюнкция
$A \to B$	Импликация
$\neg A$	Отрицание

# Темпоральная логика



 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

## Темпоральная логика

 $\Diamond A$  Eventually

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

# Темпоральная логика

 $\Diamond A$  Eventually  $\Box A$  Always

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

### Темпоральная логика

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

### Темпоральная логика

$A \wedge B$	Коньюнкция
$A \vee B$	Дизьюнкция
$A \to B$	Импликация
$\neg A$	Отрицание

# Темпоральная логика

$\Diamond A$	Eventually
$\Box A$	Always
$A \leadsto B$	Leads to
$\Box \Diamond A$	Infinitely ofte

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

## Темпоральная логика

 $\Box \Diamond A$  Infinitely often  $\Diamond \Box A$ 

 $A \wedge B$  Коньюнкция  $A \vee B$  Дизьюнкция  $A \to B$  Импликация  $\neg A$  Отрицание

## Темпоральная логика

$A \wedge B$	$A/\backslashB$	Коньюнкция
$A \vee B$	$A \setminus /B$	Дизьюнкция
$A \to B$	A=>B	Импликация
$\neg A$	~A	Отрицание

# Темпоральная логика

$\Diamond A$	<>A	Eventually
$\Box A$	[]A	Always
$A \leadsto B$	~>A	Leads to
$\Box \Diamond A$	[]<>A	Infinitely often
$\Diamond\Box A$	<>[]A	<b>Eventually always</b>

**EXTENDS Naturals** 

CONSTANTS MinValue, MaxValue
ASSUME MinValue < MaxValue

VARIABLE counter

Invariant ≜ counter ∈ MinValue. . MaxValue

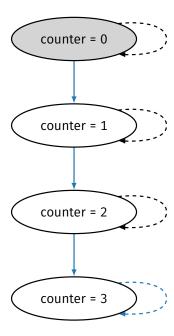
Success  $\triangleq \Diamond \Box (counter = MaxValue)$ 

Init ≜ counter = MinValue

Next ≜ counter' = IF counter < MaxValue

THEN counter + 1

FLSE counter



#### **EXTENDS** Naturals

CONSTANTS MinValue, MaxValue ASSUME MinValue < MaxValue

VARIABLE counter

Invariant ≜ counter ∈ MinValue. . MaxValue

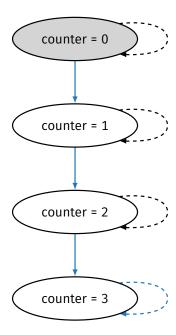
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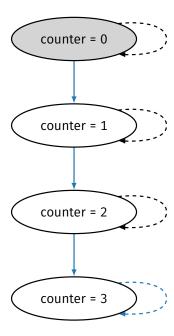
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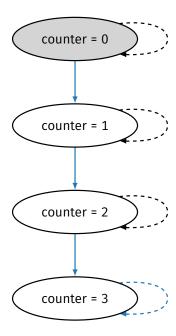
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**EXTENDS** Naturals

CONSTANTS MinValue, MaxValue
ASSUME MinValue < MaxValue

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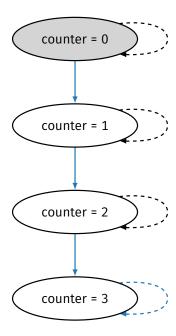
Invariant ≜ counter ∈ MinValue. . MaxValue

Success  $\triangleq \Diamond \Box (counter = MaxValue)$ 

Init ≜ counter = MinValue

Next  $\triangleq$  counter' = IF counter < MaxValue THEN counter + 1 FLSE counter

 $Spec \triangleq \land Init \\ \land \Box [Next]_{counter} \\ \land WF_{counter}(Next)$ 



**EXTENDS Naturals** 

CONSTANTS MinValue, MaxValue
ASSUME MinValue < MaxValue

VARIABLE counter

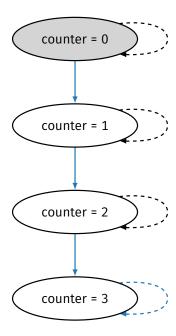
#### Invariant ≜ counter ∈ MinValue.. MaxValue

Success  $\triangleq \Diamond \Box (counter = MaxValue)$ 

Init ≜ counter = MinValue

Next  $\triangleq$  counter' = IF counter < MaxValue THEN counter + 1 FLSE counter

 $Spec \triangleq \land Init \\ \land \Box [Next]_{counter} \\ \land WF_{counter}(Next)$ 



**EXTENDS** Naturals

CONSTANTS MinValue, MaxValue
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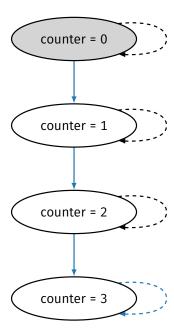
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Next  $\triangleq$  counter' = IF counter < MaxValue THEN counter + 1 FLSE counter

 $\begin{aligned} \mathsf{Spec} &\triangleq \wedge \mathsf{Init} \\ &\wedge \Box [\mathsf{Next}]_{\mathsf{counter}} \\ &\wedge \mathsf{WF}_{\mathsf{counter}}(\mathsf{Next}) \end{aligned}$ 



**EXTENDS Naturals** 

CONSTANTS MinValue, MaxValue
ASSUME MinValue < MaxValue

VARIABLE counter

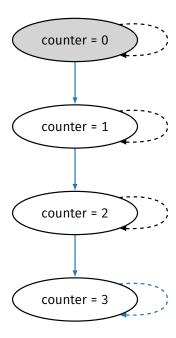
Invariant ≜ counter ∈ MinValue. . MaxValue

Success  $\triangleq \Diamond \Box (counter = MaxValue)$ 

#### Init ≜ counter = MinValue

 $\begin{aligned} \text{Next} \triangleq \text{counter}' &= \text{IF counter} < \text{MaxValue} \\ &\quad \text{THEN counter} + \mathbf{1} \\ &\quad \text{FLSE counter} \end{aligned}$ 

 $Spec \triangleq \land Init \\ \land \Box [Next]_{counter} \\ \land WF_{counter}(Next)$ 



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ASSUME MinValue < MaxValue

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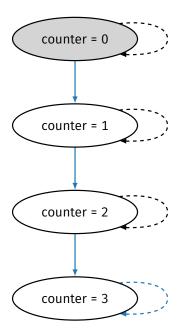
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FLSE counter

 $\begin{aligned} \mathsf{Spec} &\triangleq \wedge \mathsf{Init} \\ &\wedge \Box [\mathsf{Next}]_{\mathsf{counter}} \\ &\wedge \mathsf{WF}_{\mathsf{counter}}(\mathsf{Next}) \end{aligned}$ 



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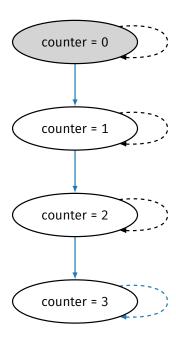
Init ≜ counter = MinValue

Next  $\triangleq$  counter' = IF counter < MaxValue THEN counter + 1 FLSE counter

Spec ≜ ∧ Init

 $\wedge \Box [Next]_{counter}$ 

 $\land WF_{counter}(Next)$ 



**EXTENDS** Naturals

CONSTANTS MinValue, MaxValue
ASSUME MinValue < MaxValue

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Invariant ≜ counter ∈ MinValue. . MaxValue

 $Success \triangleq \Diamond \Box (counter = MaxValue)$ 

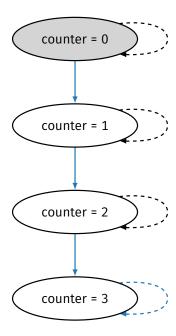
Init ≜ counter = MinValue

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FLSE counter

Spec  $\triangleq \land Init$  $\land \Box [Next]_{counter}$  $\land WF_{counter}(Next)$ 



**EXTENDS** Naturals

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Success  $\triangleq \Diamond \Box (counter = MaxValue)$ 

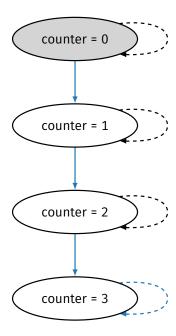
Init ≜ counter = MinValue

Next ≜ counter' = IF counter < MaxValue

THEN counter + 1

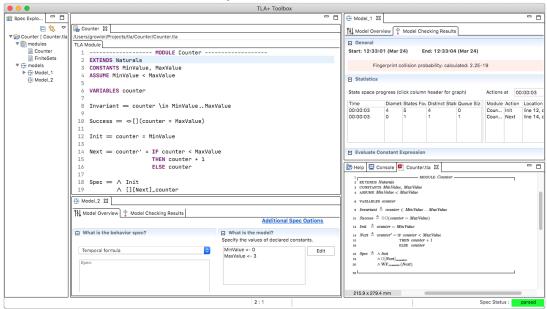
FLSE COUNTER

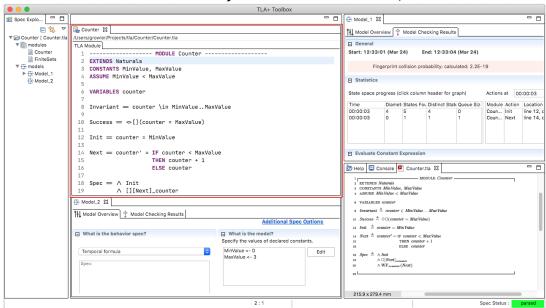
 $Spec \triangleq \land Init \\ \land \Box [Next]_{counter} \\ \land WF_{counter}(Next)$ 

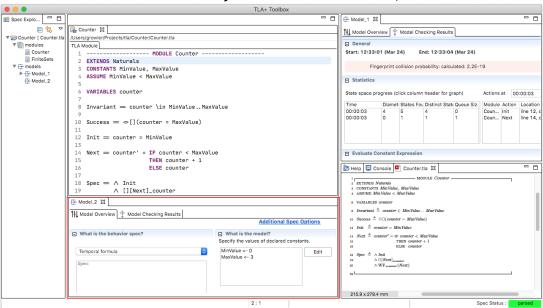


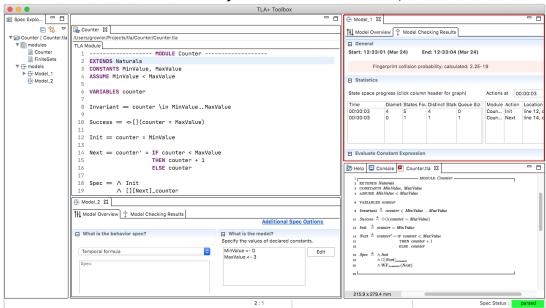
# **TLC**

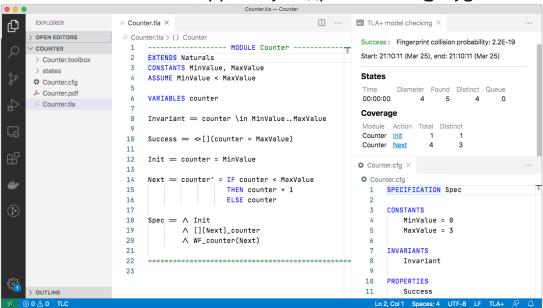
Explicit State Model Checker for TLA+

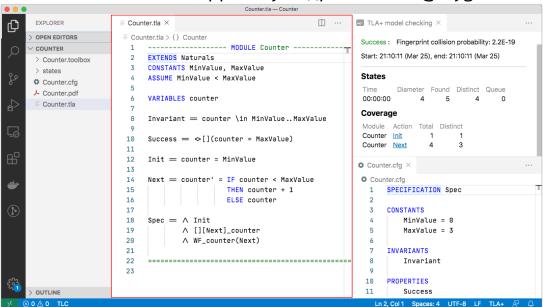


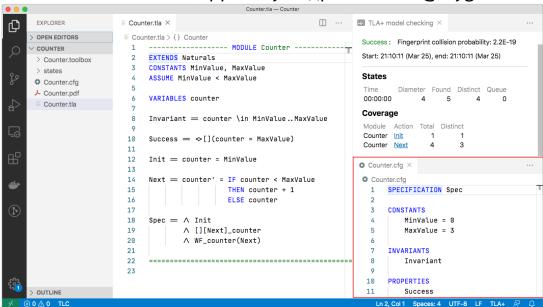


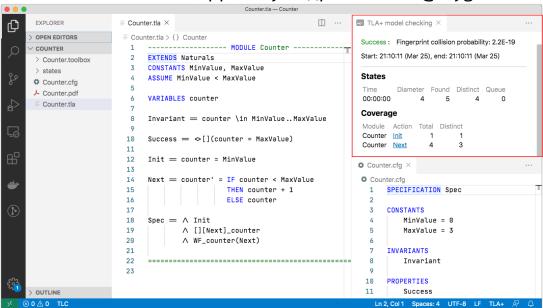












# ДЕМО

00\_Counter.tla

```
14 fair process proc1 = 1
15 begin p1:
a := b + 1;
17 b := a + 1;
18 end process;
19
20 fair process proc2 = 2
21 begin p2:
b := a + 1;
a := b + 1;
24 end process;
26 end algorithm; *)
```

#### Алгоритм

```
14 fair process proc1 = 1
15 begin p1:
a := b + 1;
17 b := a + 1;
18 end process;
19
20 fair process proc2 = 2
21 begin p2:
  b := a + 1;
22
a := b + 1;
24 end process;
26 end algorithm; *)
```

#### Переменные

```
(*--algorithm 01_PCDemo

variables
    a = 0,
    b = 0;

define
    Success == <>[](a+b = 5)
end define;
```

```
14 fair process proc1 = 1
15 begin p1:
a := b + 1;
17 b := a + 1;
18 end process;
19
20 fair process proc2 = 2
21 begin p2:
  b := a + 1;
22
a := b + 1;
24 end process;
26 end algorithm; *)
```

#### Определения TLA<sup>+</sup>

```
(*--algorithm 01_PCDemo

variables
    a = 0,
    b = 0;

define
    Success == <>[](a+b = 5)
end define;
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a := b + 1;
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### Процессы

```
14 fair process proc1 = 1
15 begin p1:
a := b + 1;
b := a + 1;
18 end process;
19
20 fair process proc2 = 2
begin p2:
b := a + 1;
a := b + 1;
24 end process;
26 end algorithm; *)
```

#### Метки

```
14 fair process proc1 = 1
15 begin p1:
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17 b := a + 1;
18 end process;
19
20 fair process proc2 = 2
begin p2:
  b := a + 1;
22
a := b + 1;
24 end process;
26 end algorithm; *)
```

```
\* BEGIN TRANSLATION
28 VARIABLES a, b, pc
29
  (* define statement *)
  Success == \langle \rangle [](a+b = 5)
34 | vars == << a, b, pc >>
35
  ProcSet == \{1\} \setminus \{2\}
37
  Init == (* Global variables *)
           / a = 0
39
           /  b = 0
40
           /\ pc = [self \in ProcSet |-> CASE self = 1 -> "p1"
41
                                                 [] self = 2 -> "p2"]
42
```

```
\* BEGIN TRANSLATION
28 VARIABLES a, b, pc
29
30 (* define statement *)
  Success == <>[](a+b = 5)
34 vars == << a, b, pc >>
  ProcSet == \{1\} \setminus \{2\}
37
  Init == (* Global variables *)
           / a = 0
39
           /  b = 0
40
           /\ pc = [self \in ProcSet |-> CASE self = 1 -> "p1"
41
                                              [] self = 2 -> "p2"]
42
```

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  VARIABLES a, b, pc
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  (* define statement *)
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  ProcSet == {1} \cup {2}
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  Init == (* Global variables *)
39
          /  b = 0
40
          /\ pc = [self \in ProcSet |-> CASE self = 1 -> "p1"
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                                             [] self = 2 -> "p2"]
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```
* BEGIN TRANSLATION
  VARIABLES a, b, pc
29
  (* define statement *)
  Success == <>[](a+b = 5)
34 | vars == << a, b, pc >>
  ProcSet == \{1\} \setminus \{2\}
  Init == (* Global variables *)
39
           /  b = 0
40
           /\ pc = [self \in ProcSet |-> CASE self = 1 -> "p1"
41
                                              [] self = 2 -> "p2"]
42
```

```
44 p1 == /\ pc[1] = "p1"
    / \ a' = b + 1
       / \ b' = a' + 1
        /\ pc' = [pc EXCEPT ![1] = "Done"]
48
  proc1 == p1
50
_{51}|p2 == /\ pc[2] = "p2"
     / \ b' = a + 1
       / \ a' = b' + 1
        /\ pc' = [pc EXCEPT ![2] = "Done"]
55
56 proc2 == p2
```

```
p1 == / pc[1] = "p1"
        / \ a' = b + 1
45
         /  b' = a' + 1
46
         /\ pc' = [pc EXCEPT ![1] = "Done"]
47
48
  proc1 == p1
50
  p2 == / pc[2] = "p2"
51
         / \ b' = a + 1
52
         / \ a' = b' + 1
53
         /\ pc' = [pc EXCEPT ![2] = "Done"]
54
55
  proc2 == p2
```

```
p1 == /\ pc[1] = "p1"
        / \ a' = b + 1
45
         / b' = a' + 1
46
         /\ pc' = [pc EXCEPT ![1] = "Done"]
47
  proc1 == p1
50
  p2 == / pc[2] = "p2"
         /\setminus b' = a + 1
52
         / \ a' = b' + 1
         /\ pc' = [pc EXCEPT ![2] = "Done"]
  proc2 == p2
```

```
(* Allow infinite stuttering to prevent deadlock on termination.
59 | Terminating == /\ \A self \in ProcSet: pc[self] = "Done"
                 /\ UNCHANGED vars
60
61
  Next == proc1 \/ proc2
            \/ Terminating
64
  Spec == /\ Init /\ [][Next]_vars
          /\ WF_vars(proc1)
66
          /\ WF vars(proc2)
67
  Termination == <>(\A self \in ProcSet: pc[self] = "Done")
```

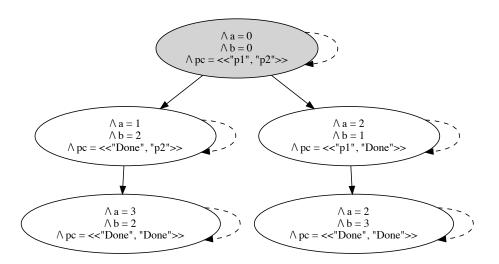
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   → *)
  Terminating == /\ \A self \in ProcSet: pc[self] = "Done"
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          /\ WF_vars(proc1)
          /\ WF vars(proc2)
67
  Termination == <>(\A self \in ProcSet: pc[self] = "Done")
```

# ДЕМО

01\_PCDemo.tla

#### TLA<sup>+</sup> Toolbox CLI

```
growler@macbook:$ tlc -dump dot,colorized 01_PCDemo.dot 01_PCDemo.tla
Computing initial states...
Finished computing initial states: 1 distinct state generated.
Checking 2 branches of temporal properties for the complete state space
  with 10 total distinct states
Finished checking temporal properties
Model checking completed. No error has been found.
  Estimates of the probability that TLC did not check all reachable states
  because two distinct states had the same fingerprint:
    calculated (optimistic): val = 5.4E-19
7 states generated, 5 distinct states found, 0 states left on queue.
growler@macbook:$ dot -Tsvg 01_PCDemo.dot -o 01_PCDemo.svg
growler@macbook:$
```



#### **PlusCal**

#### Атомарность

```
/* (*--algorithm A

variables
/* a = 0,
/* b = 0;

define
/* Success == <>[](
/* (a + b) = 5
/* end define;
```

```
16 fair process proc1 = 1
17 begin
      p1_1: a := b + 1;
18
      p1_2: b := a + 1;
19
20 end process;
22 fair process proc2 = 2
23 begin
      p2_1: b := a + 1;
24
      p2_2: a := b + 1;
25
26 end process;
27
28 end algorithm; *)
```

## ДЕМО

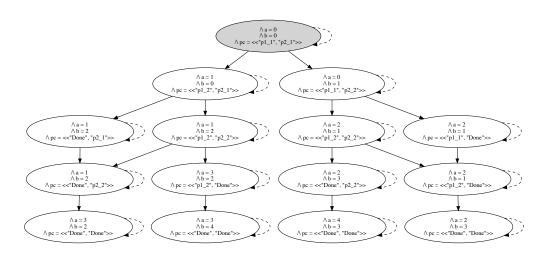
02\_PCDemo.tla

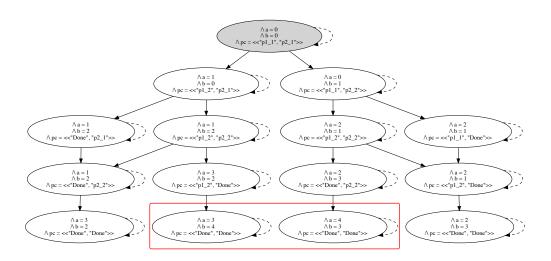
Error: Temporal properties were violated. Error: The following behavior constitutes a counter-example: State 1: <Initial predicate> /\ a = 0 /\ b = 0 /\ pc = <<"p1\_1", "p2\_1">> State 2: <p1\_1 line 48, col 9 to line 51, col 17 of module 02\_PCDemo> /\ a = 1 /\ b = 0 /\ pc = <<"p1 2", "p2 1">> State 3: <p2 1 line 60, col 9 to line 63, col 17 of module 02 PCDemo> /\ a = 1 / b = 2 /\ pc = <<"p1\_2", "p2\_2">> State 4: <p2\_2 line 65, col 9 to line 68, col 17 of module 02\_PCDemo> / a = 3 / b = 2 /\ pc = <<"p1\_2", "Done">> State 5: <p1 2 line 53, col 9 to line 56, col 17 of module 02 PCDemo> /\ a = 3 /\ b = 4 /\ pc = <<"Done", "Done">>

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## Sets (Множества)

```
Procs == {1, 5, 8}

Ops == {"inc", "dec"}

Vals == 1..MaxValue
```

### Sets (Множества)

```
Procs == {1, 5, 8}
Ops == {"inc", "dec"}
Vals == 1..MaxValue
Cmd == Ops \union {NULL}
{1, 2} \intersect {2, 3} = {2}
Var \in 1..10
ASSUME Var \notin Procs
```

### Sets (Множества)

```
Procs == \{1, 5, 8\}
Ops == {"inc", "dec"}
Vals == 1..MaxValue
Cmd == Ops \union {NULL}
\{1, 2\} \setminus \{2, 3\} = \{2\}
Var \in 1..10
ASSUME Var \notin Procs
TRUE \in BOOLEAN
"string" \in STRING
UNION \{\{1, 2\}, \{2, 3\}\} = \{1, 2, 3\}
```

# Tuples (Кортежи)

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```
a == <<1, 5, 8>>
a[2] = 5
Len(a) = 3
Head(a) = 1
Tail(a) = <<5, 8>>
SubSeq(a, 1, 2) = <<1, 5>>
```

## Tuples (Кортежи)

```
a == <<1, 5, 8>>
a[2] = 5
Len(a) = 3
Head(a) = 1
Tail(a) = <<5, 8>>
SubSeg(a, 1, 2) = \langle \langle 1, 5 \rangle \rangle
<<1, 2>> \o <<4, 8>> = <<1, 2, 4, 8>>
a := Append(a, 5)
a[i] := i
```

## Records (Записи)

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```
s == [rdy|->0, ack|->0, val|->0]
s.rdy = 0
s["ack"] = 0
"a" :> 1 = [a|->1]
[a|->1] @@ [b|->2] = [a|->1, b|->2]
```

## Records (Записи)

```
s == [rdy|->0, ack|->0, val|->0]
s.rdy = 0
s["ack"] = 0
"a" :> 1 = [a|->1]
[a|->1] @@ [b|->2] = [a|->1, b|->2]
s.ack := 1
p.a := p.b || p.b = p.a
```

## Functions (Функции)

```
Squares == [n \in 1..Nat |-> n * n]
Squares[2] = 4
DOMAIN Squares = 1..Nat
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DOMAIN Squares = 1..Nat
s == [rdy|->0, ack|->0, val|->0]

DOMAIN s = {"ack", "rdy", "val"}

DOMAIN <<5, 6, 7>> = 1..3
```

```
g func sender(ch chan *Data) {
    ch <- MESSAGE
    close(ch)
}

func receiver(ch chan *Data) (result *Data) {
    for {
        if r := <-ch; r == nil {
            return
        } else {
            result = r
        }
    }
}</pre>
```

```
----- MODULE 03_GoChannel -----

INSTANCE TLC
CONSTANTS NULL, Message

(*--algorithm GoChannel
variables
ch = [open |-> TRUE, sent |-> FALSE, rcvd |-> FALSE, val |-> NULL],
result = NULL;

define
Success == <>[](result = Message)

Ordered == pc.sender = "Done" ~> pc.receiver = "Done"
end define;
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```

```
15 | fair process Sender = "sender"
  begin
16
     Send:
                                             31
       assert ch.open;
19
       await ~ch.sent;
       ch.val := Message || ch.sent :=
20
                                             34
        → TRUE:
     WaitSent:
       await ch.sent /\ ch.rcvd;
                                             36
       ch.sent := FALSE || ch.rcvd :=
23
        → FALSE;
                                             38
     Close:
24
                                             39
       assert ch.open;
       ch.open := FALSE;
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                                             41
  end process:
```

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29 | fair process Receiver = "receiver"
  begin Receive:
    while TRUE do
       either
         await ch.sent /\ ~ch.rcvd;
         result := ch.val || ch.val :=
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       or
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         goto Done;
       end either:
    end while:
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```

### ДЕМО

03\_GoChannel.tla

```
27 macro recv(var, ch)
28 begin
      await ~ch.open \/ (ch.sent /\
29
       30
      if ~ch.open then var := NULL;
      else
31
          var := ch.val || ch.val := NULL
             || ch.rcvd := TRUE;
      end if;
  end macro:
  macro close(ch)
  begin
      assert ch.open;
38
      ch.open := FALSE;
  end macro;
```

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```
sent = <<>>, recvd = <<>>;
      Success == <>[](sent = recvd)
42 fair process Sender = "sender"
43 variable i = 0;
44 begin Send:
      while nextId \in 1..MaxMsg do
           i := nextId || sent :=
           → Append(sent, nextId) | |

→ nextId := nextId + 1;

47
          Send_1: send(ch, i);
           Send 2: wait sent(ch);
48
      end while;
49
      close(ch);
51 end process:
```

```
fair process Receiver = "receiver"
variable j = 0;
begin Receive:
while TRUE do
    recv(j, ch);
if j /= NULL then
    recvd := Append(recvd, j);
else
    goto Done;
end if;
end while;
end process;
```

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           end if:
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48
      end while:
49
      close(ch);
50
  end process:
```

```
53 fair process Receiver = "receiver"
54 variable j = 0;
  begin Receive:
       while TRUE do
56
           recv(j, ch);
           if j /= NULL then
58
               recvd := Append(recvd, j);
59
           el se
               goto Done;
           end if:
62
       end while;
  end process;
```

### ДЕМО

04\_GoChannel.tla

### **Go Channel Bug**

```
05_channelbug_test.go
10 func proc(to time.Duration) int {
     ch := make(chan int)
    go func() {
    // do some processing
       ch <- RESULT
14
     }()
15
     select {
16
    case rslt := <-ch:</pre>
       return rslt
18
    case <-time.After(to):</pre>
19
       return 0
20
```

### **Go Channel Bug**

```
05_channelbug_test.go
10 | func proc(to time.Duration) int {
     ch := make(chan int)
11
     go func() {
    // do some processing
       ch <- RESULT
14
     }()
15
     select {
16
    case rslt := <-ch:
       return rslt
18
     case <-time.After(to):</pre>
19
       return 0
20
```

```
05_ChannelBug.tla
24 | fair process processor = "processor"
25 begin
       Proc_1: send(ch, "result");
26
       Proc 2: wait sent(ch);
28 end process;
29
30 | fair process receiver = "receiver"
31 variable rslt = NULL;
  begin Rec 1:
       either
           recv(rslt, ch);
35
       or
           skip;
36
       end either:
38 end process;
```

### **Go Channel Bug**

```
05_channelbug_test.go
10 func proc(to time.Duration) int {
     ch := make(chan int)
11
     go func() {
     // do some processing
       ch <- RESULT
14
     }()
15
     select {
16
    case rslt := <-ch:
       return rslt
18
     case <-time.After(to):</pre>
19
      return 0
20
22
```

```
05_ChannelBug.tla
24 | fair process processor = "processor"
25 begin
       Proc_1: send(ch, "result");
26
       Proc 2: wait sent(ch);
  end process;
29
30 | fair process receiver = "receiver"
31 variable rslt = NULL;
   begin Rec 1:
       either
           recv(rslt, ch);
34
       or
           skip;
36
       end either;
38 end process;
```

### ДЕМО

05\_ChannelBug.tla

```
Error: Deadlock reached.

Error: The behavior up to this point is:

State 1: <Initial predicate>
/\ ch = [open |-> TRUE, val |-> NULL, sent |-> FALSE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_1", receiver |-> "Rec_1"]
/\ rslt = NULL

State 2: <Proc_1 line 55, col 11 to line 59, col 25 of module 05_ChannelBug>
/\ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_2", receiver |-> "Rec_1"]
/\ rslt = NULL

State 3: <Rec_1 line 69, col 10 to line 78, col 52 of module 05_ChannelBug>
/\ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_2", receiver |-> "Done"]
/\ rslt = NULL

7 states generated, 6 distinct states found, 1 states left on queue.
```

```
Error: Deadlock reached.

Error: The behavior up to this point is:

State 1: <Initial predicate>
/ ch = [open |-> TRUE, val |-> NULL, sent |-> FALSE, rcvd |-> FALSE]
/ pc = [processor |-> "Proc_1", receiver |-> "Rec_1"]
/ rslt = NULL

State 2: <Proc_1 line 55, col 11 to line 59, col 25 of module 05_ChannelBug>
/ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/ pc = [processor |-> "Proc_2", receiver |-> "Rec_1"]
/ rslt = NULL

State 3: <Rec_1 line 69, col 10 to line 78, col 52 of module 05_ChannelBug>
/ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/ pc = [processor |-> "Proc_2", receiver |-> "Done"]
/ rslt = NULL

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Error: The behavior up to this point is:

State 1: <Initial predicate>
/\ ch = [open |-> TRUE, val |-> NULL, sent |-> FALSE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_1", receiver |-> "Rec_1"]
/\ rslt = NULL

State 2: <Proc_1 line 55, col 11 to line 59, col 25 of module 05_ChannelBug>
/\ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_2", receiver |-> "Rec_1"]
/\ rslt = NULL

State 3: <Rec_1 line 69, col 10 to line 78, col 52 of module 05_ChannelBug>
/\ ch = [open |-> TRUE, val |-> "result", sent |-> TRUE, rcvd |-> FALSE]
/\ pc = [processor |-> "Proc_2", receiver |-> "Done"]
/\ rslt = NULL

7 states generated, 6 distinct states found, 1 states left on queue.
```

### **Go Unbound Queue**

```
8 func buffer1(inpCh, outCh chan int) {
                                                 24
     var oC chan int
     var out int
                                                 26
     var buf []int
     defer close(outCh)
                                                 28
     for {
                                                 29
       select {
14
                                                 30
       case inp, ok := <-inpCh:
15
         if !ok {
                                                 32
16
           return
         } else if oC != nil {
18
           buf = append(buf, inp)
19
         } else {
20
           out = inp
           oC = outCh
```

```
case oC <- out:
    if len(buf) > 0 {
      out, buf = headtail(buf)
    } else {
      oC = nil
inp := make(chan int)
out := make(chan int)
go buffer1(inp, out)
```

### **Go Unbound Queue**

```
56 fair process Buffer = "buffer"
  variable k = NULL, buffer = <<>>;
  begin BufferProcess:
    while TRUE do
59
      either
60
         recv(k, inpCh);
       Buffer 1:
         if k /= NULL then
           buffer := Append(buffer, k);
64
         else
           close(outCh);
           goto Done;
         end if;
68
```

```
or
when Len(buffer) > 0;
Buffer_2:
    send(outCh, Head(buffer));
Buffer_3:
    wait_sent(outCh);
    buffer := Tail(buffer);
end either;
end while;
end process;
```

### ДЕМО

06\_UnboundQueue.tla

```
Error: Temporal properties were violated.
Error: The following behavior constitutes a counter-example:
. . .
State 30: <Consume line 138, col 12 to line 146, col 66 of module 06 UnboundQueue>
/\ buffer = <<3>>
/  i = 3
/\ i = NULL
/\ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Consumer 1"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1. 2. 3>>
/ id = 4
State 31: <Consumer 1 line 148. col 15 to line 154. col 72 of module 06 UnboundOueue>
/\ buffer = <<3>>
/\ i = 3
/\ i = NULL
/ \ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Done"]
/\ recvd = <<1, 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/\ id = 4
State 32: Stuttering
406 states generated. 213 distinct states found. 0 states left on queue.
```

```
Error: Temporal properties were violated.
Error: The following behavior constitutes a counter-example:
. . .
State 30: <Consume line 138, col 12 to line 146, col 66 of module 06 UnboundQueue>
/\ buffer = <<3>>
/  i = 3
/\ j = NULL
/\ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Consumer 1"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/ id = 4
State 31: <Consumer 1 line 148. col 15 to line 154. col 72 of module 06 UnboundOueue>
/\ buffer = <<3>>
/\ i = 3
/\ i = NULL
/ \ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Done"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
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```
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/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Consumer 1"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/ id = 4
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/\ buffer = <<3>>
/\ i = 3
/\ i = NULL
/ \ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Done"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/\ id = 4
State 32: Stuttering
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. . .
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/  i = 3
/\ j = NULL
/\ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Consumer 1"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/ id = 4
State 31: <Consumer 1 line 148. col 15 to line 154. col 72 of module 06 UnboundOueue>
/\ buffer = <<3>>
/\ i = 3
/\ i = NULL
/ \ k = NULL
/\ pc = [buffer |-> "Done", producer |-> "Done", consumer |-> "Done"]
/\ recvd = <<1. 2>>
/\ inpCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ outCh = [sent |-> FALSE, open |-> FALSE, val |-> NULL, rcvd |-> FALSE]
/\ sent = <<1, 2, 3>>
/\ id = 4
State 32: Stuttering
406 states generated. 213 distinct states found. 0 states left on queue.
```

### Go Unbound Queue (fixed)

```
fair process Buffer = "buffer"
                                                 68
                                                        or
  variable k = NULL, buffer = <<>>,
                                                          when Len(buffer) > 0;
                                                 69
            exit = FALSE:
                                                        Buffer 2:
56
                                                 70
  begin BufferProcess:
                                                          send(outCh, Head(buffer));
    while TRUF do
                                                        Buffer 3:
58
    either
                                                          wait sent(outCh);
59
         when ~exit;
                                                          buffer := Tail(buffer);
                                                 74
         recv(k, inpCh);
                                                 75
                                                        or
       Buffer 1:
                                                          when Len(buffer) = 0 / \text{exit};
                                                 76
         if k /= NULL then
                                                          close(outCh);
           buffer := Append(buffer, k);
                                                          goto Done;
64
                                                 78
         else
                                                        end either;
                                                 79
           exit := TRUE:
                                                      end while:
         end if:
                                                    end process:
```

### ДЕМО

07\_UnboundQueue.tla

### Go Unbound Queue (fixed)

```
func buffer2(inpCh, outCh chan int) {
     var exit bool
    var oC chan int
    var out int
    var buf []int
    defer close(outCh)
10
    for {
       select {
       case inp, ok := <-inpCh:
         if !ok {
14
           if oC == nil {
             return
           } else {
             exit = true
18
             inpCh = nil
19
20
```

```
} else if oC != nil {
21
           buf = append(buf, inp)
         } else {
23
24
           out = inp
           oC = outCh
25
26
       case oC <- out:
         if len(buf) > 0 {
28
           out, buf = headtail(buf)
29
         } else if !exit {
30
           oC = nil
         } else {
           return
34
36
37
```

## А на практике?

### Amazon Web Services успешно использует TLA<sup>+</sup> Tools<sup>1</sup> с 2011 года

"At AWS, formal methods have been a big success. They have helped us prevent subtle, serious bugs from reaching production, bugs that we would not have found via any other technique."

1. http://lamport.azurewebsites.net/tla/formal-methods-amazon.pdf

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Microsoft использует TLA<sup>+</sup> во многих проектах<sup>2</sup>, включая Azure CosmosDB<sup>3</sup>

"TLA<sup>+</sup> is not yet a prerequisite for our hiring. However, a candidate's knowledge of TLA<sup>+</sup> is given significant weight in our evaluation. To us, it is a great indicator of those who really care about quality and correctness."

- 1. http://lamport.azurewebsites.net/tla/formal-methods-amazon.pdf
- 2. https://www.microsoft.com/en-us/research/search/?q=TLA%2B
- 3. https://docs.microsoft.com/en-us/azure/cosmos-db/consistency-levels

### MongoDB для поиска<sup>1</sup> ошибок в протоколе<sup>2</sup> репликации

"We expect that formally modeling our system upfront could have saved 100s of hours of engineering time."

- 1. conf.tlapl.us/07\_-\_TLAConf19\_-\_William\_Schultz\_-\_Fixing\_a\_MongoDB\_Replication\_Protocol\_Bug\_with\_TLA.pdf
- 2. github.com/will62794/mongo-repl-tla-models

### MongoDB для поиска<sup>1</sup> ошибок в протоколе<sup>2</sup> репликации

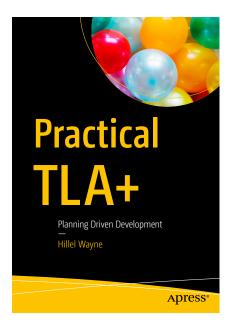
"We expect that formally modeling our system upfront could have saved 100s of hours of engineering time."

# CockroachDB для спецификации<sup>3</sup> протокола распределенных транзакций<sup>4</sup>

"We found that the process of writing this specification gave us more confidence in the Parallel Commit protocol itself and in its integration into CockroachDB."

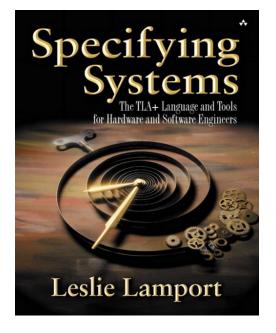
- 1. conf.tlapl.us/07\_-\_TLAConf19\_-\_William\_Schultz\_-\_Fixing\_a\_MongoDB\_Replication\_Protocol\_Bug\_with\_TLA.pdf
- 2. github.com/will62794/mongo-repl-tla-models
- 3. www.cockroachlabs.com/blog/parallel-commits
- 4. github.com/cockroachdb/cockroach/tree/master/docs/tla-plus

### С чего начать?



Practical TLA<sup>+</sup>
Planning Driven Development
by Hillel Wayne
Apress, 2018

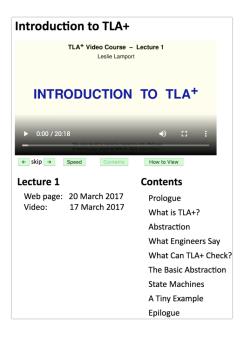
PlusCal, идеально для вхождения в тему!



Specifying Systems
The TLA<sup>+</sup> Language and Tools for
Hardware and Software Engineers
by Leslie Lamport
Addison-Wesley Professional, 2002

TLA⁺, обязательно иметь под рукой

(последняя версия всегда доступна на сайте Лэмпорта, бесплатно!)



### TLA<sup>+</sup> Video Course

by Leslie Lamport

lamport.azurewebsites.net/video/videos.html

3.5 часа TLA⁺, обязательно посмотреть

(Лэмпорт прекрасный лектор!)

Lamport TLA<sup>+</sup> Site: https://lamport.azurewebsites.net/tla/tla.html

Learn TLA<sup>+</sup>: https://learntla.com

google group: https://groups.google.com/forum/#!forum/tlaplus

reddit: https://www.reddit.com/r/tlaplus/

TLA<sup>+</sup> Tools: https://github.com/tlaplus/tlaplus

# Спасибо!





(Презентация и код: github.com/growler/gophercon-russia-2020-talk)