## Aufgabe 5.1

```
ENTITY xnor IS
  PORT (x,y : IN bit; z : OUT bit)
END xnor
ARCHITECTURE behavior OF xnor IS
  CONSTANT tdhl : TIME := 25ps;
  CONSTANT tdlh : TIME := 45ps;
BEGIN
  PROCESS(x,y)
BEGIN
  IF x=y
    THEN z <= '1' AFTER tdlh;
  ELSE z <= '0' AFTER tdhl;
END IF
END PROCESS
END behavior</pre>
```

## Aufgabe 5.2

```
\overline{a} \vee by
\overline{(\overline{(a} \vee by))}
\overline{((a\overline{b} \vee \overline{y}))}
\overline{(\overline{a} nor\overline{b} nory)}
\overline{a} nor\overline{b} nory nor\overline{a}\overline{b} nory
(anora) nor((bnory) nor(bnory)) nor((anora) nor((bnory) nor(bnory))
```