

x.(4) len make. frekarer W alson.

for
$$T_N = \frac{T_S}{N}$$
 (ornelide $T_N = T_3 = \frac{T_S}{3}$)

TN: TDM is orthe dorbeter ovar, washin

fu= 1 : TDM Fareton frehansi you do hirt

TDM isouth band gently By alson.

$$f_N = 2B\tau$$
 almele. $\Rightarrow B\tau = \frac{f_N}{2} = \frac{1}{2T_N}$

Eger isauettern band gentslikten and degitse, 1= 1/2, N x_i (t) $\rightarrow W_i$ Winder = naké Wi fs } 2 Winder FN=Nfs > 2N Winder Örnek: N=3 x,(k), x,(k), x3(k) Wa 2Wa (Ts < 1/4) fsz Lwo 21(4) Her jost hend BT= 1 = 4No Nyquet frehansonder Frame rate

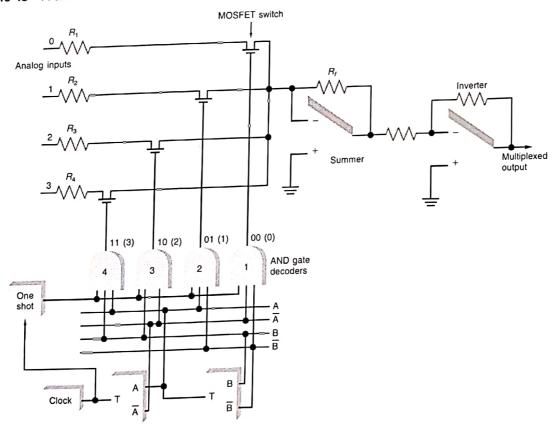
One complete revolution of the commutator switch is referred to as a *frame*. In other words, during one frame, each input channel is sampled one time. The number of contacts on the multiplexed switch or commutator sets the number of samples per frame. The number of frames completed in 1 s is called the *frame rate*. Multiplying the number of samples per frame by the frame rate yields the commutation rate or multiplex rate, which is the basic frequency of the composite signal, the final multiplexed signal that is transmitted over the communication channel.

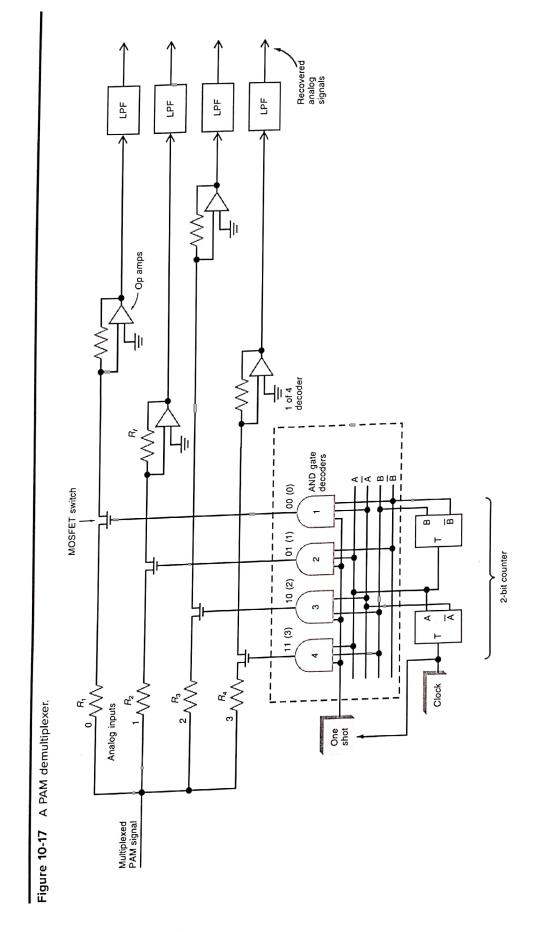
In Fig. 10-14, the number of samples per frame is 4. Assume that the frame rate is 100 frames per second. The period for one frame, therefore, is 1/100 = 0.01 s = 10 ms. During that 10-ms frame period, each of the four channels is sampled once. Assuming equal sample durations, each channel is thus allotted 10/4 = 2.5 ms. (As indicated earlier, the full 2.5-ms period would not be used. The sample duration during that interval might be, e.g., only 1 ms). Since there are four samples taken per frame, the commutation rate is 4×100 or 400 pulses per second.

Electronic Multiplexers. In practical TDM/PAM systems, electronic circuits are used instead of mechanical switches or commutators. The multiplexer itself is usually implemented with FETs, which are nearly ideal on/off switches and can turn off and on at very high speeds. A complete four-channel TDM/PAM circuit is illustrated in Fig. 10-15.

The multiplexer is an op amp summer circuit with MOSFETs on each input resistor. When the MOSFET is conducting, it has a very low on resistance and therefore acts as a closed switch. When the transistor is off, no current flows through it, and it therefore acts as an open switch. A digital pulse applied to the gate of the MOSFET turns the transistor on. The absence of a pulse means that the transistor is off. The control pulses to the MOSFET switches are such that only one MOSFET is turned on at a time. These MOSFETs are turned on in sequence by the digital circuitry illustrated.

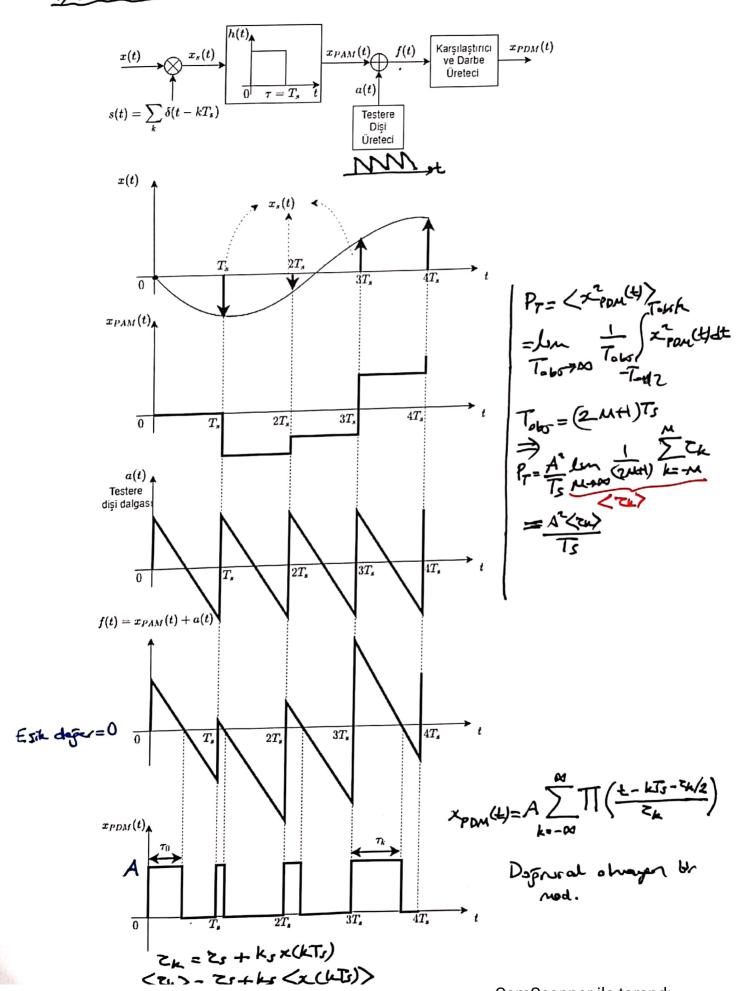
Figure 10-15 A time-division multiplexer used to produce pulse-amplitude modulation.





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1) Darbe Süresi Modülaspon (Pulse Dundon Mad, pom) = PWM, PLM



2) Darbe Yeri Madalagero (Pulse Pasithan Mada, PPM)

