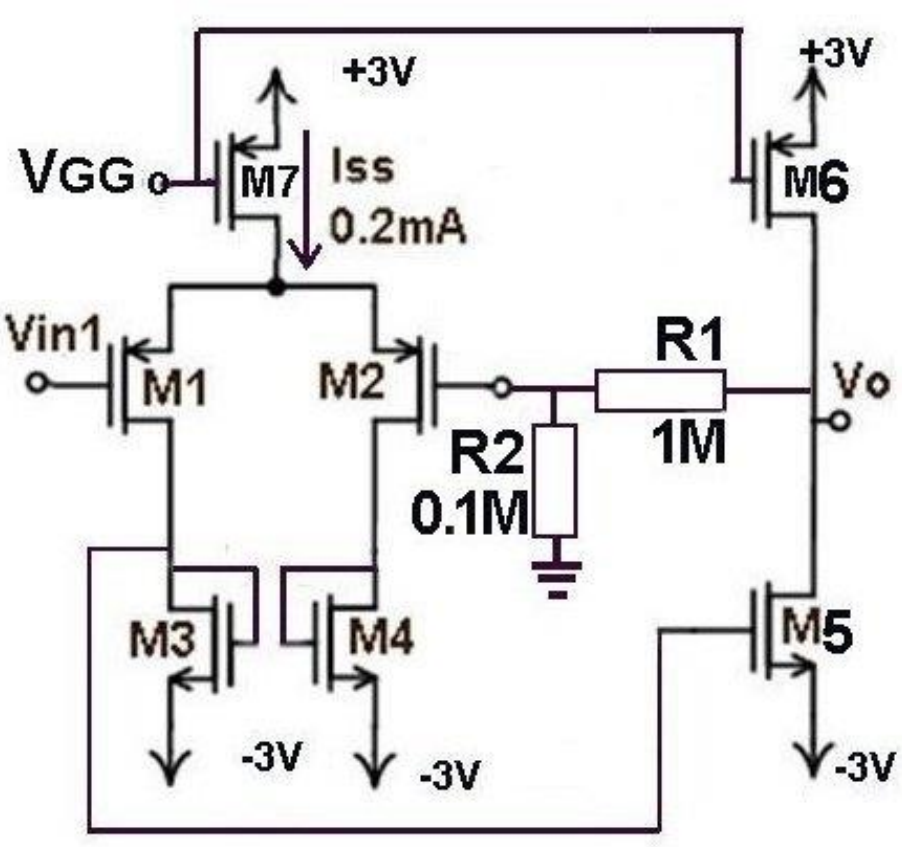
Analog Electronic Circuits **Summer-2020 Midterm-3** **27.08.2020**

NAME: **NO:**



For the transistors in the figure V_{DD}=3.3V, C_{ox} =50 fF/(μm)^2, μ_n = 0.06 m^2.V^{-1}.s^{-1}, μ_p= 0.02 m^2.V^{-1}.s^{-1}, V_{THN} = 0.5V, V_{THP} = -0.5V, V_{AN}=V_{AP}=100V , $(W/L)_1$= $(W/L)_2$=12 , $(W/L)_3$=$(W/L)_4$=2, $(W/L)_5$=4, $(W/L)_6$=$(W/L)_7$=12 and (for all the transistors) L=2μm are given.

VG1=VG2=0 for DC Case.

Cgs=W.L.Cox **Cgd=Cgs / 3**

Find the **Phase Margin** of the circuit.