

For all of the transistors in the figure, $\beta=2\text{mA/V}^2$, $|V_{Th}|=1\text{V}$ and $V_A=100\text{V}$ are given.

- Find R and V_{GG} .
- A diode-connected PMOS transistor will be used in place of R . Find β_p value of the PMOS transistor.
- Obtain V_{GG} by using diode-connected NMOS transistors (2 transistors). Draw the structure. Find β_n values of the transistors.
- Find the differential gain ($A_d = v_{out}/[v_{in1} - v_{in2}]$).
- Find CMRR of the whole circuit. (**Note that CMRR is not ∞**)

