The circuit whose state diagram is shown in Figure 1 will be designed using D-type flip-flops and AND (VE), OR (VEYA), NOT (TÜMLEME). (Q = D)

1) What kind of state machine is the circuit? Why?
2) Draw the state transition table of the circuit using the codes shown above the states.
3) Find the reduced Boolean functions for flip-flop inputs and outputs.
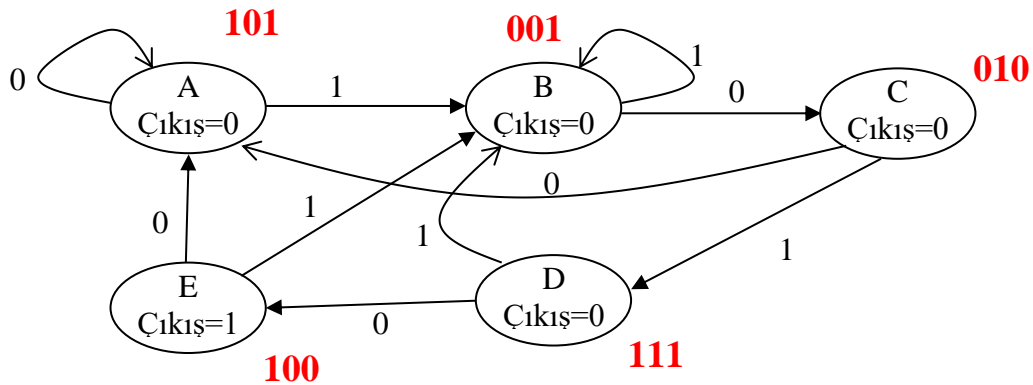

Figure 1