



For the transistors in the figure  $V_{DD}=3.3V$ ,  $C_{ox}=50 \text{ fF}/(\mu\text{m})^2$ ,  $\mu_n = 0.06 \text{ m}^2.\text{V}^{-1}.\text{s}^{-1}$ ,  $\mu_p = 0.02 \text{ m}^2.\text{V}^{-1}.\text{s}^{-1}$ ,  $V_{THN} = 0.5V$ ,  $V_{THP} = -0.5V$ ,  $(W/L)_1=2$ ,  $(W/L)_2=20$ ,  $(W/L)_3=10$ ,  $(W/L)_4=1$  and (for all the transistors)  $L=2\mu\text{m}$  are given.

**Note:**  $c_{gs} \approx W.L.C_{ox}$ ,  $c_{gd} \approx c_{gs}/3$

- DC value at the input is 1.65V. What should VGG be for  $V_o=1.65V$ ?
- (Part a is not necessary) Draw magnitude of  $V_o/V_i$  versus frequency. Find the cut-off frequency.
- (Part a is not necessary) Draw phase of  $V_o/V_i$  versus frequency.