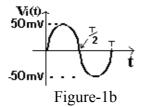
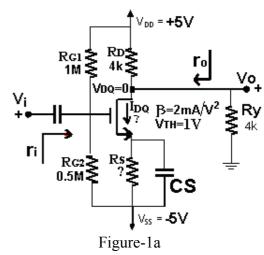
\*

Q3- Figure-1a shows an amplifier circuit employing a MOSFET. For the MOSFET,  $\beta$ =2mA/V<sup>2</sup> and V<sub>TH</sub>=2V are given.

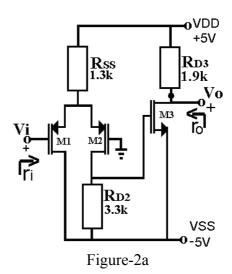
- a)  $V_{DQ}$  is required to be 0V at the operating point. Find  $I_{DQ}$  and  $R_S$ . (10Points)
- b) Find the DC power dissipated on the MOSFET. (10Points)
- c) Find ac input and output resistances of the circuit (ri and ro). (10Points)
- d) Draw the ac output voltage vo(t) for the ac input voltage vi(t) given in Figure-1b. (10Points)





Q4- For the MOS transistors given in Figure-2a,  $\beta_1 = \beta_2 = \beta_3 = 1 \text{mA/V}^2$ ,  $V_{TH1} = V_{TH2} = -1 \text{V}$  and  $V_{TH3} = 1 \text{V}$  are given.

- a) Find drain currents of the transistors for Vi=0V. (10Points)
- b) Find ac gain (vo/vi) of the circuit. (10Points)



c) A PMOS transistor will be used in place of  $R_{SS}$  (Figure-2b). DC values of the circuit (the values found in a) are required not to change. Find the gate bias voltage ( $V_{GB}$ ) of the transistor ( $\beta_4$ =2mA/ $V^2$ ,  $V_{TH4}$ = -1V and  $V_{A4}$ =100V). (10Points)

d) Find the ac gain for the new case. (10Points)

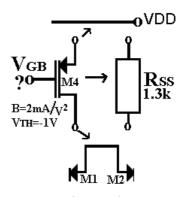


Figure-2b

b) 
$$V_{00} = 0V$$
  $V_{50} = -4.18V \Rightarrow V_{05} = 4.18V$   
 $P_{TT} = T_{0} \cdot V_{05} = 1.25 \text{mA} \cdot 4.18V \approx 6 \text{mW}$   
 $C)$   $C = R_{01} / R_{02} = 1 \text{m} / 0.5 \text{m} \approx 3.33 \text{k}$   
 $C = R_{01} / R_{02} = 1 \text{m} / 0.5 \text{m} \approx 3.33 \text{k}$   
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 $C = R_{01} / R_{$ 

C-4-a) M1 is in the saturation mode, since its drain is at the lowest potential. M2 is probably in the softwarthon mode. Thus, IDI = IDZ (Probably)

250 | TRSS |  $V_{GSI} = V_{GI} - V_{SI}$ 00 - ( $V_{DD} - 2$  ID. RSS)  $V_{GSI} = V_{GI} - V_{SI}$   $V_{GSI} = V_{GI} - V_{SI}$  $\frac{1 \times A/V^{2}}{2} \left( \frac{-5 + 2 J_{0} \cdot R_{SS}}{V_{GS}} - (-1) \right)^{2}$   $\frac{1}{V_{GS}} = \frac{1}{2} \left( \frac{16 - 20,8 K. I_{0}}{10^{3}} + \frac{6,76 M. I_{0}}{10^{6}} \right)$   $\Delta = 21,8 L, \quad I_{01,2} = \frac{-5 + VA}{2a} = \frac{11,4 + \sqrt{21,8L}}{2 \times 3,38 k}$ Inthe VSICVGI VGS>VT Jaturation mode Cut-off mode

M3 is probably in the sorturation mode. V63 = VRD2 + VSS = In Roz + Vss  $V_{G(3)} = V_{G3} - V_{S3} = -1.7 + 5 = 3.3V$ > Jo1 = 12 (V652 - VTh)2 1m (3,3-1)2=2,65mA (hech then)  $V_{603} = V_{60} - V_{703} = V_{70} - V_{703} = 0$ (chech the condition)  $V_{603} = V_{63} - V_{703} = -1.7 - 0 = -1.7$ 1 V603 & VTh3

-1,7 < 1 => M3 is in the

Jointwratton mode.

501 = Par = 1,61ms

Jon = 2,65 m A In = Pm2 = 1/4/ms 9M7 = VIATOR = 2,3MS Viewer Parte Source Common- Common- Common- Follower Parte Source

$$\frac{V_{d3}}{V_{f3}} = \frac{-213m \cdot 1.9k}{1+0}$$

$$= -4.137$$

$$\frac{V_{23}}{V_{93}} = \frac{-\beta_{m1} \cdot R_{23}}{1 + \beta_{m3} \cdot R_{53}}$$

$$\frac{V_{23}}{V_{93}} = \frac{-2.3m. \, 1.9k}{1 + 0}$$

$$\frac{V_{23}}{V_{93}} = \frac{-2.3m. \, 1.9k}{1 + 0}$$

$$\frac{V_{23}}{V_{23}} = \frac{-4.177}{1 + 0}$$

$$\frac{V_0}{V_i} = 0.39 \times 4.65 \times (-4.17) = -7.9$$

This transistor is not a amplification transistor. It is used as a DC current source the output resistance of which is not infinite... My should be in the faturation mode. ID4 = 12 (V654 - VTh4) V654 = - V 2 Jog + VTh4 = -2,41V V654 = V64 - V54 = V6A - V00 = -2,41V V615=-2,41+400 V61 = 2,59 V Only Vs1 changes, the others does not change.  $\frac{V_{60}}{V_{60}} = \frac{P_{m1} R_{51}}{1 + P_{m1} R_{51}} = \frac{P_{64} | \Gamma_{12}}{1 + P_{m1} R_{51}} = \frac{V_{64}}{1 + Q_{64}} = \frac{$ √S1 = 1/4/m. 0,7/k = 0,5

~= 0,5 × 4,65 × (-4,17)=-10/