

$k_p' = \mu_p C_{ox} = 40 \mu A/V^2$ ,  $k_n' = \mu_n C_{ox} = 80 \mu A/V^2$ ,  $V_{An} = V_{Ap} = 40V$ ,  $V_{Th,p} = -0.8V$ ,  $V_{Th,n} = 0.6V$  are given. Aspect ratio values of the transistors can be obtained from the table given below.  
(***Vin1=Vin2=0 for DC case***)

- [illegible]

	L(um)	W(um)
M1	0.7	28
M2	0.7	28
M3	0.7	7
M4	0.7	7
M5	0.7	14
M6	0.7	14
M7	0.7	14
M8	0.7	14
M9	0.7	14