

NAME:

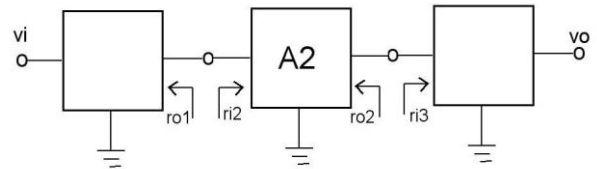
Number:

P1- Explain the trans-resistance amplifier.

P2- For the circuit in the figure

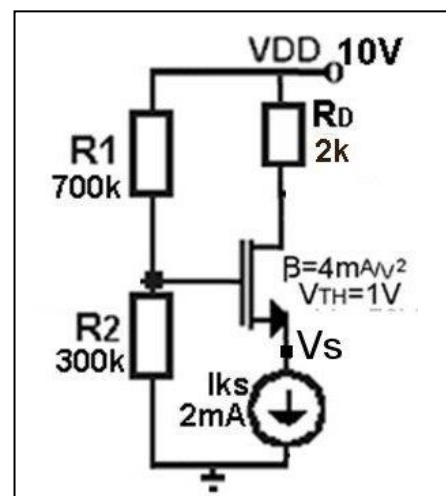
$$r_{i2} \ll r_{o1} \text{ and } r_{o2} \gg r_{i3}$$

are given. What is the appropriate amplifier type of A2?  
(voltage, current, trans-conductance, trans-resistance)



P3- Give the model of the ideal transistor.(10p)

P4- For the transistor in the figure,  $\beta = 4\text{mA/V}^2$ ,  $V_{TH} = 1\text{V}$  and  $V_A = 80\text{V}$  are given. **Find  $V_s$ .**  
(You can ignore  $r_{ds}$ )

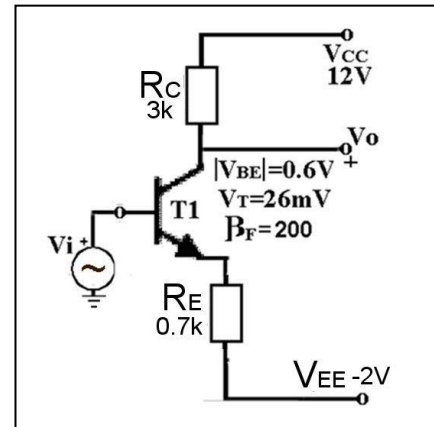


Answer-4:  $V_s =$

P5- Give the ac model of the MOS transistor in P4 (do not ignore  $r_{ds}$ ).

P6-The figure shows an amplifier circuit employing a BJT. For the BJT,  $\beta_F=200$  and  $V_{BE}=0.6V$  are given.  $V_T \approx 25mV$ .

a) Find the collector current of the transistor ( $I_{CQ}$ ). Does  $I_{CQ}$  provide the maximum amplitude value for sinusoidal case? ( $V_{CEsat}=0$ )



Answer-6a:  $I_c =$

b) Find the voltage-gain ( $v_o/v_i$ ) of the circuit.

Answer-6b:  $A_v =$

c) Find the trans-resistance gain ( $v_o/i_i$ ) of the circuit.

Answer-6c:  $R_m =$