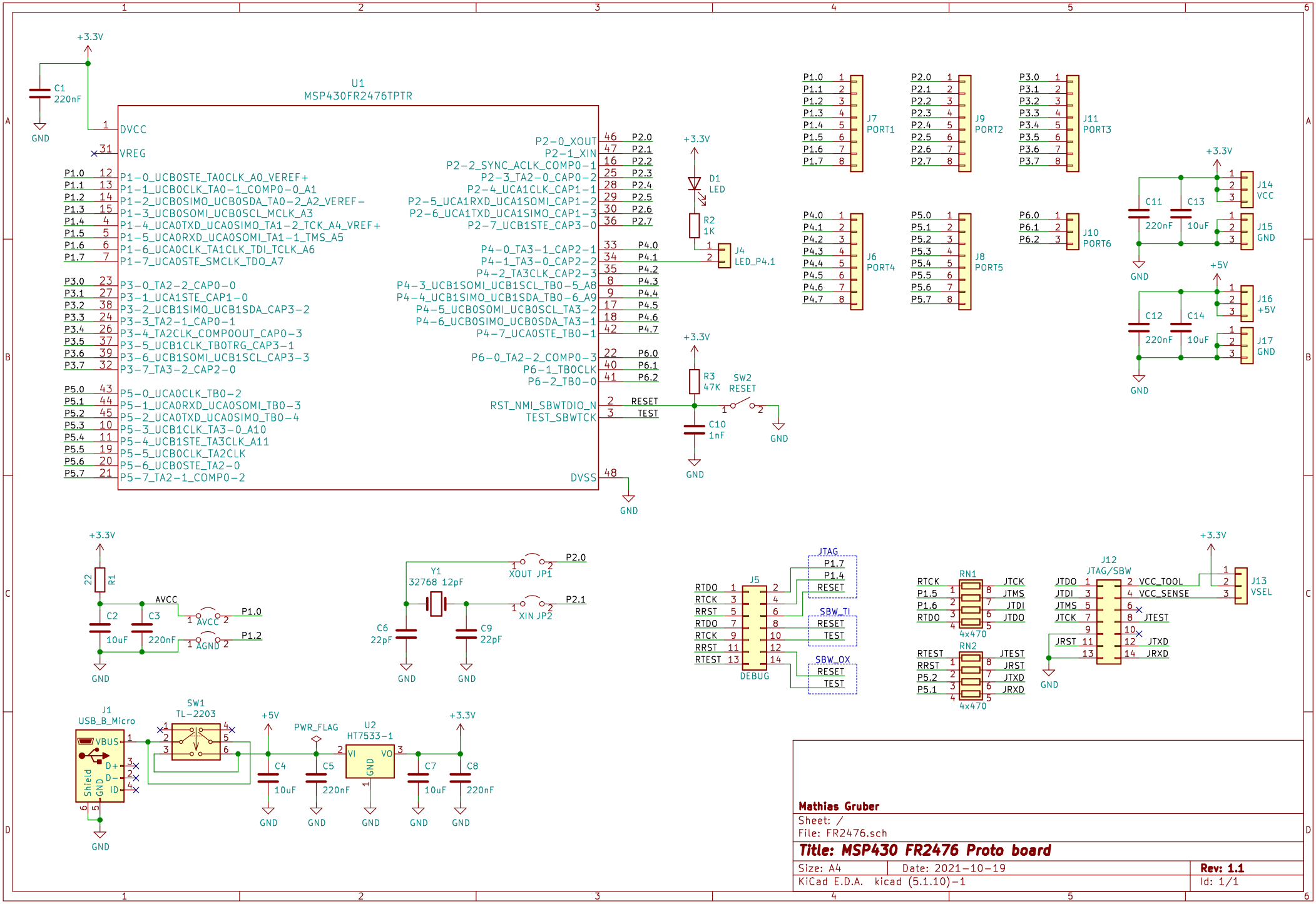
U1
MSP430FR2476TPTR
DVCC
VREG
P1-0_UCB0STE_TA0CLK_A0_VEREF+
P1-1_UCB0CLK_TA0-1_COMP0-0_A1
P1-2_UCB0SIMO_UCB0SDA_TA0-2_A2_VEREF-
P1-3_UCB0SOMI_UCB0SCL_MCLK_A3
P1-4_UCA0TXD_UCA0SIMO_TA1-2_TCK_A4_VREF+
P1-5_UCA0RXD_UCA0SOMI_TA1-1_TMS_A5
P1-6_UCA0CLK_TA1CLK_TDI_TCLK_A6
P1-7_UCA0STE_SMCLK_TDO_A7
P3-0_TA2-2_CAP0-0
P3-1_UCA1STE_CAP1-0
P3-2_UCB1SIMO_UCB1SDA_CAP3-2
P3-3_TA2-1_CAP0-1
P3-4_TA2CLK_COMP0OUT_CAP0-3
P3-5_UCB1CLK_TB0TRG_CAP3-1
P3-6_UCB1SOMI_UCB1SCL_CAP3-3
P3-7_TA3-2_CAP2-0
P5-0_UCA0CLK_TB0-2
P5-1_UCA0RXD_UCA0SOMI_TB0-3
P5-2_UCA0TXD_UCA0SIMO_TB0-4
P5-3_UCB1CLK_TA3-0_A10
P5-4_UCB1STE_TA3CLK_A11
P5-5_UCB0CLK_TA2CLK
P5-6_UCB0STE_TA2-0
P5-7_TA2-1_COMP0-2
P2-0_XOUT
P2-1_XIN
P2-2_SYNC_ACLK_COMP0-1
P2-3_TA2-0_CAP0-2
P2-4_UCA1CLK_CAP1-1
P2-5_UCA1RXD_UCA1SOMI_CAP1-2
P2-6_UCA1TXD_UCA1SIMO_CAP1-3
P2-7_UCB1STE_CAP3-0
P4-0_TA3-1_CAP2-1
P4-1_TA3-0_CAP2-2
P4-2_TA3CLK_CAP2-3
P4-3_UCB1SOMI_UCB1SCL_TB0-5_A8
P4-4_UCB1SIMO_UCB1SDA_TB0-6_A9
P4-5_UCB0SOMI_UCB0SCL_TA3-2
P4-6_UCB0SIMO_UCB0SDA_TA3-1
P4-7_UCA0STE_TB0-1
P6-0_TA2-2_COMP0-3
P6-1_TB0CLK
P6-2_TB0-0
RST_NMI_SBWTDIO_N
TEST_SBWTCK
DVSS
C1 220nF
D1 LED
R2 1K
J4 LED_P4.1
R3 47K
SW2 RESET
C10 1nF
J7 PORT1
J9 PORT2
J11 PORT3
J6 PORT4
J8 PORT5
J10 PORT6
J14 VCC
J15 GND
J16 +5V
J17 GND
C11 220nF
C13 10uF
C12 220nF
C14 10uF
R1 22
C2 10uF
C3 220nF
AVCC
AGND
Y1 32768 12pF
XOUT JP1
XIN JP2
C6 22pF
C9 22pF
J5 DEBUG
JTAG
SBW_TI
SBW_OX
RN1 4x470
RN2 4x470
J12 JTAG/SBW
VCC_TOOL
VCC_SENSE
J13 VSEL
J1 USB_B_Micro
SW1 TL-2203
PWR_FLAG
U2 HT7533-1
C4 10uF
C5 220nF
C7 10uF
C8 220nF
Mathias Gruber
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Title: MSP430 FR2476 Proto board
Size: A4
Date: 2021-10-19
Rev: 1.1
KiCad E.D.A. kicad (5.1.10)-1
Id: 1/1