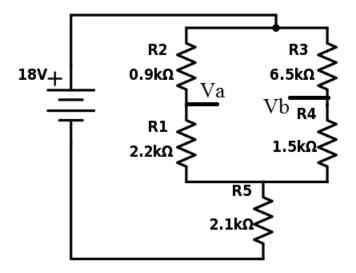
Voltage Differences Review

Grounds and Commons

In class, we discussed how to find voltages at nodes in a circuit, with and without a ground. For the circuit shown below find:

- 1. Va
- 2. Vb
- 3. $\Delta Vab = (Va-Vb)$.



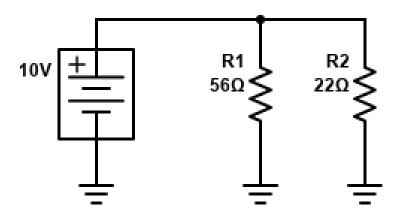
Voltage Differences Review

Grounds and Commons

Now that you understand how to analyze circuits without a ground or a common in them, MOST circuits with a ground or a common will be easy to analyze.

Often, the ground is connected to the negative terminal of your DC voltage source. In these cases, there is absolutely no change in the way you analyze the circuit.

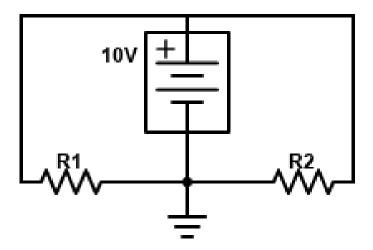
If you see multiple grounds or commons in a schematic diagram. Treat the grounds as if they are one node. If one were to connect all of the grounds in the diagram below with a conducting wire, erase two of the grounds, and leave one ground in the diagram (it doesn't matter which one) one would end up with an equivalent circuit. Try it out and see if you can analyze the circuit below.



Voltage Differences Review

Grounds and Commons

Again, in many circuits the ground is connected to the negative terminal of the battery and you can just ignore it while analyzing the circuit. If I gave you the resistor values in the circuit below, it should be very straight forward. As a challenge, lets try giving you the total current coming out of the battery; 150 mA and the current going through R1; 100ma and ask you to find the value of R1 and R2. Work hard to see if you can figure this out!



Voltage Differences Review

Grounds and Commons

We've also talked about what to do if a ground is attached to a point in the circuit that is not the negative terminal of a battery. If two batteries are in series, you can simply add their voltages. Analyze the circuit below as if there were no ground. Once you've got all of your currents and voltages, reintroduce the ground and assume the voltage at the node it is attached to is 0V. Find the voltages at the rest of the nodes from there.

Find the voltage at every node for the circuit below.

