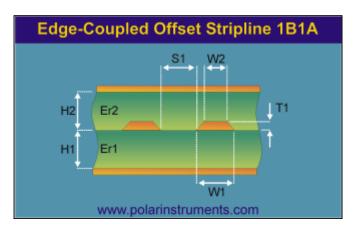
Polar Si9000 PCB Transmission Line Field Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>	
Substrate 1 Height	H1	0.3200	+/-	0.0000	0.3200	0.3200	
Substrate 1 Dielectric	Er1	4.4000	+/-	0.0000	4.4000	4.4000	
Substrate 2 Height	H2	0.3500	+/-	0.0000	0.3500	0.3500	
Substrate 2 Dielectric	Er2	4.4000	+/-	0.0000	4.4000	4.4000	
Lower Trace Width	W1	0.2500	+/-	0.0000	0.2500	0.2500	
Upper Trace Width	W2	0.2500	+/-	0.0000	0.2500	0.2500	
Trace Separation	S1	0.2500	+/-	0.0000	0.2500	0.2500	
Trace Thickness	T1	0.0300	+/-	0.0000	0.0300	0.0300	
_							
Differential Impedance	Zdiff	85.87			85.87	85.87	
Delay (Odd Mode) (ps/m)	D	6996.899			6996.899	6996.899	
Odd Mode Impedance	Zodd	42.93			42.93	42.93	
Even Mode Impedance	Zeven	56.28			56.28	56.28	
Common Mode Impedance	Zcommon	28.14			28.14	28.14	

Notes: (First 5 lines will print)

Add your comments here

