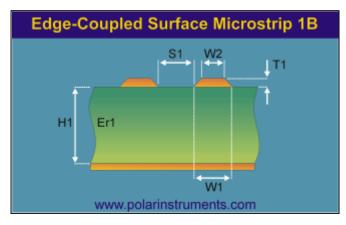
## Polar Si9000 PCB Transmission Line Field Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	8.5000	+/-	0.0000	8.5000	8.5000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	6.0000	+/-	0.0000	6.0000	6.0000
Upper Trace Width	W2	6.0000	+/-	0.0000	6.0000	6.0000
Trace Separation	S1	5.5000	+/-	0.0000	5.5000	5.5000
Trace Thickness	T1	1.8000	+/-	0.0000	1.8000	1.8000
_						
Differential Impedance	Zdiff	110.31			110.31	110.31
Delay (Odd Mode) (ps/in)	D	129.783			129.783	129.783
Odd Mode Impedance	Zodd	55.16			55.16	55.16
Even Mode Impedance	Zeven	95.91			95.91	95.91
Common Mode Impedance	Zcommon	47.95			47.95	47.95

Notes: (First 5 lines will print)

Add your comments here

