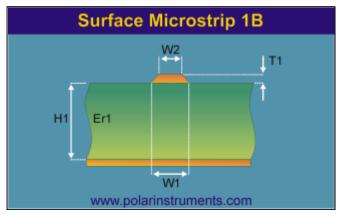
## Polar Si9000 PCB Transmission Line Field Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	0.0085	+/-	0.0000	0.0085	0.0085
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	0.0070	+/-	0.0000	0.0070	0.0070
Upper Trace Width	W2	0.0060	+/-	0.0000	0.0060	0.0060
Trace Thickness	T1	0.0012	+/-	0.0000	0.0012	0.0012
_						
Impedance	Zo	75.18			75.18	75.18
Delay (ps/in)	D	144.114			144.114	144.114
Inductance (nH/in)	L	10.834			10.834	10.834
Capacitance (pF/in)	С	1.917			1.917	1.917
_						

Notes: (First 5 lines will print)

Add your comments here

