



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	13.0000	+/-	0.0000	13.0000	13.0000
Substrate 1 Dielectric	Er1	4.2000	+/-	0.0000	4.2000	4.2000
Lower Trace Width	W1	9.7000	+/-	0.0000	9.7000	9.7000
Upper Trace Width	W2	9.7000	+/-	0.0000	9.7000	9.7000
Trace Separation	S1	6.3000	+/-	0.0000	6.3000	6.3000
Trace Thickness	T1	2.3500	+/-	0.0000	2.3500	2.3500
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Differential Impedance	Zdiff	101.71	-----		101.71	101.71
Delay (Odd Mode) (ps/in)	D	129.614	-----		129.614	129.614
Odd Mode Impedance	Zodd	50.86	-----		50.86	50.86
Even Mode Impedance	Zeven	97.38	-----		97.38	97.38
Common Mode Impedance	Zcommon	48.69	-----		48.69	48.69

Notes: (First 5 lines will print)

[1] 吴川斌的博客. PCB叠层设计及阻抗计算[M].
第2.1节