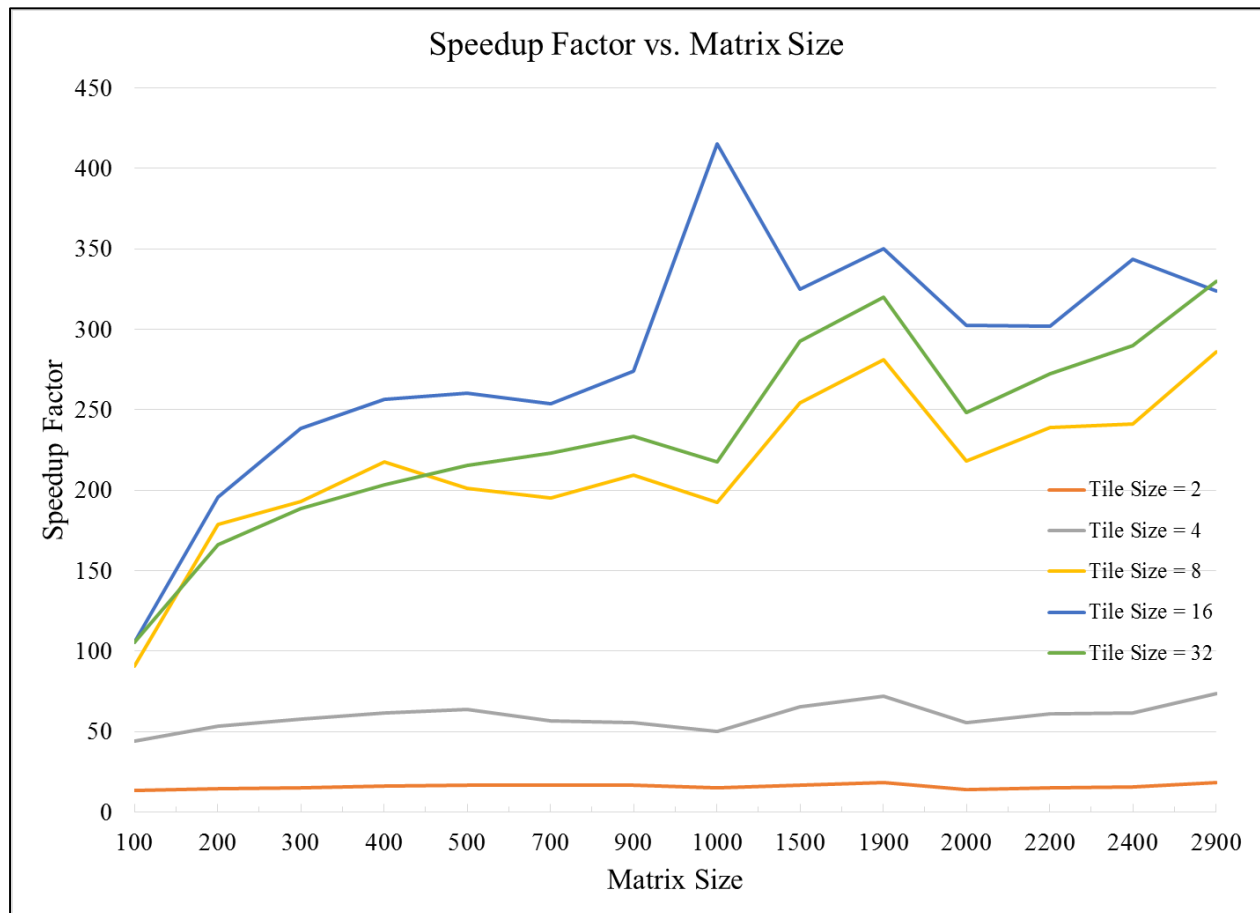


Gaurav Sheni

CSC 391

Graph

September 30, 2015



Analysis

According to this data, the tile optimal size, is 16 for this CPU and GPU implementation of matrix multiplication. Throughout most of the data seen, the tile size of 16 performed the best with the highest speedup factor.

Note, the data point at tile size 16, matrix size 1000 is not a mistake. Multiple checks were done to confirm that it was this value.