

Signal Tap Logic Analyzer - /home/gshimabuko/Documents/Estudo/Coursera/Expanded\_Nios\_Training/Traffic\_Lights/DE0\_NANO/traffic\_lights - traffic\_lights - [stp1.stp]\*

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Instance Manager: Ready to acquire

Instance	Status	Enabled	LEs: 1183	Memory: 8704	Small: 0/0	Medium: 55/66	Large: 0/0
auto_sigtaltap_0	Not running	<input checked="" type="checkbox"/>	1183 cells	8704 bits	0 blocks	2 blocks	0 blocks

JTAG Chain Configuration: JTAG ready

Hardware: USB-Blaster [2-1] Setup...

Device: @1: 10CL025(Y[Z]/EP3C Scan Chain

<< SOF Manager: s/traffic\_lights.sof ...

Attached SOF F auto\_sigtaltap\_0

log: Trig @ 2020/05/07 19:32:00 (0:0:2.4 elapsed) #1

Type	Alias	Name	
		□ HEX0[7..0]	7Fh
		□ HEX1[7..0]	9Ch
		□ HEX2[7..0]	7Fh
		□ HEX3[7..0]	9Ch
		LEDR[0]	0
		LEDR[1]	0
		LEDR[2]	1
		LEDR[3]	1
		LEDR[4]	0
		LEDR[5]	0
		LEDR[6]	0
		LEDR[7]	0
		LEDR[8]	1
		LEDR[9]	1
		LEDR[10]	0
		LEDR[11]	0
		□ sevseg:inst2 Input[3..0]	1h
		□ sevseg:inst1 Input[3..0]	
		□ HEX4[7..0]	F9h
		□ HEX5[7..0]	

Data Setup

Hierarchy Display: traffic\_lights sevseg:inst2 sevseg:inst1

Data Log: auto\_sigtaltap\_0

signal\_set: 2020/05/06 19:33:32 #0

trigger: 2020/05/06 19:33:32 #1

log: Trig @ 2020/05/06 19:39:42 (0:0:11.6 elapsed)

0% 00:00:00