

SHIMABUKO Guilherme

Digital Design and
Verification Engineer



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Subject: Application for the Digital Design and Verification Engineer job opening

Recently graduated as an Electrical Engineer, I have a strong interest in Electronics and Computer Systems and I take a special interest in Digital Electronics, which is why I was elated to hear of this opportunity. I am positive that my experiences as an undergraduate intern and now as a research assistant will meet your expectations for this post.

During my time at the university, I looked for every opportunity to gain more knowledge and experience in both management and technical fields. As a member of the Junior Enterprise, I had the chance to learn a lot about project management and team work, skills that I carried into every post I occupied since. The Junior Enterprise was also where I received a referral for my first R&D internship at a University Laboratory to develop electronic prototypes for Technology and Arts exhibits. While the main focus of my advisor at this job was purely artistic, this project grew into a larger system, which later became my graduation dissertation. This first internship turned into other opportunities at two other laboratories, as well as an internship at a small Integrated Circuits Design firm, where I learned the foundation of Digital Integrated Circuits and FPGA design that allowed me to get to where I am today.