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**Erweiterung einer Messschaltung zur Erfassung
von höhenstrahlungsbedingten Ausfällen
von Gallium-Nitrid-Halbleitern**

Masterarbeit

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Ich erkläre hiermit, dass ich die vorliegende Arbeit selbstständig angefertigt und keine anderen als die angegebenen Quellen und Hilfsmittel verwendet habe.

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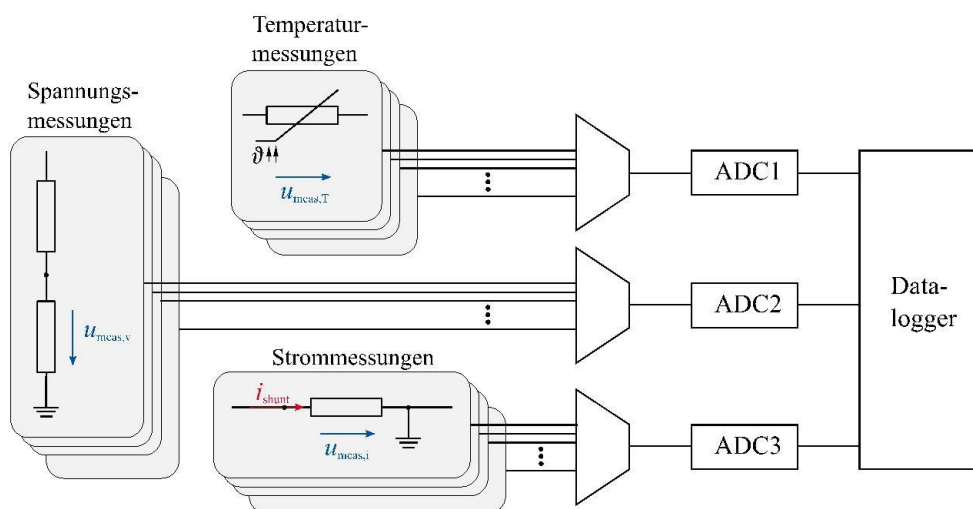


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Erweiterung einer Messschaltung zur Erfassung von höhenstrahlungsbedingten Ausfällen von Gallium-Nitrid-Halbleitern

Halbleiterbauelemente, die in Einsatzgebieten mit hoher kosmischer Höhenstrahlung betrieben werden, können durch sogenannte Single Event Effects (SEE) unvorhersehbar versagen. Im Rahmen dieser Bachelor-Arbeit soll bei der Entwicklung eines Aufbaus für Langzeitmessungen von Auswirkungen der Höhenstrahlung auf Gallium-Nitrid-Leistungshalbleiter unterstützt werden. Hierbei soll eine große Anzahl an Testobjekten für einen Zeitraum von einem Jahr unter erhöhter Strahlung betrieben werden. Zur Auswertung ist die kontinuierliche Erfassung vieler Messgrößen erforderlich. Es sind Spannungs-, Strom- und Temperaturmessungen vorgesehen, sowie Eingänge für zusätzliche Daten. Die Erfassung soll über Daten-Logger an einen Computer übertragen werden. Ziel ist es, die Anzahl der notwendigen ADCs gering zu halten. Dies kann beispielsweise mit Multiplexern erfolgen, wie in der untenstehenden Abbildung gezeigt.



In dieser Arbeit soll ein vollständiges Messsystem entworfen werden. Dazu gehören:

1. Einarbeitung in die Erfassung von plötzlichen Ausfällen durch SEE
2. Bestimmung der notwendigen Messgrößen
3. Auswahl und Auslegung der Spannungs-, Strom- und Temperaturmessungen
4. Untersuchung und Auswahl von Methoden zur Zusammenfassung mehrerer analoger Signale
5. Entwurf, Aufbau und Inbetriebnahme des vollständigen Messsystems
6. Dokumentation in Form einer schriftlichen Arbeit

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Formelzeichenkonvention

a	reelle Größe, Momentanwert
A	Effektivwert
\hat{A}	Amplitude
\mathbf{a}, \mathbf{A}	Matrix
$\mathbf{a}, \boldsymbol{\alpha}$	komplexer Momentanwert
\vec{a}, \vec{A}	vektorielle Größe
$\vec{a}, \vec{\alpha}$	Raumzeiger, Raumvektor
\underline{A}	Zeitzeiger, komplexe Darstellung einer Wechselgröße
$ a $	Betrag einer reellen Größe
$ \underline{A} $	Betrag einer komplexen Größe
$\Re\{\underline{A}\}$	Realteil einer komplexen Größe
$\Im\{\underline{A}\}$	Imaginärteil einer komplexen Größe

1 Introduction

1.1 Importance of measurement

This article is dedicated to studying the decay of gallium nitride components caused by cosmic rays and measuring the amount of gallium nitride affected by cosmic rays. And study the cause of damage to the gallium nitride transistor - whether it is related to temperature. This article measures the voltage signal and temperature signal to meet the above requirements.

1.2 Introduction to various data measurement methods

With the advent of the digital age, digital signal processing technology has spread to all areas of daily life. In projects such as chemical engineering, medicine, industry, and scientific research, people need to detect and process the corresponding signals. People usually convert the analog signals transmitted by the sensor into digital signals according to the sampling theorem, and then process these digital signals. Data acquisition is the core of digital signal processing, and its results directly affect the follow-up work. A complete data acquisition system should include sensors, signal conditioning, data processing chips and software. The development of society and the advancement of science and technology make digital signal processing technology more and more intelligent and real-time, and the accuracy and speed of data collection are also getting higher and higher. Nowadays, many occasions such as radar measurement system, aerospace navigation, medical imaging, etc. require high-precision data acquisition, which means that people put forward higher requirements for data acquisition systems, because the measurement results in these environments need to be extremely accurate. In addition to the basic functions of data collection, people must also implement multiple functional modes, multiple range ranges, multiple control methods, etc. for different industries and different collection environments. In addition, with the rapid development of electronic technology, portable solutions have begun to occupy an increasing proportion in data acquisition, and more and more have begun to lean towards low voltage, low power consumption, and miniaturized designs. Therefore, there are more and more types of data acquisition instruments, and the update speed is getting faster and faster. General-purpose data acquisition systems cannot meet various needs in a targeted manner, which forces many companies to develop various dedicated data acquisition systems.

Traditional data acquisition systems mainly use data acquisition cards. In recent years, the speed of data acquisition systems and the rapid development of digital signal processing technology, while the application of USB technology, Ethernet technology and wireless communication technology in the data acquisition system has expanded its interface methods, and portable data acquisition systems have become increasingly favor of users. The TDS-150 portable data recorder produced by Tokyo Sokki Research Institute is a static data acquisition instrument with 50 measurement channels, sleep interval timing and data stora-

ge functions, and long-term automatic measurement. It is equipped with USB and RS-233 port can read various measurement settings and measurement data and transfer them to the computer. The compact DAQ of NI Company in the United States is combined with Ethernet technology to extend the scope of high-speed data acquisition to remote sensors and electronic measurement in the laboratory and even the world. It is tightly integrated with LABVIEW software and can obtain the highest performance with less development investment.

1.3 The main content of this paper

Based on the analysis of data acquisition systems at home and abroad, this article takes into account the speed, accuracy and scalability of data acquisition, chooses the ARM M4 controller STM32F429ZG and STM32F303ZET from ST Company, and designs a set of data acquisition system with strong versatility. Realize the functions of high-speed and high-precision data acquisition, display and transmission. The main research contents of this article are as follows:

- (1) Analyzed the research status of the data acquisition system, divided the system into different modules, studied the hardware circuit and software design of the data acquisition system based on the ARM M4 chip STM32, and initially realized the high-precision acquisition, display and transmission of signals Features.
- (2) Use the 12-bit precision ADC module in the Stm32 chip to collect the voltage signal and use the capture mode of the GPIO module to collect the temperature through the DS18B20 sensor, and then realize the data transmission in different single-chip computers through the SPI protocol.
- (3) Realize serial communication through Ethernet module.
- (4) Realize data display on the PC screen through IoT.

2 Fundamentals

This chapter introduces the basic knowledge of the measurement system modules and the basic structure of the corresponding circuit, and introduces several applications related to the Internet, which can handle the measurement results well. The measured current value can be obtained by Ohm's law by measuring the voltage, so this article only collects the voltage value to measure the voltage value and the current value.

2.1 ADC

In order to plan long-term measurement measurement devices, we must first understand the basic types of ADCs, the characteristics of each type, and their accuracy. In addition, you need to understand the basic circuit of the specific selection of the microcontroller, and you can know how the ADC performs voltage measurement.

2.1.1 Introduction of voltage/current-sensor

The essence of voltage/current-sensor sensor measuring voltage is a voltage divider circuit composed of resistors. Its principle is the knowledge of resistor series voltage divider. The typical circuit is shown in Figure 2.1. Here, U_{in} is the voltage of component under test,

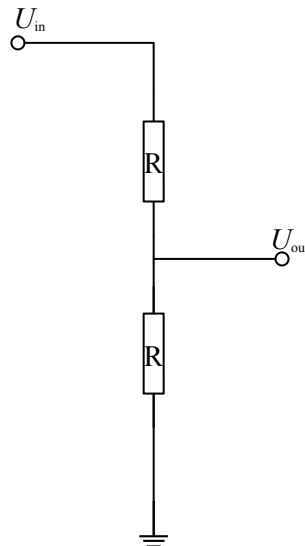


Fig. 2.1: Structure of divider resistor

U_{out} is voltage of divider resistor R_2 , and R_1 , R_2 are divider resistors.

Therefore, the measured voltage provided to the ADC channel is the following formula:

$$U_{out} = \frac{R_2}{R_1 + R_2} U_{in} \quad (2.1)$$

There are a variety of precision ADCs available on the market to choose from, as shown in the Figure 2.2 below, the appropriate ADC precision can be selected according to the signal bandwidth, so that it is convenient for users to measure.

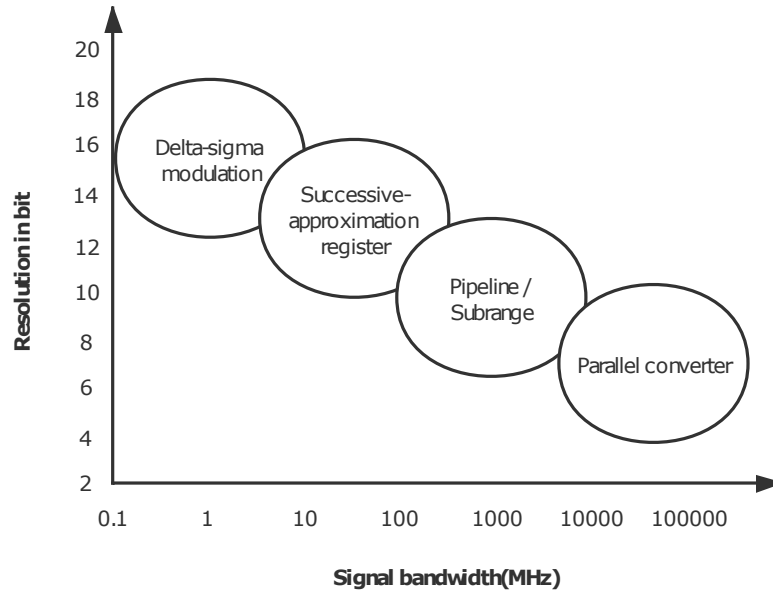


Fig. 2.2: ADC-Architecture depending on signal bandwidth and resolution

In the case of methods without feedback, the parallel converter has the simplest structure, which is why, with high resolution, a large number of components with high demands on accuracy are required. Here, too, there are methods with range selectivity, oversampling and ramp methods. A folding process has also established itself alongside a multiplex technique. The following Figure 2.3 briefly describes the basic structure of the parallel converter:

Since with this parallel converter (flash converter) the entire conversion takes place within one clock period, it is the fastest method. It is easy to see, however, that the number of components required quickly becomes very large, since the entire chain with resistor, comparator and latch is required for each of the $2^n - 1$ comparison values. The thermometer code present at the output of the latches is encoded in a binary code (often a Gray code, in which neighboring numbers differ by only one bit). Differential amplifiers are particularly suitable as comparators, with the gain v having to be at least so large that $\Delta U/2$ is sufficient to control the necessary logic level of the latches.

The second important method is called the weighing method, which requires feedback via a digital-to-analog converter, as Figure 2.4 shows. As soon as the track and hold element (T&H) has switched to hold mode, the weighing cycle begins with the most significant bit. If $u_e > \frac{U_{max}}{2}$, the top bit remains set and the second bit is set to 1 on a trial basis.

The comparator decides whether the word in the SAR is smaller than the input signal. Then the check bit remains set and the next lowest bit is checked. If $u_e < u_{DA}(W)$, the check bit is reset.

The conversion time is n clock periods, whereby the digital-to-analog converter must have full accuracy. The advantage of high resolutions is that only one precise comparator is re-

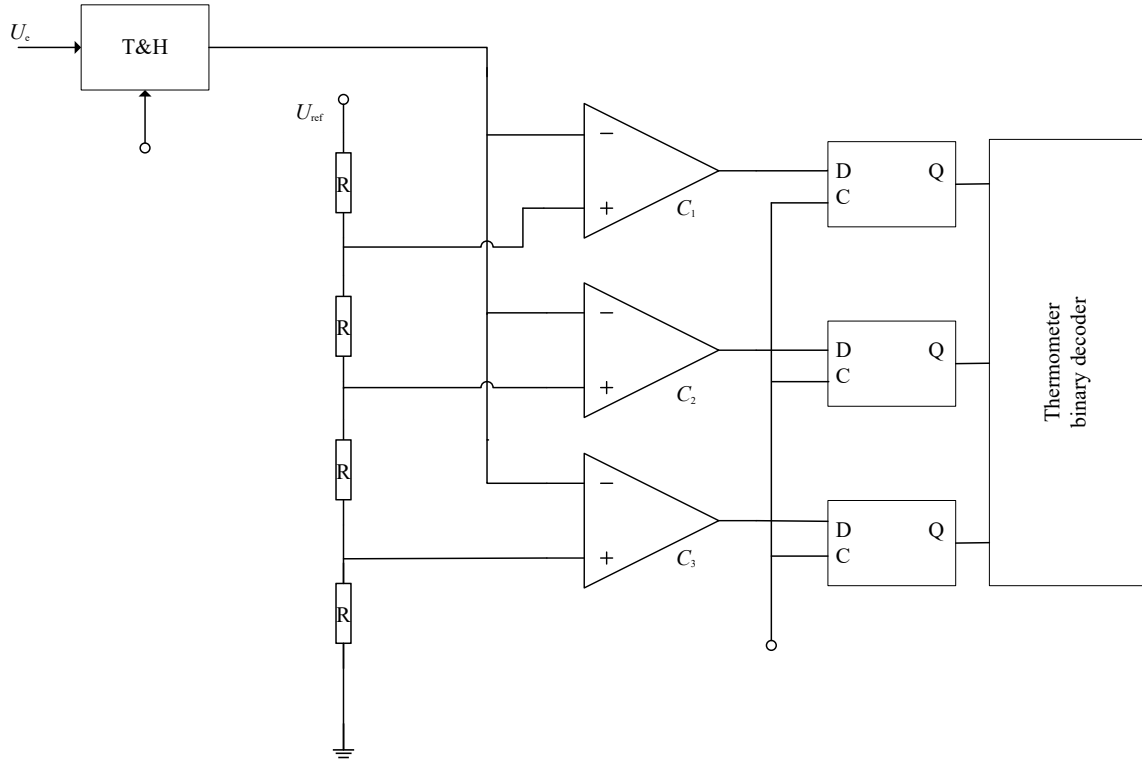


Fig. 2.3: Block diagram of a parallel converter

quired. This enables the design of very energy efficient implementations. If a counter is used instead of the SAR, the Laund can count the clock cycles backwards through the comparator. In the steady state, the data word indicates. There are also many variants of this counting method, whereby in the worst case the conversion time can be up to 2^n clock cycles. In the gallium nitride measurement, because the precision is required but the precision is not the highest, the above-mentioned method is selected.

2.1.2 ADC in microcontroller STM32F303ZET

In this measurement, we choose STM32F303ZET as a microcontroller containing multiple 12-bit precision ADCs. Its core is ARM-Cortex-M4, and its ADC is of type Successive approximation register. Each stm32F303 has a 40-channel independent 16-bit precision ADC, and the price is very high, so we choose this microcontroller to measure the voltage signal to determine whether the gallium nitride is affected by cosmic rays. The following Figure 2.5 is the typical connection diagram using the ADC:

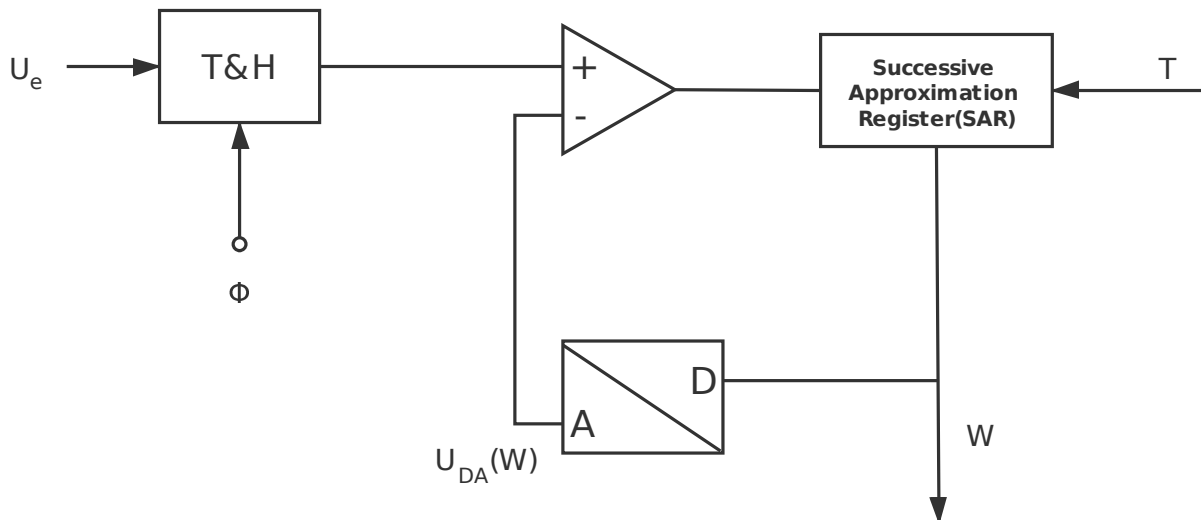


Fig. 2.4: Converter according to the weighing method

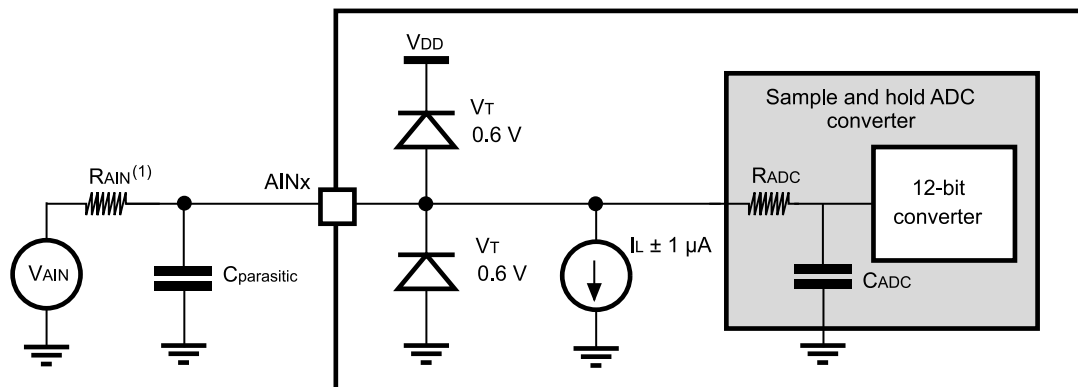


Fig. 2.5: Typical connection diagram using the ADC

2.2 Ethernet

Ethernet has very extensive and in-depth applications in various fields and industries. This is mainly due to the high flexibility and ease of implementation of Ethernet. Because Ethernet has the advantages of simple networking, low cost, excellent compatibility, reliable connection, and convenient topology adjustment, it has advantages that other network technologies do not have in terms of being a gateway for smart homes, Internet of Things or wireless sensor networks. , Thus get vigorous development and application. This article will introduce in detail how to connect the embedded system to the Ethernet, how to use the hardware protocol stack to make your solution or application connect to the Internet quickly and efficiently, how to realize TCP/IP communication, and how to realize the upper application layer protocol and many more.

2.2.1 Introduce of W5500

The W5500 network expansion board integrates a hardware TCP/IP protocol stack chip W5500 and an RJ-45 with a network transformer. Among them, W5500 is a full hardware TCP/IP embedded Ethernet controller, which provides a simpler Internet connection solution for embedded systems. Hardware logic gate circuits are used to implement the transmission layer and network layer of the TCP/IP protocol stack (such as : TCP, UDP, ICMP, IPv4, ARP, IGMP, PPPoE and other protocols), and integrates the data link layer, physical layer, and 32K bytes of on-chip RAM as a data receiving and sending buffer. Make the host computer main control chip only need to undertake the processing task of TCP/IP application layer control information. This greatly saves the workload of the host computer for data replication, protocol processing, and interrupt processing, and improves system utilization and reliability.

Its module structure is shown in the Figure 2.6 below:

W5500 supports 8 sockets at the same time to facilitate communication with different IPs

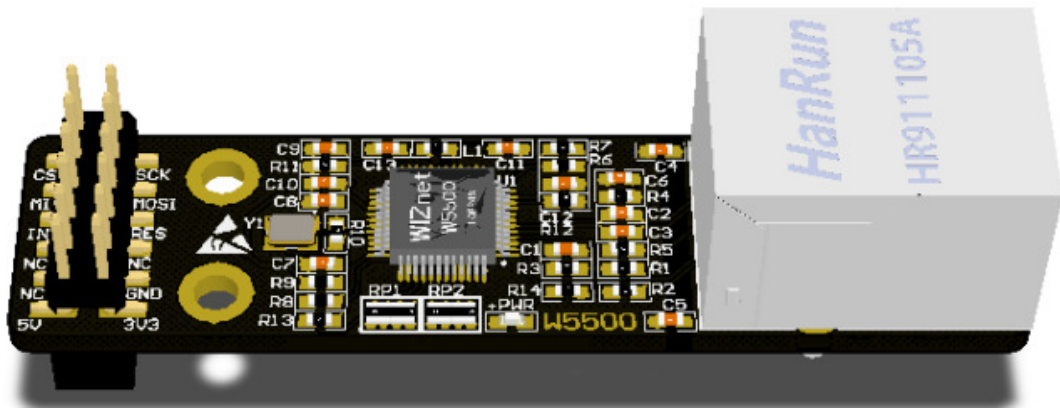


Fig. 2.6: Physical model of W5500

and devices; in order to reduce system energy. W5500 provides Wake-on-LAN mode (WOL) and power-down mode for customers to choose to use; W5500 is non-aggressive. The hardware network engine can prevent similar torrent, fraud and injection network attacks and improve network security.

2.2.2 solution of ethernet access

For non-operating system, how does the single-chip microcomputer required by the system realize network access? I will categorize these schemes according to the different TCP/IP protocol stacks below.

It is divided into two categories: the first category is the traditional software TCP/IP protocol stack solution; the second category is the latest hardware TCP/IP Protocol stack scheme. Below I will analyze the implementation of these two types of solutions:

1) MAC+PHY solutions:

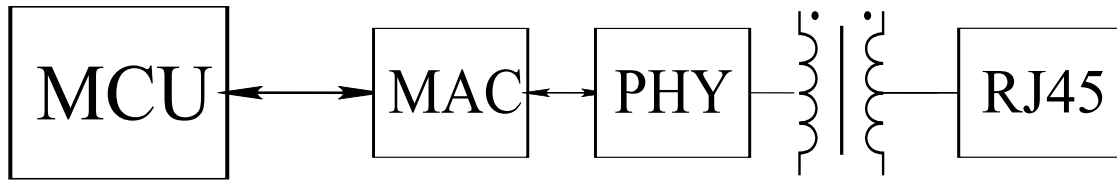


Fig. 2.7: MAC+PHY Ethernet solution

The traditional Ethernet access scheme is as shown in the figure below. The MCU+MAC+PHY is added to the network interface to realize the Ethernet. The physical connection of the network realizes communication and upper-layer applications by implanting TCP/IP protocol code in the main control chip.

2) Hardware protocol stack chip solution:

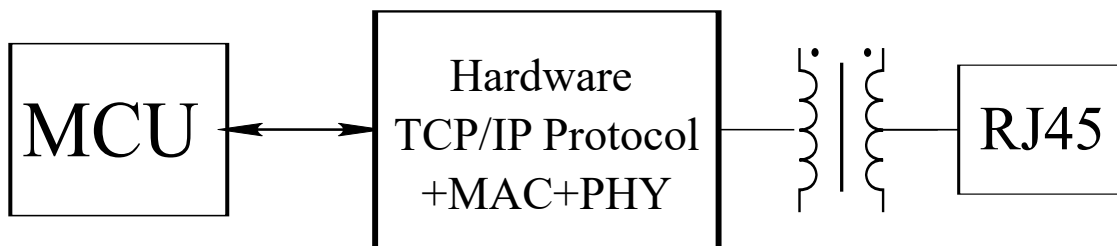


Fig. 2.8: Hardware protocol stack chip solution

The hardware protocol stack chip scheme is shown in the figure below. The MCU + hardware protocol stack chip (including MAC and PHY) directly adds the network interface, and the single-chip microcomputer can be easily connected to the network. All the work of processing the TCP/IP protocol is through the "little secretary" of the MCU-the hardware protocol Stack chips to complete.

This solution was first proposed by WIZnet and successfully launched the Ethernet series of chips: W5100, W5200, W5300 and W5500. The so-called hardware protocol stack refers to the implementation of the traditional software TCP/IP protocol stack with hardware-based logic gate circuits, as shown in the Figure 2.9 below.

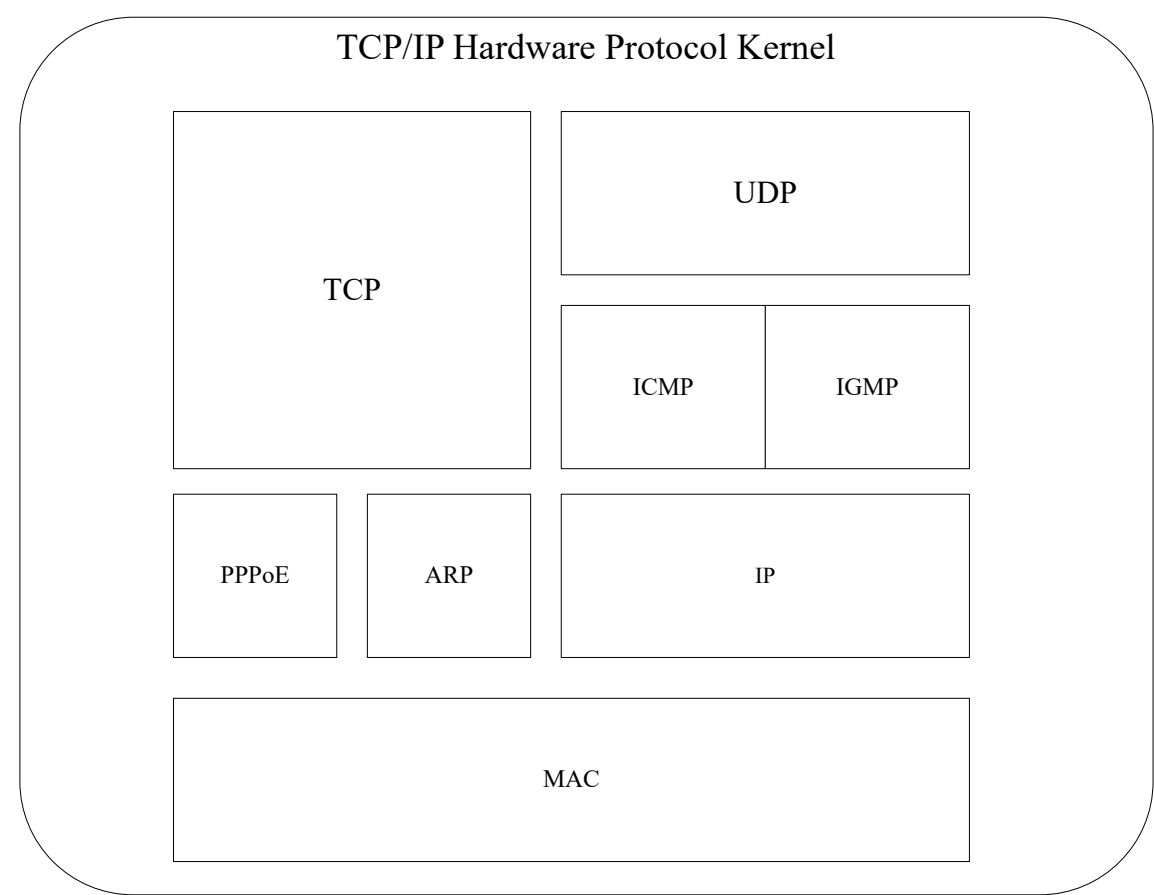


Fig. 2.9: Hardware protocol kernel

The core of the Ethernet chip is composed of protocols such as TCP, UDP, ICMP, IGMP in the transport layer, IP, ARP, PPPoE and other protocols in the network layer, and MAC in the link layer, plus physical layer PHY and peripheral registers and memory. The SPI interface constitutes this complete set of hardware-based Ethernet solutions.

This hardware TCP/IP protocol stack replaces the previous MCU to handle these interrupt requests. That is, the MCU only needs to process user-oriented application layer data. The transmission layer, network layer, link layer and physical layer are all controlled by the peripheral WIZnet The chip is complete. This set of solutions simplifies the aforementioned five-layer network model from two aspects of hardware overhead and software development, and simplifies product development solutions. In this way, engineers no longer have to face the cumbersome communication protocol code, only need to understand the simple register function and Socket programming to complete the network function development part of the product development work.

2.3 SMTP

When collecting temperature signals, if the state of more than 50 units under test suddenly changes within 10s, it is likely to be a manual error. At this time, it is necessary to set up an email-related program in the measurement system to send an email to notify the researcher. This article uses the SMTP protocol. SMTP stands for Simple Mail Transfer Protocol. It is a group of mails used to transfer mail from source address to destination address Rules, which

control the way in which letters are transferred. The actual process of sending emails can be shown in Figure 2.10:

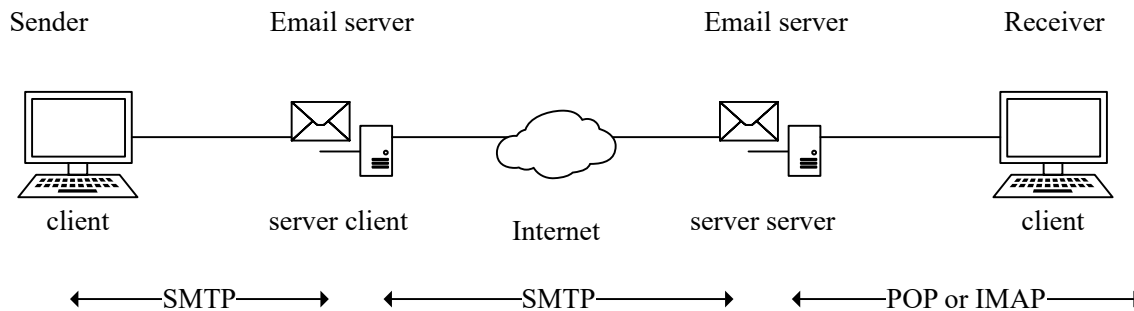


Fig. 2.10: Schematic diagram of the mail sending process

An important feature of it is that it can relay mail during transmission, that is, mail can be relayed through hosts on different networks. Usually it works in two situations: one is the transmission of mail from the client to the server; the other is the transmission from one server to another server. SMTP is a request/response protocol, it monitors port 25, is used to receive the user's Mail request, and establish an SMTP connection with the remote Mail server.

SMTP usually has two working modes. Send SMTP and receive SMTP. The specific working method is: Send SMTP after receiving the user's mail request, determine whether the mail is a local mail, if it is directly delivered to the user's mailbox, otherwise query the MX record of the remote mail server to the DNS, and establish a connection with the remote A two-way transmission channel between receiving SMTP, after which SMTP commands are issued by the sending SMTP, received by the receiving SMTP, and the response is transmitted in the opposite direction. Once the transmission channel is established, the SMTP sender sends a MAIL command to indicate the sender of the mail. If the SMTP receiver can receive the mail, an OK response is returned. The SMTP sender then issues the RCPT command to confirm whether the mail has been received. If the SMTP receiver receives it, it will return an OK response; if it cannot receive it, it will send a rejection response (but not stop the entire mail operation), and both parties will repeat this many times. When the receiver receives all the mails, it will receive a special sequence. If the receiver successfully processes the mail, it will return an OK response.

2.4 UDP

In the transport layer of the TCP/IP protocol stack, TCP is connection-oriented, and the UDP protocol to be demonstrated next. It is non-connection oriented.

When UDP transmitting data, there is no confirmation, retransmission, or congestion mechanism. Since this measurement can obtain a large amount of data in a short time, the packet loss and error data can be verified by the large amount of data received later, so this time we choose UDP.

Compared with TCP, UDP does not return a response signal for each received frame during transmission. When network transmission fails, such as a router restart and the network is suddenly interrupted, UDP can still receive the signal transmitted by the server as much as possible. The comparison figure 2.11 is as follows :

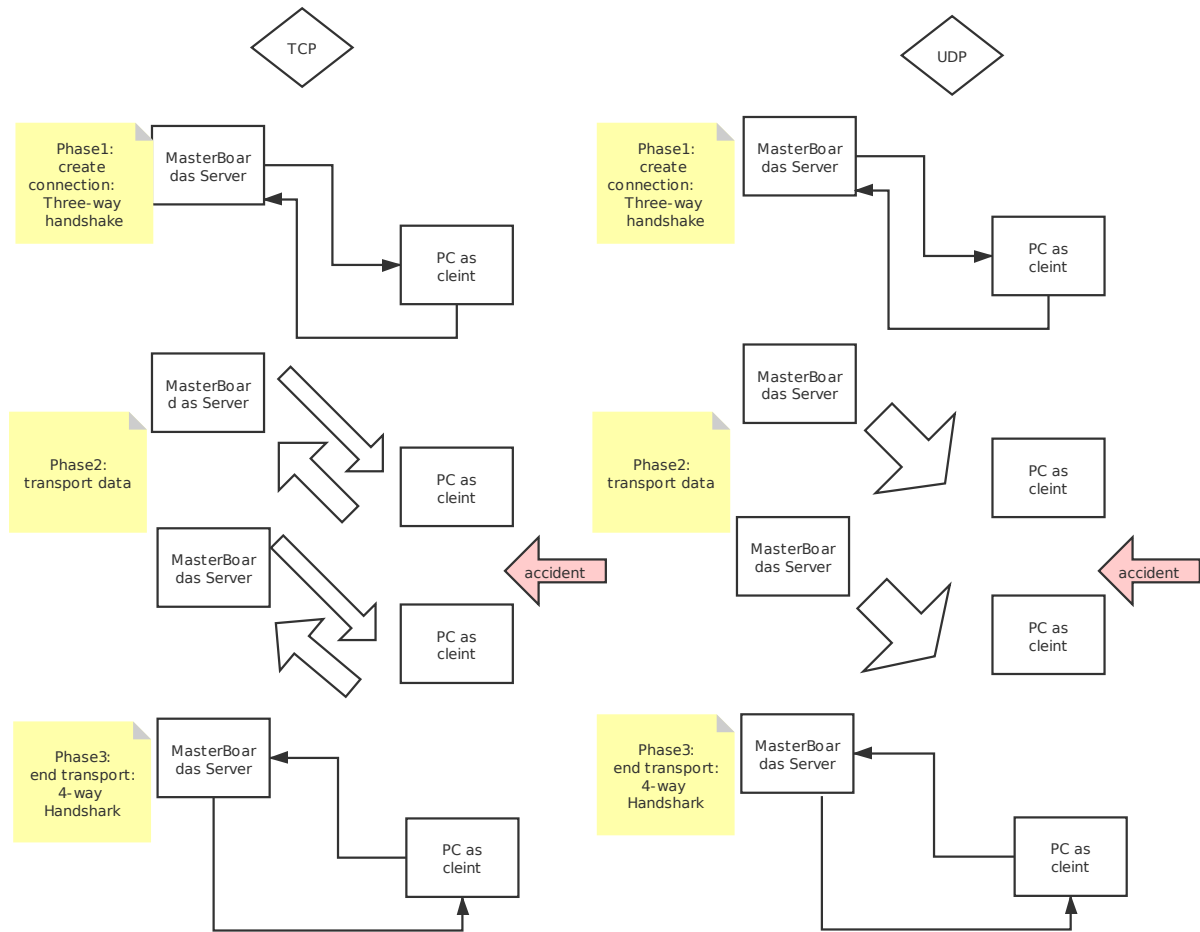


Fig. 2.11: Comparison of TCP and UDP

The UDP establishment process of W5500 is also very convenient, which can be easily realized by simply reading and writing registers. After the program is initialized, enter the main loop function. When the Socket is closed, before communicating, we first initialize the Socket port in UDP mode. When the socket is in the initialization completed state, that is, the SOCK UDP state, data can be sent by broadcast at this time.

Next, use W5500 to demonstrate how to use UDP to send and receive data. There are two issues to pay attention to before the test.

First, it is recommended to turn off the PC firewall.

Secondly, if the W5500 module and the PC are directly connected via a network cable, you need to modify the IP address of the PC to a static IP, and keep it in the same network segment as the W5500 IP.

If you connect to the router directly, you do not need to modify the IP address of the PC. The specific test steps are:

1. Use the IP address obtained by the router to set the configuration file in the W5500, this IP must be in the same network segment as the router gateway.
2. Obtain the computer IP address through the command line and write it as remote IP into the configuration file of the w5500 Ethernet chip.
3. Compile the code, and then burn the program to the Wildfire development board.
4. Connect the network cable and USB serial port cable. Open the serial port debugging tool, reset the Wildfire development board, and get the setting information from the output result.

2.5 SPI

The Serial Peripheral Interface (SPI) is a synchronous serial communication interface specification used for short-distance communication, primarily in embedded systems. The interface was developed by Motorola in the mid-1980s and has become a de facto standard. Typical applications include Secure Digital cards and liquid crystal displays.

SPI devices communicate in full duplex mode using a master-slave architecture with a single master. The master device originates the frame for reading and writing. Multiple slave-devices are supported through selection with individual slave select (SS), sometimes called chip select (CS), lines.

Sometimes SPI is called a four-wire serial bus, contrasting with three-, two-, and one-wire serial buses. The SPI may be accurately described as a synchronous serial interface,[1] but it is different from the Synchronous Serial Interface (SSI) protocol, which is also a four-wire synchronous serial communication protocol. The SSI protocol employs differential signaling and provides only a single simplex communication channel. SPI is one master and multi slave communication.

The figure 2.12 below is a standard SPI transmission process:

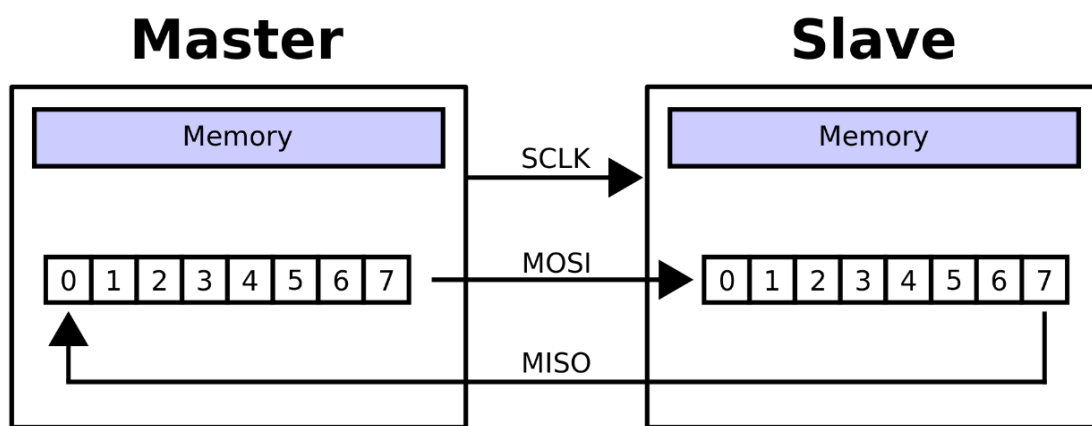


Fig. 2.12: standard SPI transmission process

2.6 DMA

The basic definition of DMA: DMA has the full name of Direct Memory Access. DMA transmission copies data from one address space to another address space, so as to realize high-speed data transmission between peripherals and memories or between memories. When CPU initializes such a transmission action, the transmission action is realized and completed through the DMA controller. The DMA transmission mode is dispense with CPU to realize direct control transmission or differing from the interrupt processing mode, it will reserve the site and recover the site process. Through RAM and IO devices, a channel of direct transmission data can be opened to improve CPU efficiency strongly.

Main features of DMA: Each channel is directly connected with the exclusive hardware DMA request. Each channel also supports software touch. Such functions can be configured through software; On the same DMA module, the priority between multiple requests can be

established through software programming (with a total of four levels: very high, high, medium, and low). When the priority setting is equal, it depends on hardware (request 0 precedes over request 1, and the rest may be deduced by analogy); For the transmission width of independent data sources and target data areas (byte, half-word, and full-word), byte control is used in this design to simulate the process of packing and unpacking. The source and target address must be aligned according to the data transmission width; The buffer management can support circulation; Each channel has three event flags (DMA semi-transmission, DMA transmission completion, and DMA transmission error). The logic of three event flags may become an individual interrupt request; Transmission between memories, transmission between peripherals and memories, and transmission between memories and peripherals; Flash memory, SRAM, SRAM of peripherals, APB1, APB2, and AHB peripherals can be used as sources and targets for access; The maximum number of programmable data transmission is 65535. STM32F429 series chip DMA controller STM32F429 series chip at most has two DMA controller. DMA1 and DMA2 respectively includes 8 channels. Each channel specializes in managing the request of one or multiple peripheral for memory access. It also has the priority of coordinating each DMA request through arbitration.

For the requests generated from peripherals (TIM, ADC, SPI1, SPI/I2S2, I2C and USART), it is inputted to DMA1/DMA2 controller through logic, implying that only one request is valid at the same time. DMA's work block diagram is stated below: 2.13 of DMA:

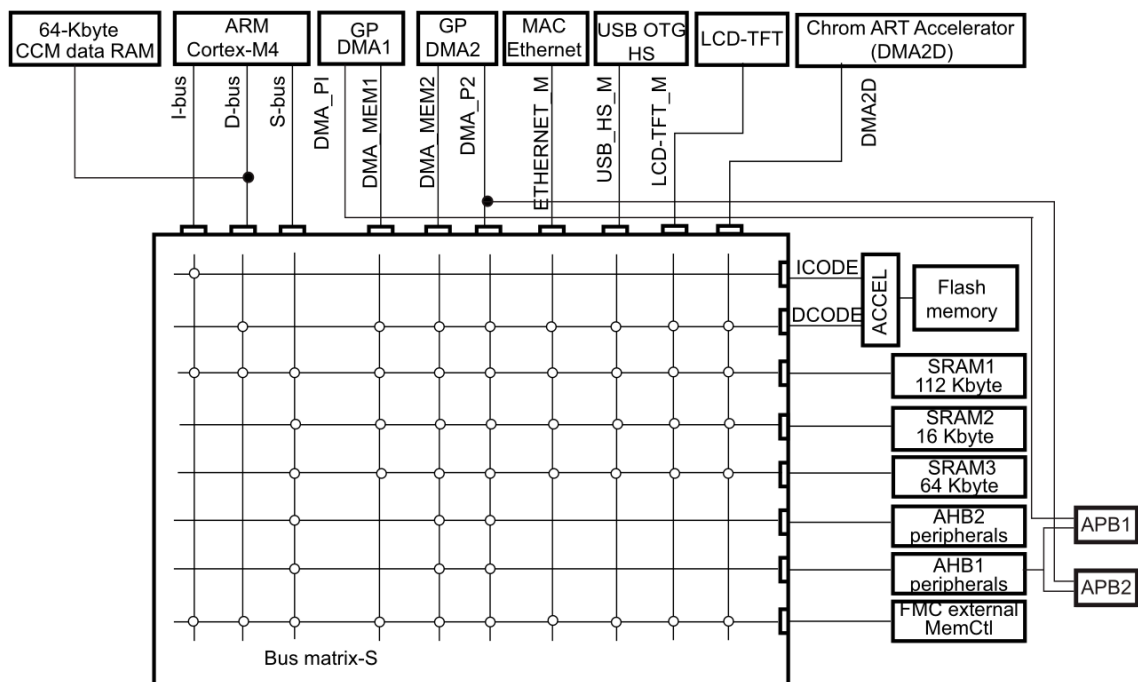


Fig. 2.13: STM32F429xx Multi-AHB matrix

2.7 Ngrok

Internal network penetration or NAT penetration aims to make the data package with a specific source IP address and source port number not block from NAT but will route to the internal network host correctly. The internal network penetration method is introduced through

the relative position between the mutual communication host in the network and NAT devices. The UDP's internal network penetration, in essence, uses the NAT system on the router. NAT is a conversion technology that transforms the private (reserved) address into the legal IP address. It is widely applied in the internet access ways with all kinds of types and different types of networks. NAT can reuse the address and realize external shelter for the internal network structure. Ngrok is a commonly used internal network penetration technology. Generally speaking, our local PC has two IPv4 addresses. One local IP is allocated by the router and the other one is the public network IP. However, people in other areas all over the world cannot directly use such two IP addresses to access the local PC. If the project's logic and data structure are not complicated, developers can use the open-source ngrok for the internal network penetration so that external personnel can access the local PC.

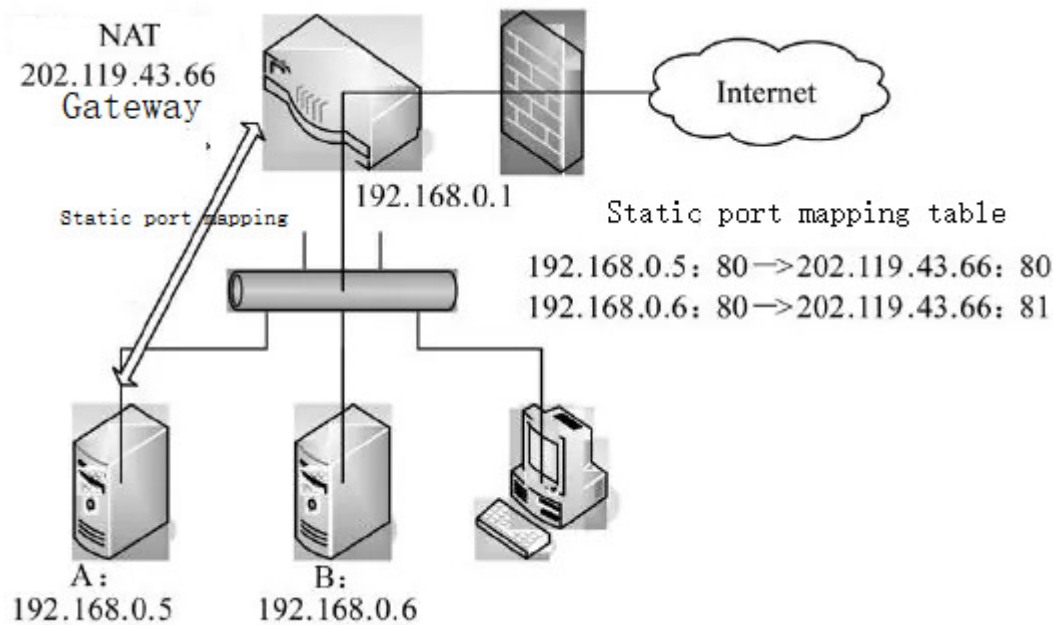


Fig. 2.14: NAT-Technical

This article uses Ngrok for intranet penetration, the structure is as follows Figure 2.15 :

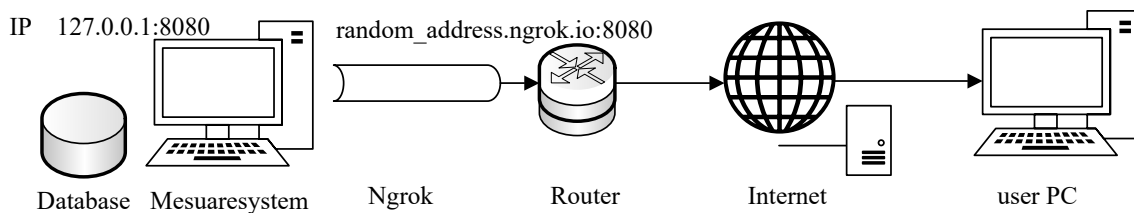


Fig. 2.15: Ngrok-Technical realization

2.8 IoT

Internet of Things is abbreviated into IoT. In brief, it means that all devices are linked to the internet in a way: From smartphones and tablet PCs (common) to automobiles and refrigerators. The main function of IoT lies in how to link devices, services, and Apps to the internet.

so that it will develop the greater role. There is almost no limitation for devices accessed in the internet and reasons for access. The important way for IoT to improve quality of life lies in making data sharing easier. IoT is conducive to streamlining our life. In the long term, it can handle some trifling matters for us. This article uses the core philosophy of IoT and issues data collected to the cloud. Users can access the cloud server through HTML5 page to gain the pushed data. If the cloud server breaks down, user PC can collect the PC through VNC remote control.

The signal collector gains the outer net IP as the server through internal network penetration. The signal collected by the single chip microcomputer through UDP is imported to the database. Users or researchers in other areas can gain the database data of the signal collector through the html webpage (deployed on the free website of githubpage). When the signal collector goes wrong, users can collect the signal machine through VNC remote control. The following Figure 2.16 is a schematic diagram of the Internet of Things structure used in this article:

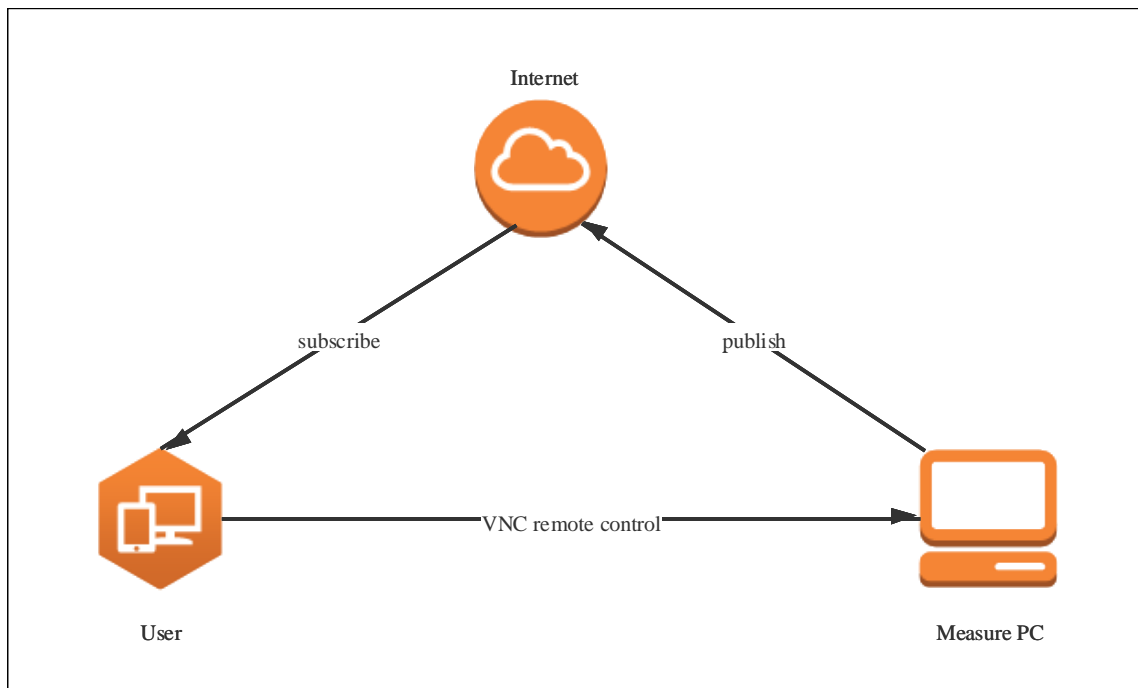


Fig. 2.16: Measuring models using the Internet of Things

The signal acquisition machine obtains the external network IP as a server through the intra-net penetration, and collects the signal collected by the single-chip microcomputer through UDP and imports it into the database. Users or researchers in other regions can obtain it through the html web page (which has been deployed on the free website of the github page) Collect the database data of the signal machine. When there is a problem with the signal machine, the user can also remotely control the signal machine through VNC.

3 Introduction of measurement circuit

3.1 Measurement circuit overview

In order to ensure the measurement accuracy, we add an ADC in front of the unit under test. If the gallium nitride is affected by cosmic rays, the ADC measurement value will change from high to low to achieve the measurement purpose. The following figure shows the basic structure of the measurement system, which is blown using a fuse.

When DUT is disturbed by the cosmic rays and it is conductive, the basic circuit uses the fuse as the action circuit. Then, the measuring signal chip suddenly gains a high level and remains the high level. Next, the software system adds one for the error signal counter. The basic constitution of the measuring circuit is shown in Figure 3.1:

In actual use, if we do not consider IGBT control, we can use IGBT as the protecting circuit component. When fusing of the gallium nitride MOSFET is affected by cosmic rays and it is conductive, the microcontroller can gain the voltage value through ADC, so as to realize whether the gallium nitride component will be influenced. The artificial circuit is illustrated below: When DUT is normally operated, IGBT in theory gains a 0V voltage and it will not turn on the microcontroller to get a low level, as shown below:

When DUT is unusually operated, IGBT in theory gets a 15V voltage (with IGBT breakover) and it will not turn on the microcontroller to get a high level, as shown below:

LM393 voltage comparator is used to control IGBT. LM393 is a common voltage comparator chip in our daily life. It has two independent operational amplifiers. The power supply is provided by wide voltage and undercurrent. Low input controls the current. Generally speaking, the actuation time only has 1.3 microseconds. When the in-phase input voltage is greater than the inverted input voltage, output voltage belongs to the high level. When the in-phase input voltage is less than the inverted input voltage, output voltage belongs to the low level.

According to STM32F429 official specification, the typical value of STM32 operating power current is 200mA but we need to select SPI channel and GPIO interface as measurement and use multi-channel ADC for measurement. When the circuit board encounters high temperature and high pressure or improper situations, the required power will rise. Under the circumstance, we need to consider the maximum power (495mW). We calculate the main control mainboard's maximum demanded power by ourselves, thus:

Since each DCDC chip can provide 6W switching power supply (which provides 350W), each main control mainboard only needs a DCDC convertor). We calculate the slave independently, thus:

In this circuit, the mature SCM chip STM32F429ZG of ST Company is used as the Ethernet-host connection chip. Since STM32F429 has more than 6-line SPI channels, it can be used as the chip to link with various chips. Since each STM32F303ZET6 has more than 40-channel ADC with the high cost performance, STM32F303ZET6 is used as the voltage signal chip for acquisition. The specific circuit diagram is shown below:

To measure 1000 DUT components, at least 1000 lines of ADC should be used. To ensure signal quality, electromagnetic compatibility, and developmental cost, ADC is only used on the slave, as shown in the picture below:

If we measure 1000 measured units and each STM32F303ZET provides independent 40-line ADC, we need 25 STM32F303 chips. The table below shows the independent ADC distribution of each STM32F303.

The picture above is the artificial circuit of this circuit. The voltage output result is choppy because of LM393's circuit property. In actual design, the filter capacitor is added to LM393. To improve reliability and SNR, this article needs to discuss the upper limit of acquisition speed to ensure that all data will be sent to the PC correctly.

According to the chip's specification, SPI of the slave only has the fastest transmission speed of 4Mbps. Moreover, four slave mainboards send data to the host mainboard. Every time, ADC voltage signal acquisition at least needs 1.5 periods and the maximum periods should be set up as 601.5. According to the specification, each switching time is:

Since we apply the mean filter method, sampling per 10 times can be used as a group of data and then through geometric mean, at least 10 μ s is needed for sampling. Considering that the host mainboard accepts the data sent from 5 slave mainboards simultaneously, the SPI receiving speed is set up as 8MB/s, for the sake of preventing congestion. Thus, sending speed of each slave mainboard must be reduced to 1.6Mbps, for the sake of ensuring that the host mainboard can receive all data from the slave mainboard. In other words, the slave mainboard applies the mean filtering circulation to increase the delay procedure every time, so as to ensure that each value received by the host is valid and reliable. At the same time, the temperature collecting speed of the mainboard should be noticed. It will exert an impact on the acquisition speed and total time of major cycles. Developers can set up the counter to set up the number of major cycles to acquire the temperature value once. According to the picture below, The host writes data in DS18B20 as writing the time slot, including "0" time slot and "1" time slot. The bus host uses "1" time slot to write logic in DS18B20 and to write logic 0 in DS18B20 as writing "0" time slot. All writing time slots at least must have 60s duration. Two adjacent writing time slots at least must have 1s recovery time. Two writing time slots can be generated by lowering the bus through the host (see the picture below), for the sake of generating "1" time slot. After lowering the bus, the host must release the bus within 15s. After releasing the bus, the pull-up resistor can recover the bus to the high level. To generate "0" time slot, after lowering the bus, the host must continue lowering the bus to satisfy the requirement of the time slot's duration (at least 60s). After the host generates the writing time slots, DS18B20 will sample the single bus(DQ) within a time quantum of 15-60s. Within the time window of sampling, if the bus belongs to the high level, the host will write 1 in DS18B20; if the bus belongs to the low level, the host will write 0 in DS18B20. To sum up, all writing time slots at least must have 60s of duration. Two adjacent time slots at least must have 1s of recovery time. All writing time slots(0 and 1) are generated by lowering the bus. When the host launches to read the time sequences, DS18B20 is only used to transmit data to the controller, thus the bus controller will issue the order of reading the transient memory [0xBE] or the order of reading the power mode [0xB4] and then it immediately starts reading time sequences. DS18B20 can provide the request information. Besides, the bus controller will read the time sequences after issuing the order of sending temperature conversion[0x44](or recalling EEPROM order[0xB8]) before reading time sequences. See details in the functional order in DS18B20 chip manual. All reading time sequences at least must reach 60s, including two reading periods with at least 1s recovery time. When the bus

controller pulls the USB cable from the high level to the low level, as starting reading time sequences, USB cables at least must remain 1s and then the bus is released. DS18B20 pulls up or pulls down the bus to transmit “1” or “0”. When the transmission logic “0” comes to an end, the bus will be released. When the pull-up resistor returns to the rising edge state, data from DS18B20 will be valid within 15s after the falling edge of reading time sequences occurs. Thus, as starting reading time sequences, the bus controller must stop I/O drive into the low voltage 15s to read I/O state. This article produces multi-layer PCB board to ensure signal quality and electromagnetic compatibility. Generally speaking, three kinds of multi-layer board layout modes are illustrated below: The designer needs to consider four rules below. Also, we need high semaphore of transmission and long-time use.

3.1.1 Simulation of Measurement circuit

3.2

4 Design of the measuring system

5 Detailed presentation of the measuring system

6 Design PCBs

7 Introduce measurement with eval boards

8 Conclusion

In order to measure the effects of cosmic radiation on GaN power semiconductors for a long time, the microcontroller STM32F303 is used to measure the voltage signal and the DS18B20 is used to measure the temperature signal.

The collected signal is sent to the main control unit of the microprocessor through AD conversion, the data collection stage is realized through database and back-end processing, and the collected data is sent to the front-end to facilitate clearly understanding of the users. The acquisition circuit is also simulated at the same time, the PCB board is designed and laid out to meet the design requirements.

Researchers can first debug the data which is collected from the host and slave through the debugger, then import the collected data into the MySQL database through the shell command line, and finally convert the IP address of the intranet to the public network address through the intranet penetration. In this case the personnel at the Hannover Institute can also observe changes of the collected signals such as the number of failed units under test. Researchers can also remotely access and control the experimental PC through VNC and collect data from the back-end to the database and then to the front-end. This realized the process of the Internet of Things.

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Brief Article

The Author

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