

# CRNCH Rogues Gallery

The CRNCH Rogues Gallery is focused on developing our understanding of next-generation hardware with an emphasis on unorthodox and uncommon technologies. This project acquires unique hardware from vendors, research labs, and startups and makes it available to students, faculty, and industry collaborators via NSF's CISE Community Research Infrastructure program.



A critical question today is: How can we rethink computing technology to restart historic explosive performance growth?

The Rogues Gallery testbed hosted by the **Center for Research into Novel Computing Hierarchies (CRNCH)** at Georgia Tech seeks answers to this question through support of interdisciplinary research programs and full-stack explorations of post-Moore computing technologies.

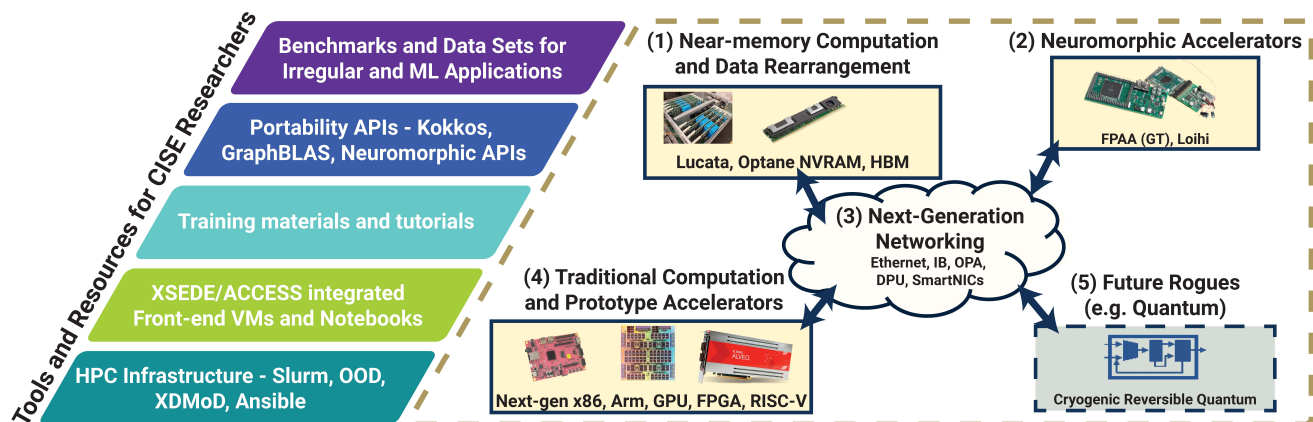
This **open-access** testbed supports novel high-performance computing (Arm, RISC-V), near-memory and neuromorphic computing as well as tools for interacting with future technologies like quantum and reversible computing.

**Opportunities for engagement** including requesting access to the testbed for research programs, co-developing tutorials for new architectures and applications, and contributions of new hardware for interactions with student and faculty researchers.

## Highlights

The Rogues Gallery has hosted the following activities since its inception in 2019:

- Over 150 users, including 50-60 external users from academia, industry, and government labs
- Tutorials at ASPLOS, PEARC, Supercomputing, MICRO, HPEC, Telluride, and HotInterconnects
- The largest installation of the Lucata Pathfinder, which is featured on the Graph500 and GreenGraph 500 lists
- Support for large student classes with over 100 students supported per semester



Georgia Tech College of Computing

Center for Research into  
Novel Computing Hierarchies

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