Accurately Modeling Sparse Accesses for Benchmarking and Architectural Simulation

Georgia Tech

Vincent Huang vhuang31@gatech.edu

Jeffrey Young jyoung9@gatech.edu

Patrick Lavin Richard Vuduc plavin3@gatech.edu

richie@cc.gatech.edu

Why do we need to model sparse accesses?

We are interested in sparse accesses like gather/scatter as they are a challenging type of memory access pattern found in high-performance applications.

Our approach consists of

- 1. Dynamic tracing to extract gather/ scatter calls from an instruction stream
- 2. Analysis to extract the most-used patterns from an application
- 3. Synthesis to create patterns that represent the application's behavior

What makes a good pattern?

To recreate sparse addresses without a full memory trace requires:

- 1. Base address and offsets
 - 1. Delta values for subsequent accesses
- Frequency of sparse accesses
 - I. Some concept of how many many "regular" accesses occur in between sparse accesses of interest

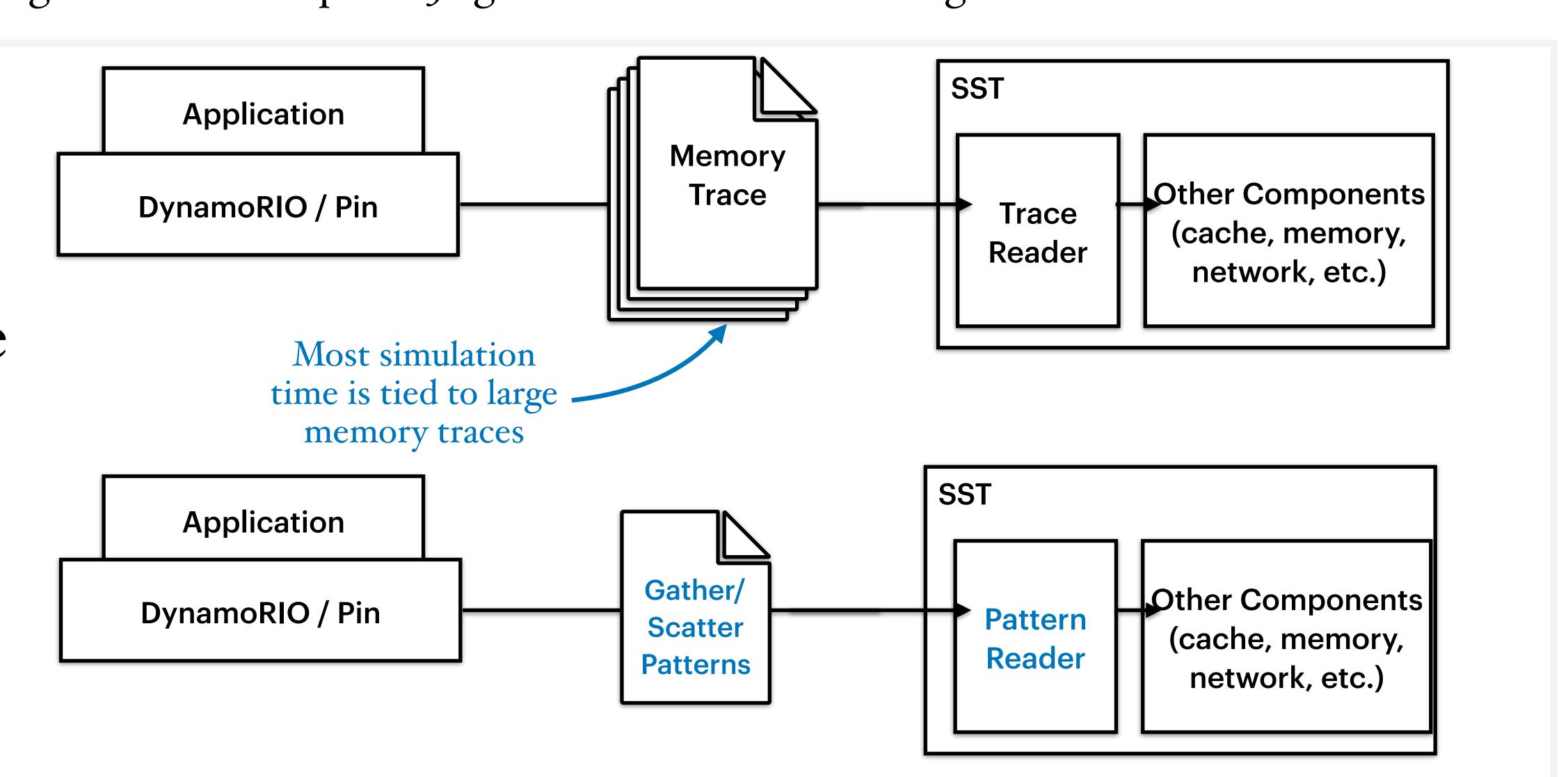
Next Steps

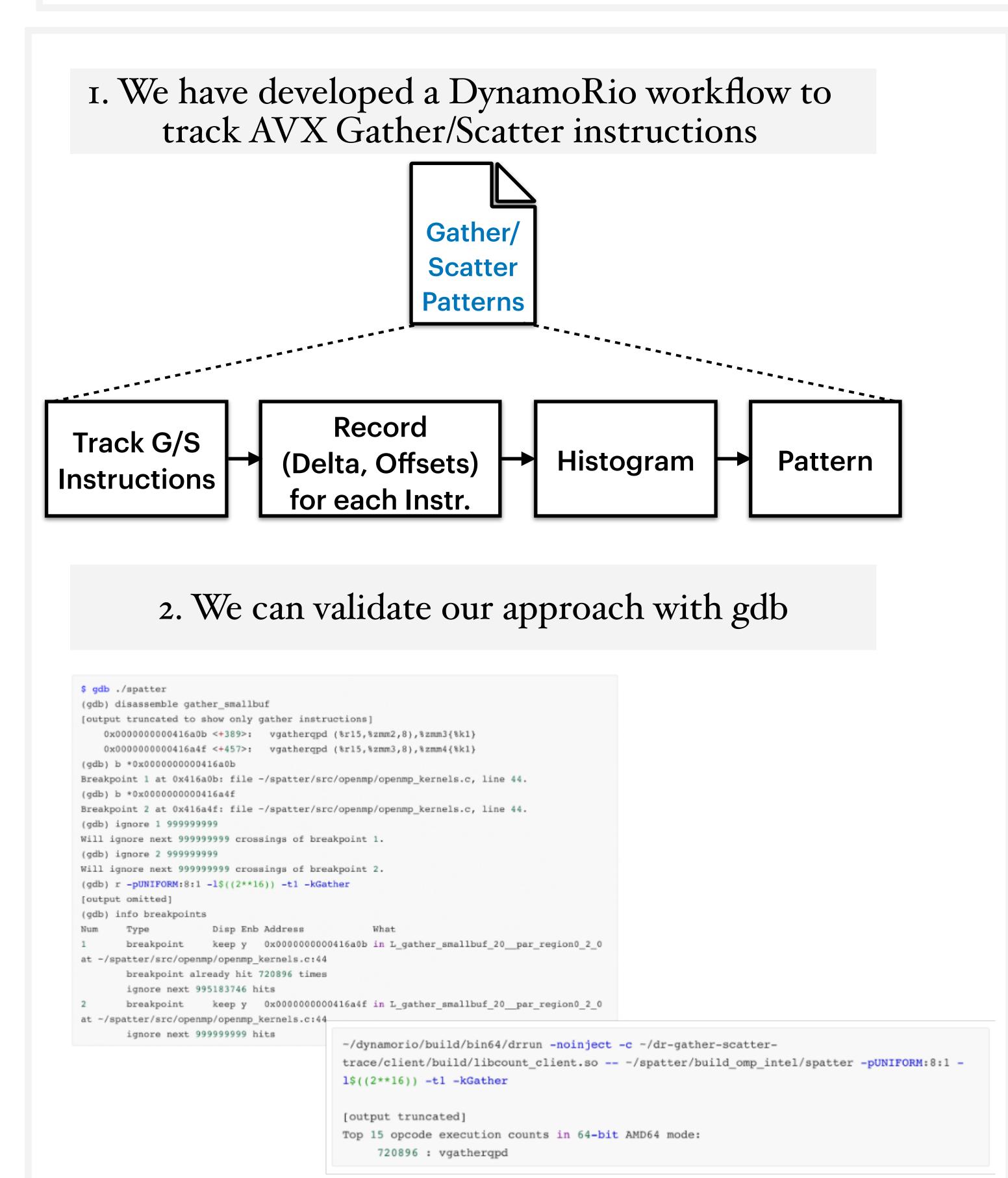
- 1. Generate more useful histogram output and test patterns against previous Spatter results
- Handle multi-threaded tracing
- Extend analysis to other sparse access types and aarch64
- Test with more applications and SST

This work was funded by Sandia National Labs and the National Science Foundation

We propose a new tool for extracting useful Gather/ Scatter patterns that can be replayed for simulations or benchmarking

Try it out! https://github.com/ hpcgarage/dr-gather-scattertrace

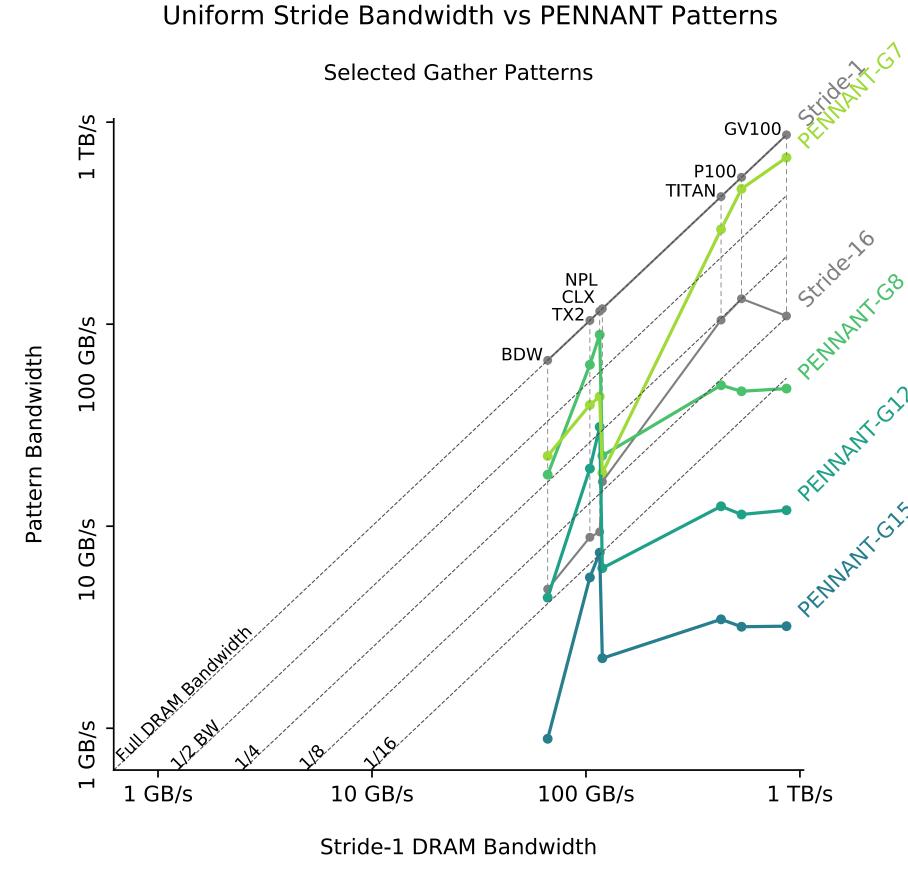




PENNANT Example

PENNANT has a variety of gatherfocused patterns in its main kernel

```
void Hydro::doCycle(const double dt) {
   dr_app_start();
    // Begin hydro cycle
    #pragma omp parallel for schedule(static)
    for (int pch = 0; pch < numpch; ++pch) {
        // 9. compute timestep for next cycle
       calcDtHydro(zdl, zvol, zvol0, dt, zfirst, zlast);
   } // for zch
    dr_app_stop();
} // end doCycle
```



```
#Point tools and Makefiles to your local install of DynamoRio
export DYNAMORIO_ROOT=~/DynamoRIO-Linux-8.0.18895
#Compile and link against this build of DynamoRio
icpc -O2 -I ~/DynamoRIO-Linux-8.0.18895/include -DLINUX -DX86_64 -qopenmp -c -o
build/Hydro.o src/Hydro.cc
linking build/pennant
icpc -o build/pennant <... .o files> build/Hydro.o -qopenmp -L ~/DynamoRIO-Linux-
8.0.18895/lib64/release/ -1 dynamorio
#Run with the custom dr-gs-trace tool. Currently we don't distinguish between threads
export OMP_NUM_THREADS=1
./DynamoRIO-Linux-8.0.18895/bin64/drrun -noinject -c ./dr-gather-scatter-
trace/client/build/libcount_client.so -- ./PENNANT/build/pennant
./PENNANT/test/sedovflat/sedovflat_1920.pnt &> pennant_sedovflat_1920.out
```

Using our tool, we can extract a G/S trace that we can then use for analysis and pattern synthesis.