Mnemônico	OpCode	Format	data_we	$register_we$	$alu_control$
ld	00000	11	0	1	0000
add	00000	10	0	1	0000
and	010001	10	0	1	0010
xor	000111	10	0	1	0100
srl	100110	10	0	1	0111