ELEC 2210 **PRE-LAB** COVER PAGE
Complete and attach this page to the front of your pre-lab.

Meeting #__1___ _____Simulation of Logic Circuits_____
Title of Lab Experiment

Student Name: _____Emerson, Gabriel, T_____
Name (Last, First, MI)

Student Email: _____gte0002@auburn.edu_____
AU 7-character username

GTA: _____Paul Atilola_____
Name of your GTA

Section you are enrolled in: (Circle One): 1 2 3 4 5 6 7 8

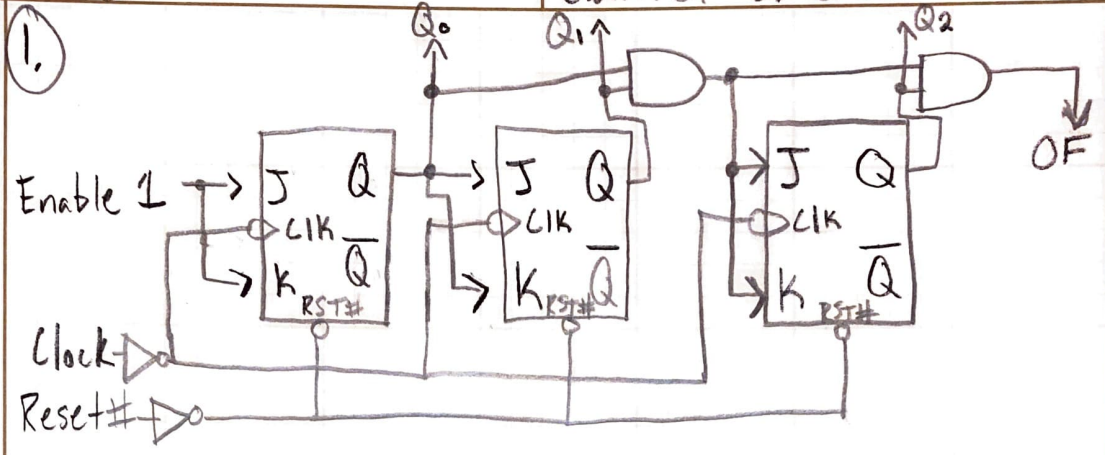
Date pre-lab submitted: (dd / mm / yy): _____12/01/21_____

I hereby certify that this pre-lab work was performed and written by me exclusively.

_____Gabriel Emerson_____ _____01/12/21_____
Student signature Date signed

Pre Lab 1 | Gabriel Emerson | 1/11

1.



2.

a) Low

b) Binary: 1101
Decimal: 13

*In order of Question
Q_D, Q_c, Q_b, Q_a

c.) Binary: 0010
Decimal: 2

d.) Borrow output sends a signal equal in width to the count down input when the counter Underflows

Carry output sends signal equal in width to the count down when the counter overflows