Gabriel Emerson
Section 001

1.) Write the truth table for 2 input
NAND Gates with input A, B and
output Z.

A	B	Z
0	0	1
0	1	1
1	0	1
1	1	0

2.) Use a KMAP to write SOP for
given truth table.

BC A	00	01	10	11
0	1	0	0	0
1	1	0	1	1

$\Rightarrow (\bar{B}\bar{C}) + (AB)$