Systems and Networking I

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- Concurrent accesses to shared resources by multiple cooperating processes/threads can lead to unexpected behavior
- Process/Thread cooperation must guarantee consistency of any shared data/resource, regardless of CPU scheduling
- Maintaining shared data consistency requires mechanisms to ensure synchronized execution of critical sections by processes/threads
- Critical sections are specific pieces of code which contain shared resources that need to be "protected"

We need to have appropriate "tools" (i.e., primitive constructs)

provided by programming languages

used as atomic building blocks for synchronization

- Locks → At each time, only one process holds a lock, executes its critical section, and finally releases the lock
- Semaphores → A generalization of locks
- Monitors → To connect shared data to synchronization primitives

Require some HW support and waiting

Locks

- Provide mutual exclusion to shared data using 2 atomic primitives:
 - lock.acquire() → wait until the lock is free, then grab it
 - lock.release() → unlock and wake up any thread waiting in acquire()
- Rules for using a lock:
 - Always acquire the lock before accessing shared data
 - Always release the lock after finishing with shared data
 - Lock must be initially free
- Only one process/thread can acquire the lock, others will wait!

Too Much Milk: Solution Using Locks

Use lock primitives

```
# Thread Bob
```

lock.acquire()

if (!milk):
 buy_milk()

lock.release()

```
# Thread Carla
```

lock.acquire()

if (!milk):
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This solution is clean and symmetric

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Q: How do we make acquire() and release() atomic?

HW Support for Synchronization

Implementing high-level synchronization primitives requires low-level hardware support

High-level atomic operations (SW)	lock, monitor, semaphore, send/receive
Low-level atomic operations (HW)	disabling interrupts, atomic instructions (test&set)

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We want to prevent the CPU scheduler to take control while an acquire() operation is ongoing

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 - internal events → discouraging threads from requesting any I/O operation within a critical section
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We cover all the possible cases where the current thread might loose control of the CPU, either voluntarily (due to internal events) or involuntarily (due to external events)

Implementing Locks: Disabling Interrupts Class Lock !

```
Class Lock {
  public void acquire(Thread t);
  public void release();

Lock() {}
}
```

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Class Lock {
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Lock() {}
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```
public void acquire(Thread t) {
    disable_interrupts();
}
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class Lock {
  public void acquire(Thread t);
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public void acquire(Thread t) {
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Implementing Locks: Disabling

Interrupts

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Class Lock {
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We need both acquire and release being implemented as system calls

```
public void acquire(Thread t) {
    disable_interrupts();
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Implementing Locks: Disabling

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Class Lock {
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Lock() {}
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Why?

```
public void acquire(Thread t) {
    disable_interrupts();
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public void release() {
    enable_interrupts();
}
```

- PROs:
 - Very simple!

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• CONs:

- Privileged instructions
- Trust no one abuses this capability, e.g.:
 - A greedy program that gets the lock and hog the CPU
 - A malicious program that gets the lock and goes into an infinite loop
- Does not work on multiprocessors!
- May loose relevant interrupts
- Masking/Unmasking interrupts is inefficient

```
Class Lock {
  public void acquire(Thread t);
  public void release();
  private int flag; // O=free; 1=busy

Lock() {
    this.flag = 0; // initially free
  }
}
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public void release() {
  this.flag = 0; // set the flag to free
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Does this solution work?

An unlucky (yet plausible) trace

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Thread 1 Thread 2

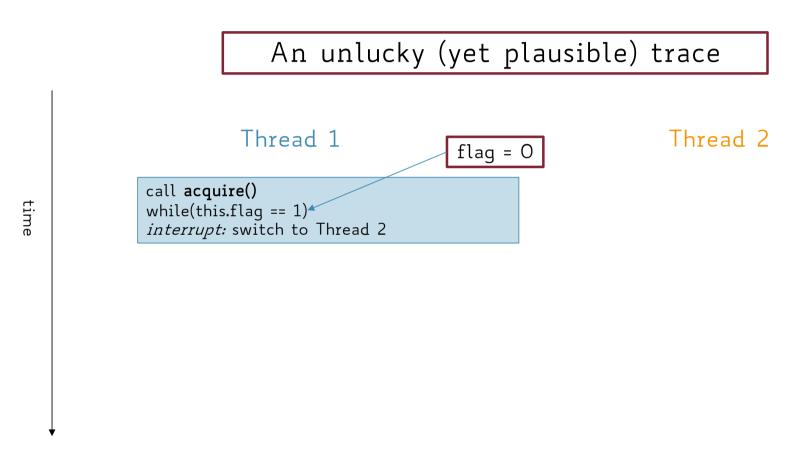
time

An unlucky (yet plausible) trace

Thread 1 Thread 2

call acquire()
while(this.flag == 1)

interrupt: switch to Thread 2



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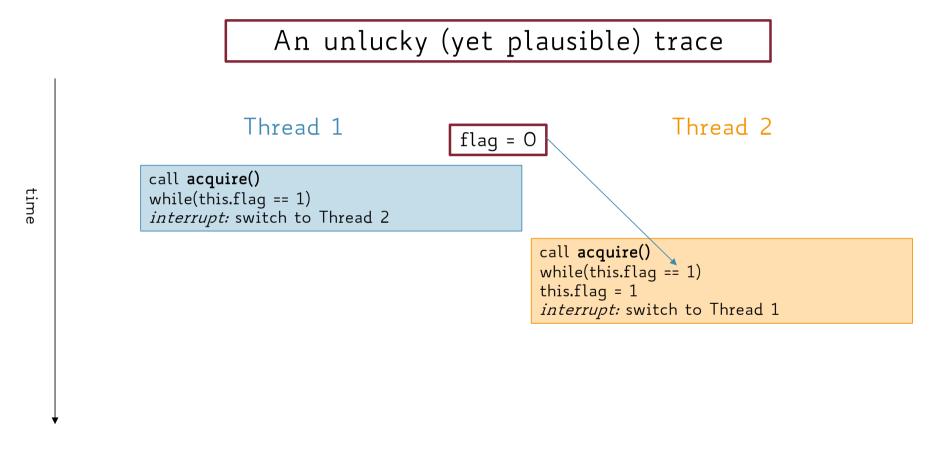
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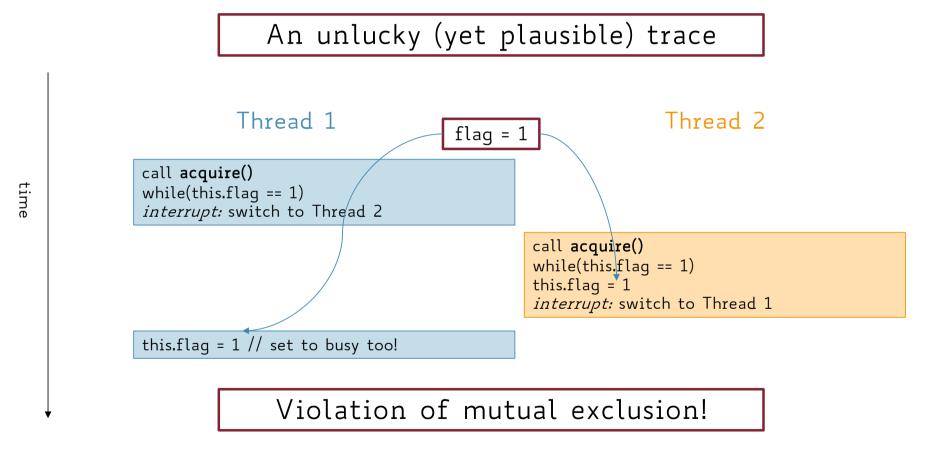
```
call acquire()
while(this.flag == 1)
this.flag = 1
interrupt: switch to Thread 1
```

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Implementing Locks: A First Attempt



Implementing Locks: A First Attempt

- Spin-Waiting is bad!
- The waiter thread will waste CPU cycles doing nothing
- On uniprocessors such a waste could be even worse
- The only thread holding the lock must take its turn on the CPU, otherwise the other(s) spin-waiting will never take it!
- What if the we have a single CPU with a non-preemptive scheduler?

HW Support for Synchronization

Implementing high-level synchronization primitives requires low-level hardware support

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 - On a uniprocessor → straightforward to implement adding a new instruction
 - On a multiprocessor → the processor issuing the instruction must also be able to invalidate any copies of the value other processes may have in their cache

- Examples:
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Lock() {
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  }
}
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Class Lock {
  public void acquire();
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Lock() {
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}
```

```
public void acquire() {
  while(test&set(this.flag) == 1) {
    // while busy do nothing
  }
}
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The lock is now busy, the boolean expression in the while guard is false and acquire terminates

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Class Lock {
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Lock() {
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}
```

```
public void acquire() {
  while(test&set(this.flag) == 1) {
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public void release() {
  this.flag = 0;
}
```

```
Class Lock {
  public void acquire();
  public void release();
  private int flag;

Lock() {
    // lock is initially free
    this.flag = O;
  }
}
```

```
public void acquire() {
  while(test&set(this.flag) == 1) {
    // while busy do nothing
  }
}
```

```
public void release() {
  this.flag = 0;
}
```

The lock is still busy, the boolean expression in the while guard is true and acquire continues to loop until release executes

```
public void acquire() {
  while(test&set(this.value) == 1) {
    // while busy do nothing
  }
}
```

• What's wrong with the above implementation?

```
public void acquire() {
  while(test&set(this.value) == 1) {
    // while busy do nothing
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}
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- What's wrong with the above implementation?
 - What is the CPU doing?

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busy waiting (spin-lock)

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 - What is the CPU doing?
 - What could happen to threads with different priorities waiting for the lock?

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public void acquire() {
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}
```

who is going to take the lock once released?

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 - What is the CPU doing?
 - What could happen to threads with different priorities waiting for the lock?

Masking Interrupts vs. Atomic Instructions

- 3 main problems with disabling interrupts:
 - overhead as it requires kernel privileges
 - trusted code (only OS kernel code can use this mechanism)
 - unfeasible with mulitprocessor architectures

Masking Interrupts vs. Atomic Instructions

- 3 main problems with disabling interrupts:
 - overhead as it requires kernel privileges
 - trusted code (only OS kernel code can use this mechanism)
 - unfeasible with mulitprocessor architectures
- 3 main problems with atomic instructions:
 - busy waiting (spin-lock)
 - unfairness as there is no queue where threads wait for the lock to be released
 - performance as N-1 waiter threads may spin for one round each before the thread holding the lock gets its turn on the CPU

Reduce Busy-Waiting: Yield!

```
Class Lock {
  public void acquire();
  public void release();
  private int flag;

Lock() {
    // lock is initially free this.flag = 0;
  }
}
```

```
public void acquire() {
  while(test&set(this.flag) == 1) {
    yield(); // give up the CPU
  }
}
```

```
public void release() {
  this.flag = 0;
}
```

An OS primitive (yield) that allows threads to give up the CPU

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An OS primitive (yield) that allows threads to give up the CPU

In this way, the waiter thread releases the CPU immediately

Still, some threads may starve in an endlessly yielding!

```
Class Lock {
  public void acquire(Thread t);
  public void release();
  private int flag;
  private int guard;
  private Queue q;

Lock() {
    // lock is initially free
    this.flag = 0;
    this.guard = 0;
    this.q = new Queue();
  }
}
```

```
public void acquire(Thread t) {
   while(test&set(this.guard) == 1) {
      // while busy do nothing
   }
   if(this.flag == 0) {
      this.flag = 1; // lock is taken
      this.guard = 0;
   }
   else {
      this.q.push(t);
      this.guard = 0;
      park(); // Solaris primitive
   }
}
```

```
public void release() {
   while(test&set(this.guard) == 1) {
      // while busy do nothing
   }
   if(q.is_empty()) {
      this.flag = 0; // lock is free
   }
   else {
      t = q.pop();
      unpark(t); // Solaris primitive
   }
   this.guard = 0;
}
```

park → Put the caller thread to sleep if it tries to acquire a busy lock

unpark → Wake up the waiter when the lock is free

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   }
   this.guard = 0;
}
```

NOTE: The flag is not set to O when another thread is woken up

The thread releasing the lock passes it directly to the next thread

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  }
  else {
    t = q.pop();
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  }
  this.guard = 0;
}
```

We can't totally get rid of busy-waiting but we can make it **independent** on how long is the critical section delimited by acquire and release

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Class Lock {
  public void acquire(Thread t);
  public void release();
  private int flag;
  private int guard;
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```
public void release() {
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```

What would happen if an unlucky thread switch happens right before calling park() and the incoming thread releases the lock?

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   }
   else {
      this.q.push(t);
      setpark(); // Solaris primitive
      this.guard = 0;
      park(); // Solaris primitive
   }
}
```

```
public void release() {
  while(test&set(this.guard) == 1) {
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```

Solution:

Use another system call to tell the OS the thread is about to park

Locks: Wrap Up

- Synchronization primitives ensure that only one process/thread at a time executes in a critical section (mutual exclusion)
- Locks allow protection of critical sections by atomically testing and taking/releasing the access to a critical section
- Locks can be implemented leveraging some HW support:
 - disabling interrupts (can miss or delay important events)
 - atomic instructions (busy waiting/spin-lock inefficient)

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- More general synchronization mechanisms
 - Not only for safely accessing critical sections
- 2 common high-level synchronization primitives:
 - Semaphores: binary (mutex) and counting
 - Monitors: mutex and condition variables

 Another data structure that provides mutual exclusion to critical sections

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- Another data structure that provides mutual exclusion to critical sections
- Can also play the role of an atomic counter
- Generalization of locks invented by Dijkstra in 1965
- Special type of (integer) variable that supports 2 atomic operations
 - wait() (also P()): decrement, block until semaphore is open
 - signal() (also V()): increment, allow another thread to enter

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- Then signal() opens the semaphore:
 - If a thread is waiting on the queue the thread is unblocked, whilst if no threads are waiting on the queue, the signal is remembered for the next thread

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- Then signal() opens the semaphore:
 - If a thread is waiting on the queue the thread is unblocked, whilst if no threads are waiting on the queue, the signal is remembered for the next thread
- In other words, signal() is stateful and has "history"

Semaphores: Types

- Binary Semaphore a.k.a. Mutex (same as a Lock)
 - Guarantees mutually exclusive access to a resource (i.e., only one process/thread executes in a critical section)
 - Its associated integer variable can only take 2 values: 0/1
 - Initialized to open (e.g., value = 1)

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Counting Semaphore

- To manage multiple shared resources
- The semaphore is initially set to the number of resources
- A process can access to a resource as long as at least one is available

```
// Semaphore S
S.wait(); // wait until S is available
<critical section>
S.signal(); notify other processes that S is open
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Each semaphore supports a queue of processes that are waiting to access the critical section (e.g., to buy milk)

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If a process executes S.wait() and semaphore S is open (non-zero), it continues executing, otherwise the OS puts the process on the wait queue

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If a process executes S.wait() and semaphore S is open (non-zero), it continues executing, otherwise the OS puts the process on the wait queue

A S.signal() unblocks one process on semaphore S's wait queue

Binary Semaphore: Example

"Too Much Milk" Using Lock

```
# Thread Bob
```

lock.acquire()

if (!milk):
 buy_milk()

lock.release()

```
# Thread Carla
```

lock.acquire()

if (!milk):
 buy_milk()

lock.release()

Binary Semaphore: Example

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if (!milk):
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"Too Much Milk" Using Semaphore

Thread Bob

S.wait()

if (!milk): buy_milk()

S.signal()

Thread Carla

S.wait()

if (!milk): buy_milk()

S.signal()

Binary Semaphore: Example

	"Too Much Milk" Using Lock		"Too Much Milk" Using Semaphore		3	
#	Thread Bob		# Thread Carla	# Thread Bob		# Thread Carla
lo	ock.acquire()		lock.acquire()	S.wait()		S.wait()
if	(!milk): buy_milk()		if (!milk): buy_milk()	if (!milk): buy_milk()		if (!milk): buy_milk()
lo	ock.release()		lock.release()	S.signal()		S.signal()

```
Class Semaphore {
  public void wait(Thread t);
  public void signal();
  private int value;
  private int guard;
  private Queue q;

Semaphore(int val) {
    // initialize semaphore
    // with val and empty queue
    this.value = val;
    this.q = new Queue();
  }
}
```

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  public void wait(Thread t);
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Semaphore(int val) {
    // initialize semaphore
    // with val and empty queue
    this.value = val;
    this.q = new Queue();
  }
}
```

```
public void wait(Thread t) {
  while(test&set(this.guard) == 1) {
    // while busy do nothing
  }
  this.value -= 1;
  if(this.value < 0) {
    q.push(t);
    park();
  }
  else {
    this.guard = 0;
  }
}</pre>
```

```
Class Semaphore {
  public void wait(Thread t);
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   if(this.value < 0) {
      q.push(t);
      park();
   }
   else {
      this.guard = 0;
   }
}</pre>
```

```
public void signal() {
  while(test&set(this.guard) == 1) {
    // while busy do nothing
  }
  this.value += 1;
  if(!q.isEmpty()) {
    t = q.pop();
    unpark(t);
  }
  this.guard = 0;
}
```

```
Class Semaphore {
  public void wait(Thread t);
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   }
   this.value -= 1;
   if(this.value < 0) {
      q.push(t);
      park();
   }
   else {
      this.guard = 0;
   }
}</pre>
```

```
public void signal() {
  while(test&set(this.guard) == 1) {
    // while busy do nothing
  }
  this.value += 1;
  if(!q.isEmpty()) {    // this.value <= 0
    t = q.pop();
    unpark(t);
  }
  this.guard = 0;
}</pre>
```

```
Class Semaphore {
  public void wait(Thread t);
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  private int value;
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   }
   this.guard = 0;
}</pre>
```

wait() and signal() are of course atomic!

S.wait()
S.wait()
S.signal()
S.signal()

S = 2

B S.wait()
S.signal()

A S.wait()
S.wait()
S.signal()
S.signal()

S = 2

S.wait() S.signal()

A possible execution flow

S (value)	Queue	А	В
2	Ø	ready to exec	ready to exec

S.wait()
S.wait()
S.signal()
S.signal()

S.wait() S.signal()

A possible execution flow

A: S.wait()

,	S (value)	Queue	А	В
	2	Ø	ready to exec	ready to exec
)				

S.wait()
S.wait()
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S.signal()

S.wait() S.signal()

A possible execution flow

A: S.wait()

S (value)	Queue	А	В
2	Ø	ready to exec	ready to exec
1	Ø	ready to exec	ready to exec

S.wait()
S.wait()
S.signal()
S.signal()

S.wait() S.signal()

A possible execution flow

A: S.wait()

B: S.wait()

S (value)	Queue	А	В	
2	Ø	ready to exec	ready to exec	
1	Ø	ready to exec	ready to exec	

99

S.wait()
S.wait()
S.signal()
S.signal()

S.wait() S.signal()

A possible execution flow

A: S.wait()

B: S.wait()

J	S (value)	Queue	А	В
	2	Ø	ready to exec	ready to exec
)	1	Ø	ready to exec	ready to exec
)	0	Ø	ready to exec	ready to exec

S.wait()
S.wait()
S.signal()
S.signal()

S.wait() S.signal()

A possible execution flow

A: S.wait()

B: S.wait()

A: S.wait()

W	S (value)	Queue	Α	В
	2	Ø	ready to exec	ready to exec
()	1	Ø	ready to exec	ready to exec
()	0	Ø	ready to exec	ready to exec
t()				

S.wait()
S.wait()
S.signal()
S.signal()

S.wait() S.signal()

S (value) A possible execution flow Queue ready to exec ready to exec A: S.wait() ready to exec ready to exec B: S.wait() 0 Ø ready to exec ready to exec A: S.wait() -1 blocked ready to exec

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S.wait()
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S.signal()
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S.wait() S.signal()

A possible execution flow	S (value)	Queue	А	В
	2	Ø	ready to exec	ready to exec
A: S.wait()	1	Ø	ready to exec	ready to exec
B: S.wait()	0	Ø	ready to exec	ready to exec
A: S.wait()	-1	Α	blocked	ready to exec
B: S.signal()	0	Ø	ready to exec	ready to exec

S.wait()
S.wait()
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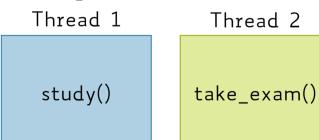
S.wait() S.signal()

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B: S.wait()	0	Ø	ready to exec	ready to exec
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B: S.signal()	0	Ø	ready to exec	ready to exec
A: S.signal()	1	Ø	ready to exec	ready to exec
A: S.signal()	2	Ø	ready to exec	ready to exec

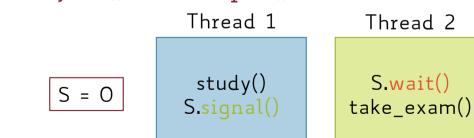
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 - The initial value of the semaphore is set to 1
 - Call wait() before the critical section, signal() after the critical section

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 - Example → join() or waitpid()

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 - Example → join() or waitpid()



Producer-Consumer

Producer Process:

```
while (true)
{
    /* produce an item in nextProduced */
    while (counter == BUFFER_SIZE)
        ; /* do nothing */
    buffer[in] = nextProduced;
    in = (in + 1) % BUFFER_SIZE;
    counter++;
}
```

Both the producer and the consumer share a common buffer (of items)

Consumer Process:

```
while (true)
{
    while (counter == 0)
        ; /* do nothing */
    nextConsumed = buffer[out];
    out = (out + 1) % BUFFER_SIZE;
    counter--;
    /* consume the item in nextConsumed */
}
```

Producer-Consumer

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    counter--;
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}
```

Both the producer and the consumer share a common buffer (of items)

counter keeps track of the number of items currently in the buffer

possible race condition as counter can be updated by the producer and consumer

Producer-Consumer: Race Condition

Producer:

```
register<sub>1</sub> = counter
register<sub>1</sub> = register<sub>1</sub> + 1
counter = register<sub>1</sub>
```

Consumer:

```
register<sub>2</sub> = counter
register<sub>2</sub> = register<sub>2</sub> - 1
counter = register<sub>2</sub>
```

Interleaving:

Assuming the initial value of counter is 5

```
T_0: producer
                              register_1 = counter
                                                            \{register_1 = 5\}
                  execute
T<sub>1</sub>: producer
                              register_1 = register_1 + 1
                                                            \{register_1 = 6\}
                  execute
T<sub>2</sub>: consumer execute
                              register_2 = counter
                                                            \{register_2 = 5\}
T<sub>3</sub>: consumer execute
                              register_2 = register_2 - 1
                                                            \{register_2 = 4\}
                              counter = register_1
                                                             \{counter = 6\}
T_4: producer
                  execute
                              counter = register_2
                                                             \{counter = 4\}
T<sub>5</sub>: consumer
                 execute
```

Producer-Consumer: Race Condition

Producer:

```
register<sub>1</sub> = counter
register<sub>1</sub> = register<sub>1</sub> + 1
counter = register<sub>1</sub>
```

Consumer:

```
register<sub>2</sub> = counter
register<sub>2</sub> = register<sub>2</sub> - 1
counter = register<sub>2</sub>
```

Interleaving:

Assuming the initial value of counter is 5

```
register_1 = counter
                                                          \{register_1 = 5\}
T_0: producer
                 execute
                                                         \{register_1 = 6\}
T_1: producer
                            register_1 = register_1 + 1
                 execute
T2: consumer execute
                            register_2 = counter
                                                          \{register_2 = 5\}
T<sub>3</sub>: consumer execute
                            register_2 = register_2 - 1
                                                         \{register_2 = 4\}
                             counter = register_1
    producer
                                                          \{counter = 6\}
                 execute
                 execute
                             counter = register_2
                                                          \{counter = 4\}
T<sub>5</sub>: consumer
```

Q1: What would be the resulting value of counter if the order of statements T4 and T5 were reversed?

Producer-Consumer: Race Condition

Producer:

```
register<sub>1</sub> = counter
register<sub>1</sub> = register<sub>1</sub> + 1
counter = register<sub>1</sub>
```

Consumer:

```
register<sub>2</sub> = counter
register<sub>2</sub> = register<sub>2</sub> - 1
counter = register<sub>2</sub>
```

Interleaving:

Assuming the initial value of counter is 5

```
register_1 = counter
                                                         \{register_1 = 5\}
T_0: producer
                 execute
                            register_1 = register_1 + 1
                                                         \{register_1 = 6\}
T_1: producer
                 execute
T<sub>2</sub>: consumer execute
                            register_2 = counter
                                                         \{register_2 = 5\}
T<sub>3</sub>: consumer execute
                            register_2 = register_2 - 1
                                                         \{register_2 = 4\}
                            counter = register_1
                                                          \{counter = 6\}
    producer
                 execute
                            counter = register_2
                                                          \{counter = 4\}
    consumer execute
```

Q2: What should the value of counter be after one producer and one consumer, assuming the original value was 5?

Producer-Consumer: Desiderata

Mutual Exclusion

 Access to the shared buffer of items must be granted to a single thread at a time (either the producer or the consumer)

Producer-Consumer: Desiderata

Mutual Exclusion

 Access to the shared buffer of items must be granted to a single thread at a time (either the producer or the consumer)

Scheduling Constraints

- Producer can put a new item iff the buffer is not full
- Consumer can take an item iff the buffer is not empty

Producer-Consumer in Java

Semaphores: Wrap Up

- Generalization of locks
- Can be used for 3 purposes:
 - To ensure mutually exclusive execution of a critical section as locks do (binary semaphore)
 - To control access to a shared pool of resources (counting semaphore)
 - To enforce scheduling constraints so as to execute threads according to some specific order