Systems and Networking – Unit I

B.Sc. in Applied Computer Science and Artificial Intelligence 2022-2023

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Recap from Last Lecture

- Process is the unit of execution (running on a single CPU)
- OS keeps track of process-related information using an ad hoc data structure called Process Control Block (PCB)
- Process can be in one of 5 possible states: new, ready, waiting, running, or terminated
- Context switch to intertwine the execution of multiple processes
- Process communication either via message passing or shared memory

Today: CPU Scheduling

Policy to establish which process to execute on the CPU

- Basic scheduling concepts
- Scheduling criteria/metrics
- Scheduling algorithms
- Advanced scheduling concepts

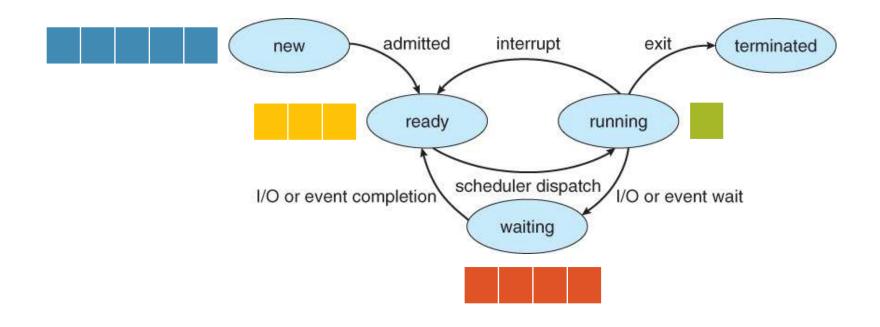
- Almost every program has some alternating cycles of CPU computations and I/O waiting
- Even a simple fetch from main memory takes a long time relatively to CPU speed
- Our assumptions: Multi-programmed, uni-processor system

• In a system running a single process, the time spent waiting for I/O is wasted, and those CPU cycles are lost forever

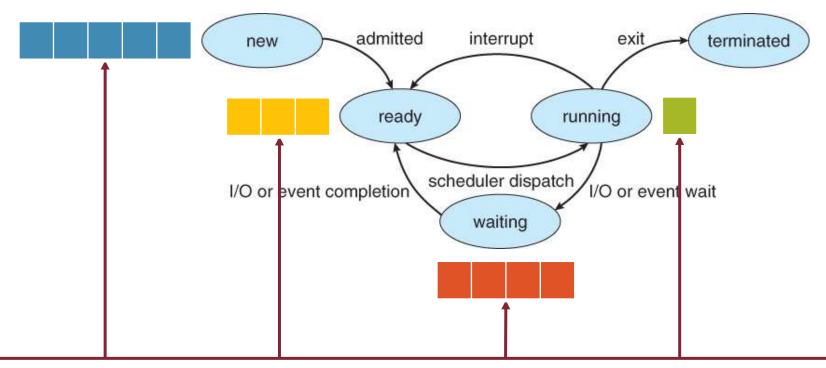
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- A scheduling system allows one process to use the CPU while another is waiting for I/O, thereby maximizing system utilization
- Challenge: Make the system as "efficient" and "fair" as possible, subject to varying and often dynamic conditions

Process Execution State Diagram

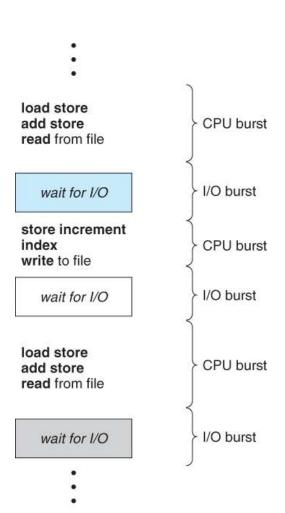


Process Execution State Diagram



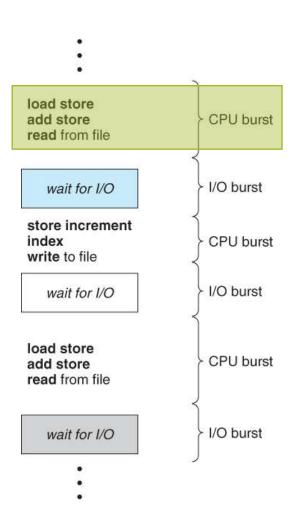
Processes managed by the OS reside in exactly one of the state queues

CPU vs. I/O Burst Cycle



All processes alternate between two states in a continuing cycle: CPU burst and I/O burst

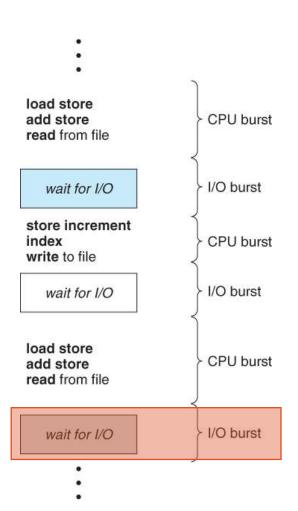
CPU vs. I/O Burst Cycle



All processes alternate between two states in a continuing cycle: CPU burst and I/O burst

CPU burst → performing calculations

CPU vs. I/O Burst Cycle

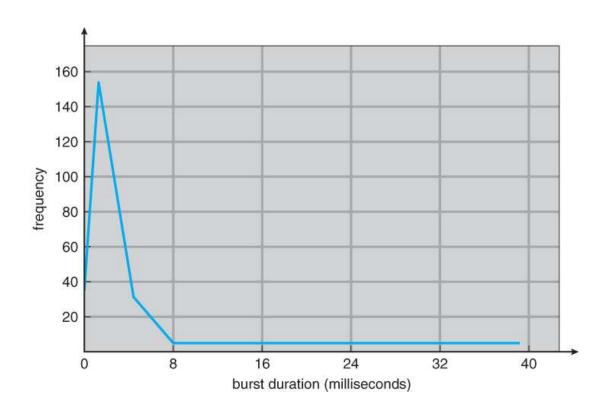


All processes alternate between two states in a continuing cycle: CPU burst and I/O burst

I/O burst → waiting for data transfer in or out of the system

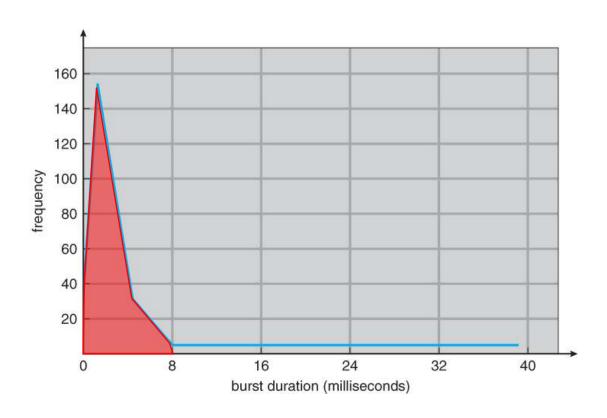
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CPU Burst Cycle: Frequency Pattern



Highly skewed distribution

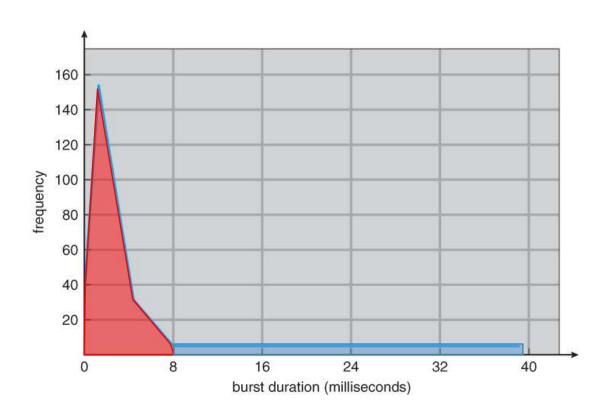
CPU Burst Cycle: Frequency Pattern



Highly skewed distribution

The vast majority of processes have short CPU bursts

CPU Burst Cycle: Frequency Pattern



Highly skewed distribution

The vast majority of processes have short CPU bursts

Few processes exhibit very long CPU bursts

Long- vs. Short-term Scheduling

Long-term scheduling

How does the OS determine the level of multiprogramming

(i.e., the number of processes to be loaded in main memory)

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CPU Scheduler

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- The data structure for the ready queue and the algorithm used to select the next process are not necessarily based on a FIFO queue

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Policy goals vs. Mechanism implementations

CPU scheduling decisions take place under one of 4 conditions:

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I. When a process switches from the running state to the waiting state

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for an I/O request or invocation of the **wait** system call

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- 2. When a process switches from the running state to the ready state

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in response to an interrupt

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after I/O completion or a return from wait

CPU scheduling decisions take place under one of 4 conditions:

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- 4. When a process is created or terminates

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No choice!
A new process must be selected

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Either continue with the current process or select a new one

Non-preemptive vs. Preemptive

Non-preemptive scheduling

If it takes place only when there is no choice (i.e., conditions | and 4)

Once a process starts it keeps running until it either voluntarily blocks or it finishes

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Preemptive scheduling

Whenever scheduling takes also place under conditions 2 and 3

Non-preemptive vs. Preemptive: Examples

	Windows	Mac	UNIX-like
Non-preemptive	up to Win 3.x	up to Mac OS 9.x	-
Preemptive	since Win 95	since Mac OS X	since forever

Preemption: Issues

- Preemption might cause troubles if it occurs while:
 - the kernel is busy implementing a system call (e.g., updating critical kernel data structures)
 - two processes share data, one may get interrupted in the middle of updating shared data structures

Preemption: Issues

- Possible countermeasures:
 - Make the process wait until the system call has either completed or blocked before allowing the preemption

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• Disable interrupts before entering critical code section and re-enabling immediately afterwards

should only be done in rare situations, and only on very short pieces of code that will finish quickly

The Dispatcher

• The module that gives control of the CPU to the process selected by the scheduler

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- The module that gives control of the CPU to the process selected by the scheduler
- Its functions include:
 - Switching context
 - Switching to user mode
 - Jumping to the proper location in the newly loaded program
- The dispatcher is run on every context switch therefore the time it consumes (dispatch latency) must be as shortest as possible

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NOTE

I/O waiting time is **not** considered here!

```
T^{arrival} = \text{arrival time}
T^{completion} = \text{completion time}
T^{burst} = \text{burst time}
T^{turnaround} = \text{tournaround time} = T^{completion} - T^{arrival}
T^{waiting} = \text{waiting time} = T^{turnaround} - T^{burst}
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51

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61

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Scheduling: Trade-off

- Ideally, choose a CPU scheduler that optimizes all metrics simultaneously
- Generally, the above is impossible and a trade-off is needed!
- Idea: Choose a scheduling algorithm based on its ability to satisfy a given policy

- Minimize average response time
 - Provide output to the user as quickly as possible

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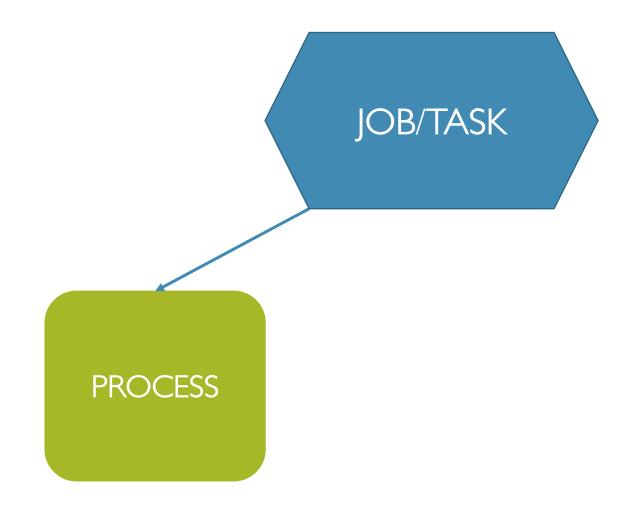
Typical of batch systems

A Quick Note on Terminology

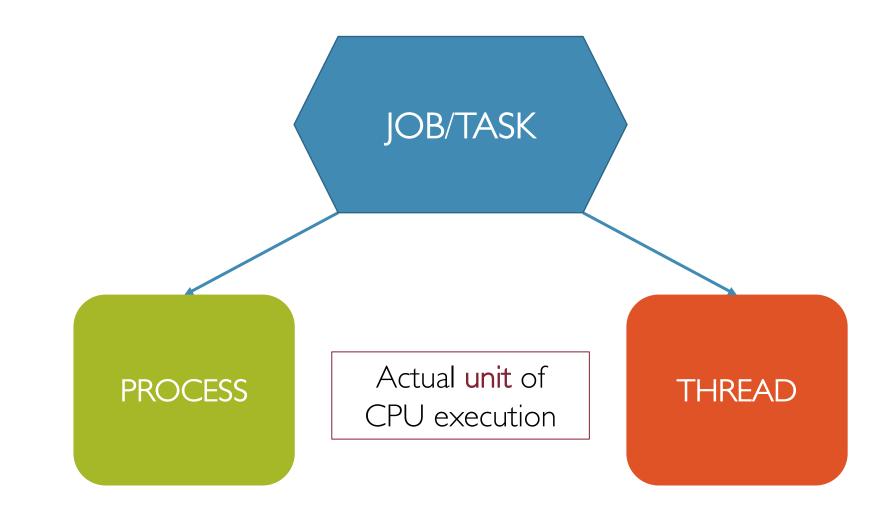
JOB/TASK

General unit of CPU execution

A Quick Note on Terminology



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• One process per user

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We will talk about threads very soon but for now most of the things we will be discussing remain valid even on a multi-threaded system

Scheduling Algorithms: An Overview

- First-Come-First-Serve (FCFS)
- Round Robin (RR)
- Shortest-Job-First (SJF)
- Priority Scheduling
- Multilevel Queue (MQ)
- Multilevel Feedback-Queue (MFQ)

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First-Come-First-Serve (FCFS)

- Very simple! Just a FIFO queue, like customers waiting in line at the post office
- The scheduler executes jobs to completion in arrival order
- The scheduler takes over only when the currently running job asks for an I/O operation (or finishes its execution)
- A job may keep using the CPU indefinitely (i.e., until it blocks)

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Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



Order	Job	CPU burst (time units)
T	Α	5
2	В	2
3	С	3



Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3

Arrival time = 0 for all*

No I/O burst

^{*} Actually, in this example, A arrives first, then B, and finally C comes: arrival time differences are considered negligible











Running

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3







Waiting



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3





Ready C

Waiting

Running B

Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3





Ready

Waiting

Running C

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



Average Waiting Time

N = number of jobs

```
\begin{split} T_i^{arrival} &= \text{arrival time of job } i \\ T_i^{completion} &= \text{completion time of job } i \\ T_i^{burst} &= \text{burst time of job } i \\ T_i^{turnaround} &= \text{tournaround time of job } i = T_i^{completion} - T_i^{arrival} \end{split}
```

$$\overline{T}^{waiting} = \text{avg. waiting time}$$
 $= \frac{1}{N} \sum_{i=1}^{N} (T_i^{turnaround} - T_i^{burst})$

Unless otherwise specified, we will assume all jobs arrive at the same time, i.e.,

$$T_i^{arrival} = 0 \ \forall i \in \{1, \dots, N\}$$

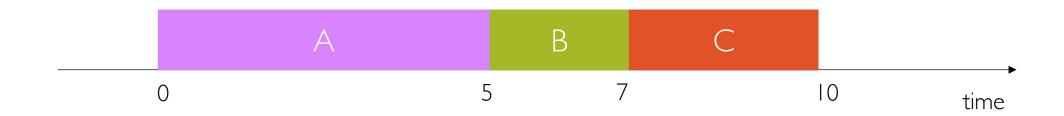


Ready

Waiting

Running

Order	Job	CPU burst (time units)
I	Α	5
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94

avg. waiting time =



Ready

Waiting

Running

Order	Job	CPU burst (time units)
I	А	5
2	В	2
3	С	3



avg. waiting time =
$$(0 + 5 + 7)/3 = 4$$



Order	Job	CPU burst (time units)
	В	2
2	С	3
3	Α	5

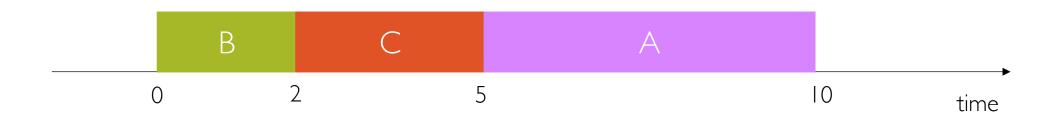


No I/O burst





Order	Job	CPU burst (time units)
I	В	2
2	С	3
3	Α	5



avg. waiting time =
$$(5 + 0 + 2)/3 \sim 2.3$$



Ready



Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



Ready



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3

A does also I/O







Waiting



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3







Ready C

Waiting A

Running B

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3

A does also I/O





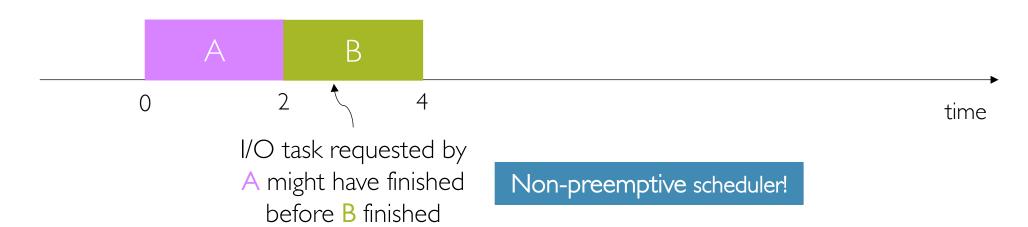
Ready C

Waiting A

Running B

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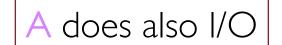




Waiting



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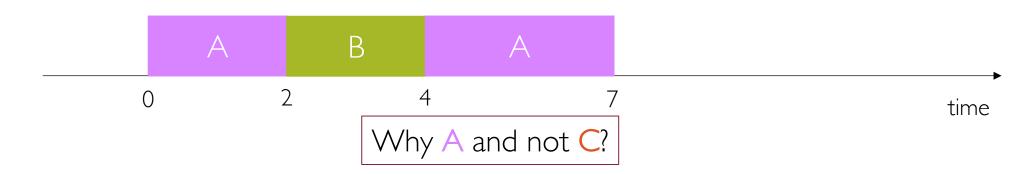


Waiting



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Ready C

Waiting

Running A

Order	Job	CPU burst (time units)
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A does also I/O



Because the FCFS scheduler cares only about the **arrival time** on the **ready** queue



Ready

Waiting

Running C

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Ready

Waiting

Running

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107



avg. waiting time =



Ready

Waiting

Running

Order	Job	CPU burst (time units)
	А	5
2	В	2
3	С	3





avg. waiting time =
$$(2 + 2 + 7)/3 \sim 3.7$$

First-Come-First-Serve (FCFS): Scenario III



Ready

Waiting

Running

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3

A does also I/O



NOTE:

We should remove from A's waiting time the time it spent doing I/O

FCFS: PROs and CONs

• PRO:

• very simple!

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• PRO:

very simple!

• CONs:

 (average) waiting time is highly variable as short CPU-burst jobs may sit behind very long ones

FCFS: PROs and CONs

• PRO:

very simple!

CONs:

- (average) waiting time is highly variable as short CPU-burst jobs may sit behind very long ones
- convoy effect → poor overlap between CPU and I/O since CPU-bound jobs will force I/O bound jobs to wait

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Round Robin (RR)

- Similar to FCFS, except that CPU bursts are assigned with limits called time quantum or (time slice)
- When a job is given the CPU, a timer is set for a certain value:
 - If the job finishes before the time quantum expires, then it is swapped out of the CPU just like the normal FCFS algorithm
 - If the timer goes off first, then the job is swapped out of the CPU and moved to the back end of the ready queue
- Used in many time-sharing systems in combination with timer interrupts

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Round Robin (RR)

- The ready queue is maintained as a circular queue
- When all jobs have had a turn, the scheduler gives the first job another turn, and so on...
- RR is fair as it shares the CPU equally among all the jobs
- The average waiting time can be longer than with other scheduling algorithms

• The performance of RR is sensitive to the time quantum selected

- The performance of RR is sensitive to the time quantum selected
- Too large time quantum degenerates to FCFS, as jobs are never preempted from the CPU (high average waiting time)

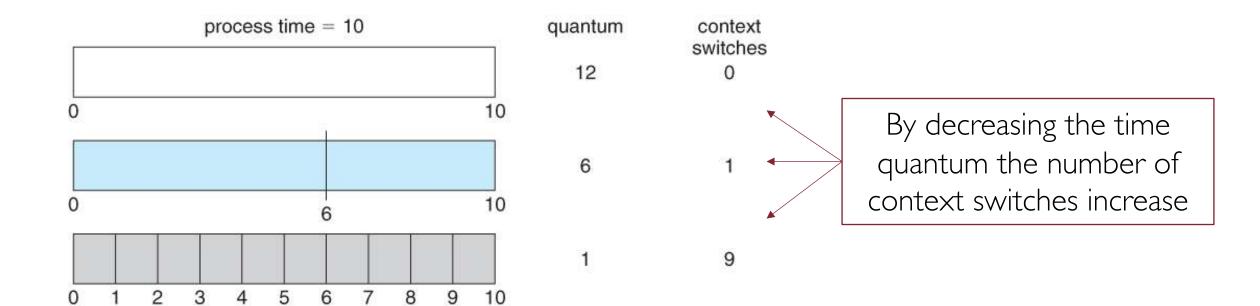
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Trade-off:

Overhead for context switching should be relatively small compared to time slice

Example: time slice = $10 \div 100$ msec. and context switch = $0.01 \div 0.1$ msec.



N = number of jobs

 $\delta = \text{time slice}$

$$\sup\{T_i^{start}\} = \delta * (i-1), \ \forall i \in \{1, \dots, N\}$$

upper-bound on the time a job is scheduled for the first time

worst-case scenario:

all job in front of the queue will use the whole time slice



Order	Job	CPU burst (time units)
I	A	5
2	В	2
3	С	3



Order	Job	CPU burst (time units)
1	Α	5
2	В	2
3	С	3

No I/O burst

Time quantum = 2

Context switch = 0









Waiting

Running

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3







Waiting



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3









Waiting



Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



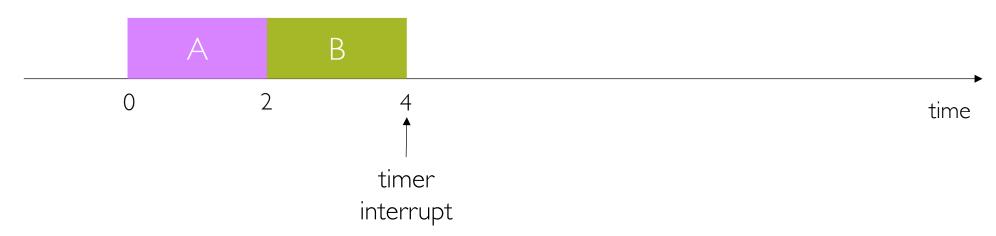




Waiting

Running B

Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3



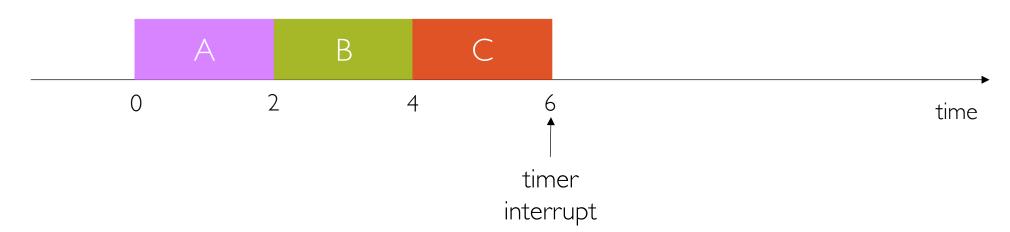


Ready A

Waiting

Running C

Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3



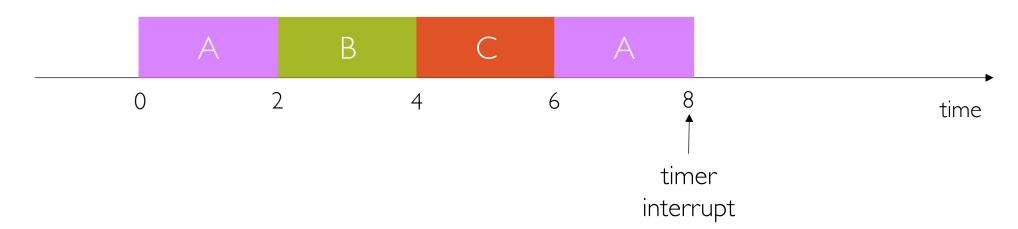


Ready C

Waiting



Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



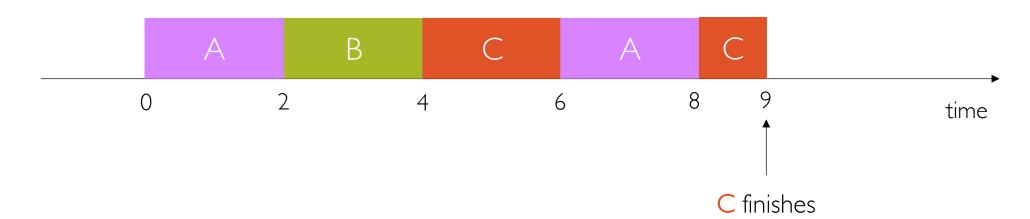




Waiting



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3



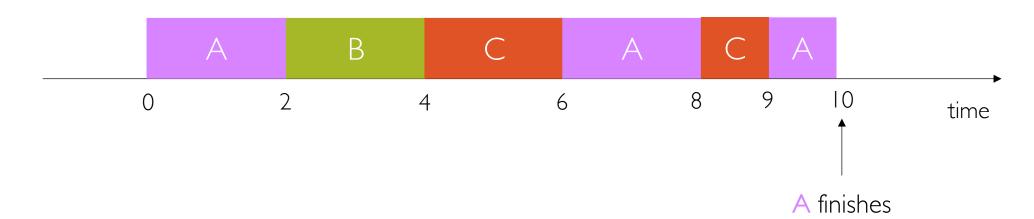


Ready

Waiting



Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3



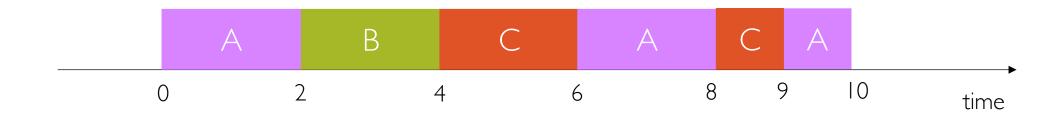


Ready

Waiting

Running

Order	Job	CPU burst (time units)
	Α	5
2	В	2
3	С	3



avg. waiting time =

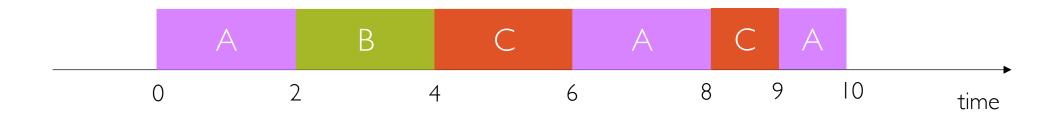


Ready

Waiting

Running

Order	Job	CPU burst (time units)
I	Α	5
2	В	2
3	С	3



avg. waiting time =
$$(5 + 2 + 6)/3 \sim 4.3$$

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	100				
В	100				
С	100				
D	100				
Е	100				
Avg.					

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	100	100			
В	100	200			
С	100	300			
D	100	400			
Е	100	500			
Avg.		300			

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	100	100	496		
В	100	200	497		
С	100	300	498		
D	100	400	499		
Е	100	500	500		
Avg.		300	498		

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
А	100	100	496	0	
В	100	200	497	100	
С	100	300	498	200	
D	100	400	499	300	
Е	100	500	500	400	
	Avg.	300	498	200	

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	_
Job	CPU burst	FCFS	RR	FCFS	RR
Α	100	100	496	0	396
В	100	200	497	100	397
С	100	300	498	200	398
D	100	400	499	300	399
Е	100	500	500	400	400
Avg.		300	498	200	398

Assumptions:

5 jobs, 100 time units of CPU burst each

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	_
Job	CPU burst	FCFS	RR	FCFS	RR
Α	100	100	496	0	396
В	100	200	497	100	397
С	100	300	498	200	398
D	100	400	499	300	399
Е	100	500	500	400	400
	Avg.	300	498	200	398

FCFS seems to outperform RR in both metrics but... is it fair?

Look at the variance rather than the average!

Assumptions:

5 jobs, different CPU burst

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	50				
В	40				
С	30				
D	20				
Е	10				
	Avg.				

Assumptions:

5 jobs, different CPU burst

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	50	50			
В	40	90			
С	30	120			
D	20	140			
Е	10	150			
	Avg.	110			

Assumptions:

5 jobs, different CPU burst

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	
Job	CPU burst	FCFS	RR	FCFS	RR
Α	50	50	150		
В	40	90	140		
С	30	120	120		
D	20	140	90		
Е	10	150	50		
	Avg.	110	110		

Assumptions:

5 jobs, different CPU burst

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	_
Job	CPU burst	FCFS	RR	FCFS	RR
Α	50	50	150	0	
В	40	90	140	50	
С	30	120	120	90	
D	20	140	90	120	
Е	10	150	50	140	
	Avg.	110	110	80	

Assumptions:

5 jobs, different CPU burst

Time quantum = I

Context switch = 0

Arrival time = 0 (for all jobs)

		turnaround time		wait tim	_
Job	CPU burst	FCFS	RR	FCFS	RR
Α	50	50	150	0	100
В	40	90	140	50	100
С	30	120	120	90	90
D	20	140	90	120	70
Е	10	150	50	140	40
	Avg.	110	110	80	80

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- Scheduling allows one process to use the CPU while another is waiting for I/O, thereby maximizing system utilization
- non-preemptive vs. preemptive scheduler
- Different scheduling policies optimize different metrics
- 2 out of 6 scheduling algorithms:
 - First-Come-First-Serve (FCFS)
 - Round Robin (RR)