Systems and Networking – Unit I

B.Sc. in Applied Computer Science and Artificial Intelligence 2022-2023

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Recap from Last Lecture

- Synchronization **primitives**:
 - Locks
 - Semaphores
 - Monitors

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 - Locks
 - Semaphores
 - Monitors
- 2 fundamental synchronization problems:
 - Producers-Consumers
 - Readers-Writers

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- 5 philosophers sitting at a round table

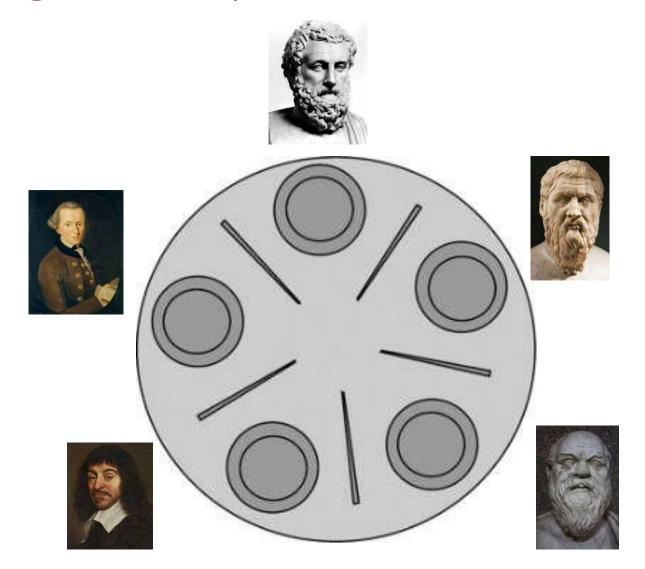
- It's lunch time at the Department of Philosophy
- 5 philosophers sitting at a round table
- Each philosopher has one chopstick on her/his left and one on her/his right (i.e., 5 chopsticks in total)

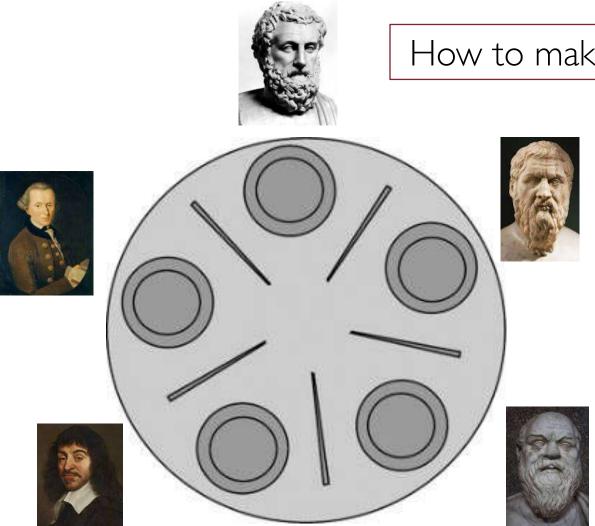
- It's lunch time at the Department of Philosophy
- 5 philosophers sitting at a round table
- Each philosopher has one chopstick on her/his left and one on her/his right (i.e., 5 chopsticks in total)
- 2 things philosophers are good at ②:
 - Eating
 - Thinking

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- After eating, put down both chopsticks and go back thinking!





How to make them not starving?

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Very inefficient! Only one philosopher at a time can eat

We still want some concurrency here ©

The Dining Philosophers: Solution 1

```
Semaphore chopsticks[5];
while(True) {
   chopsticks[i].wait(); // wait on the left chopstick
   chopsticks[(i+1)%5].wait(); // wait on the right chopstick
   eat();
   chopsticks[i].signal(); // signal on the left chopstick
   chopsticks[(i+1)%5].signal(); // signal on the right chopstick
   think();
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Is this solution correct?

No! Possible deadlock if all philosophers take the left chopstick

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Idea: Before picking one chopstick be sure also the second one is available, otherwise wait for the neighbour to finish

Testing if either one of the two neighbours of a given philosopher is currently eating (condition variables)

Never gonna pick a single chopstick!

```
class Philosopher {
    enum Status {
        THINKING,
        HUNGRY,
        EATING
    }
    Status state;

    public Philosopher() {
        this.state = THINKING;
    }
}
```

```
void canEat(int i) {
    State state = this.philosophers[i].state;
    State left = this.philosophers[[(i-1)%5].state;
    State right = this.philosophers[[(i+1)%5].state;
    if(left != EATING && right != EATING && state == HUNGRY) {
        this.philosophers[i].state = EATING;
        this.philosophers[i].notify();
    }
}
```

```
class DiningPhilosophers {
    Philosopher[5] philosophers;

public DiningPhilosopers() {
    for(int i=0; i < 5; ++i) {
        this.philosophers[i] = new Philosopher();
    }
}
// continue implementation ----->
```

```
void synchronized pickup(int i) {
    this.philosophers[i].state = HUNGRY;
    canEat(i);
    if(this.philosophers[i].state != EATING) {
        this.philosophers[i].wait();
    }
}
```

```
void synchronized putdown(int i) {
    this.philosophers[i].state = THINKING;
    canEat((i - 1) % 5); // left neighbour
    canEat((i + 1) % 5); // right neighbour
}
```

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 - Banking system: read vs. update account balances

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 - Producer-Consumer
 - Audio/Video player embedded in a web browser: shared data buffer + network and render threads
 - Reader-Writer
 - Banking system: read vs. update account balances
 - Dining Philosophers
 - Lock on multiple resources: e.g., travel reservation (hotel, airline, car rental databases)

Our Journey

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"When two trains approach each other at a crossing, both shall come to a full stop and neither shall start up again until the other has gone."

Kansas legislation early 1900's

Intuitively, a condition where two or more threads are waiting for an event that can only be generated by the very same threads

```
Thread A

printer.wait();
disk.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread B

disk.wait();
printer.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread A

A starts first

printer.wait();

disk.wait();

// copy from disk to printer

printer.signal();

disk.signal();
```

```
Thread B

disk.wait();
printer.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread A

printer.wait(); Acquires printer and context switch
disk.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread B

disk.wait();
printer.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread A

printer.wait();
disk.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread B

B takes over

disk.wait();

printer.wait();

// copy from disk to printer

printer.signal();

disk.signal();
```

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printer.signal();
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```
Thread A

printer.wait(); A executes again and blocks
disk.wait();

// copy from disk to printer

printer.signal();
disk.signal();
```

```
Thread B

disk.wait();
printer.wait();

// copy from disk to printer

printer.signal();
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A waits B to release the disk

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printer.signal();
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```

B waits A to release the printer

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- Deadlock detection: finds instances of deadlocks and tries to recover
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- Deadlock avoidance (online): runtime support checks resource requests made by threads to avoid deadlocks

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- Related terms but each one refers to a specific situation
- Starvation occurs when a thread waits indefinitely for some resource but other threads are actually making progress using that resource
- The main difference with deadlock is that the system is not completely stuck!

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 - No Preemption \rightarrow a thread can only release a resource voluntarily; neither another thread nor the OS can force it to release the resource
 - Circular Wait \rightarrow a set of waiting threads t_1, \ldots, t_n where t_i is waiting on $t_{(i+1)\%n}$

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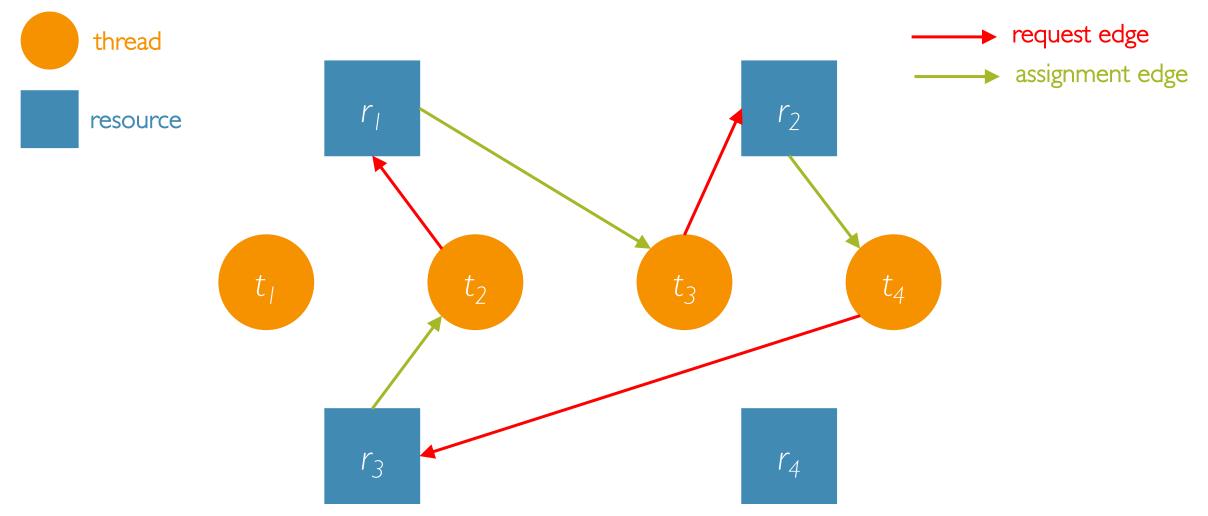
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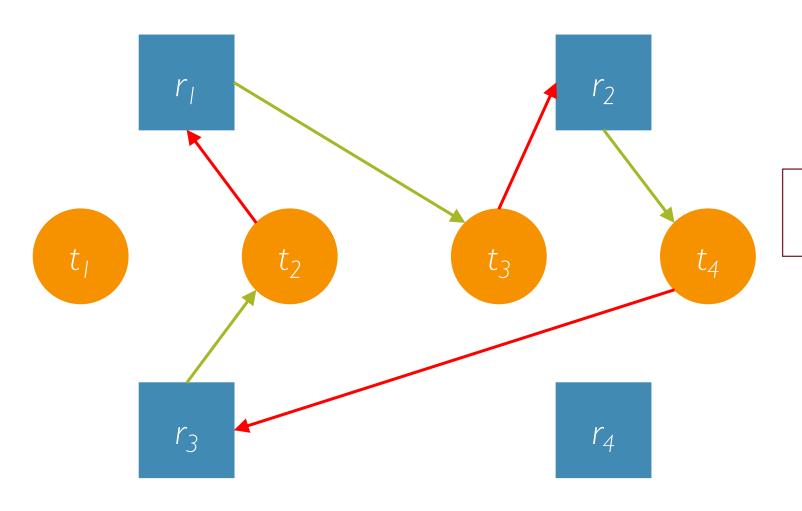
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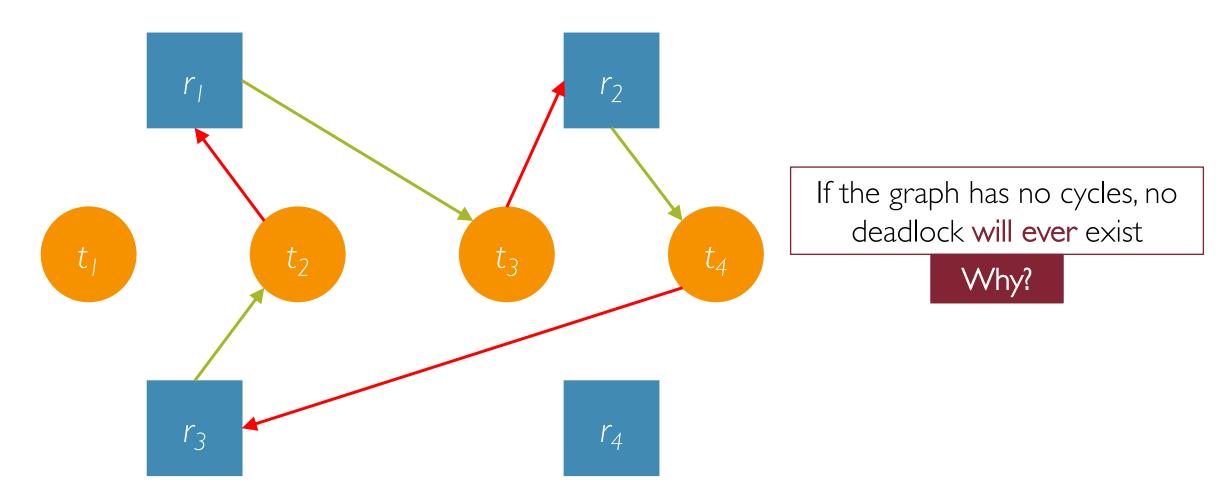
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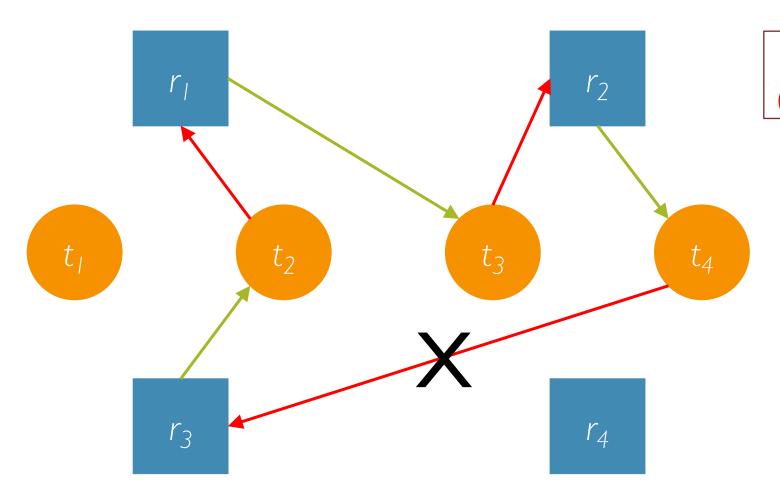
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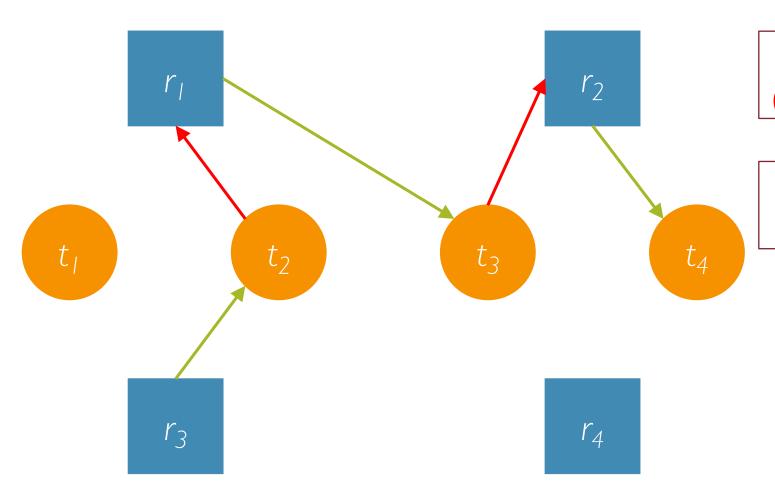
If the graph has no cycles, no deadlock will ever exist





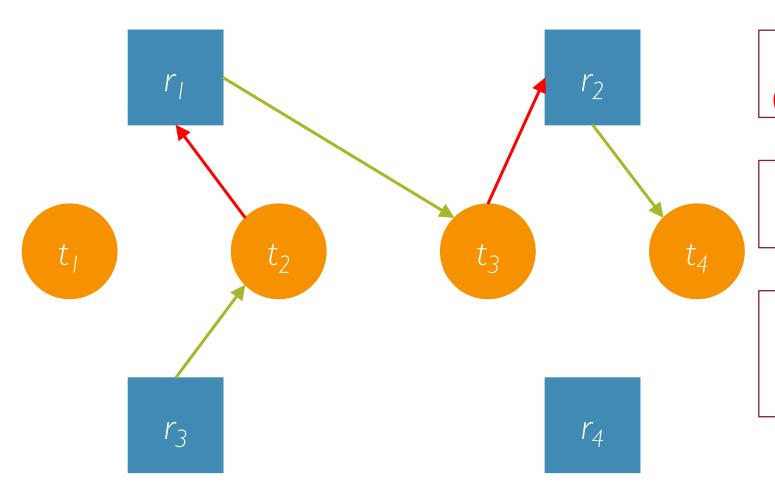
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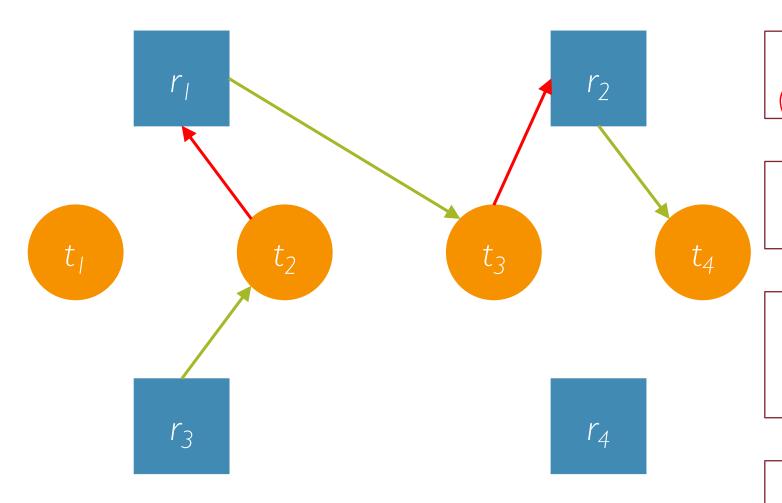
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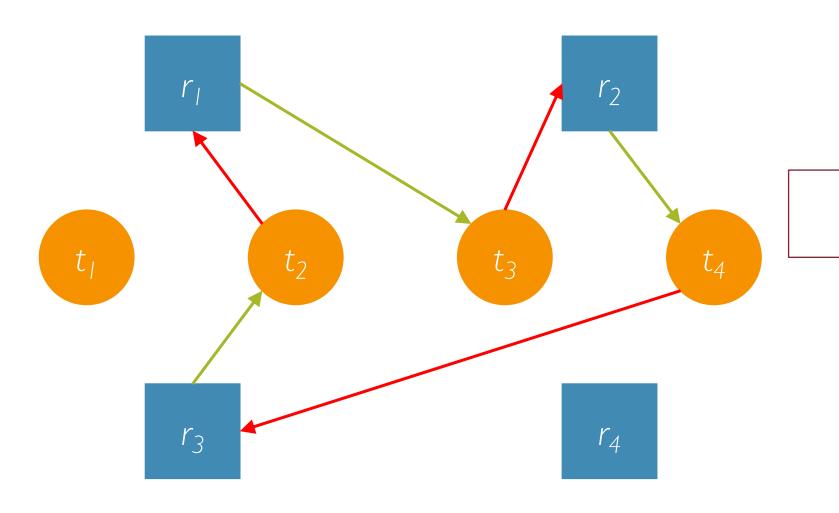


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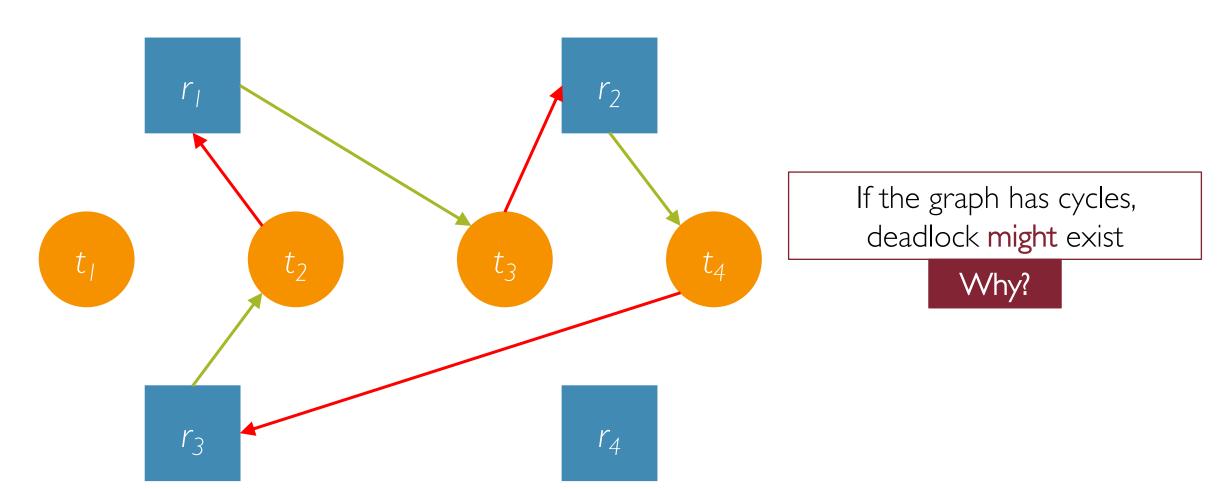
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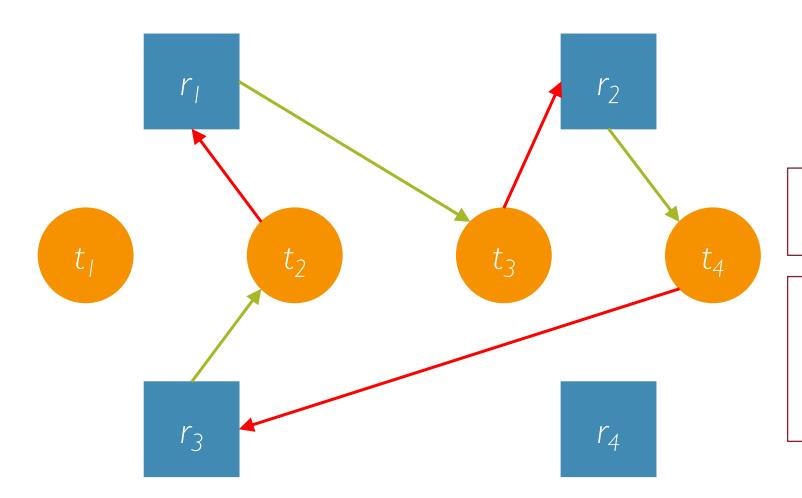
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And so on and so forth...



If the graph has cycles, deadlock might exist

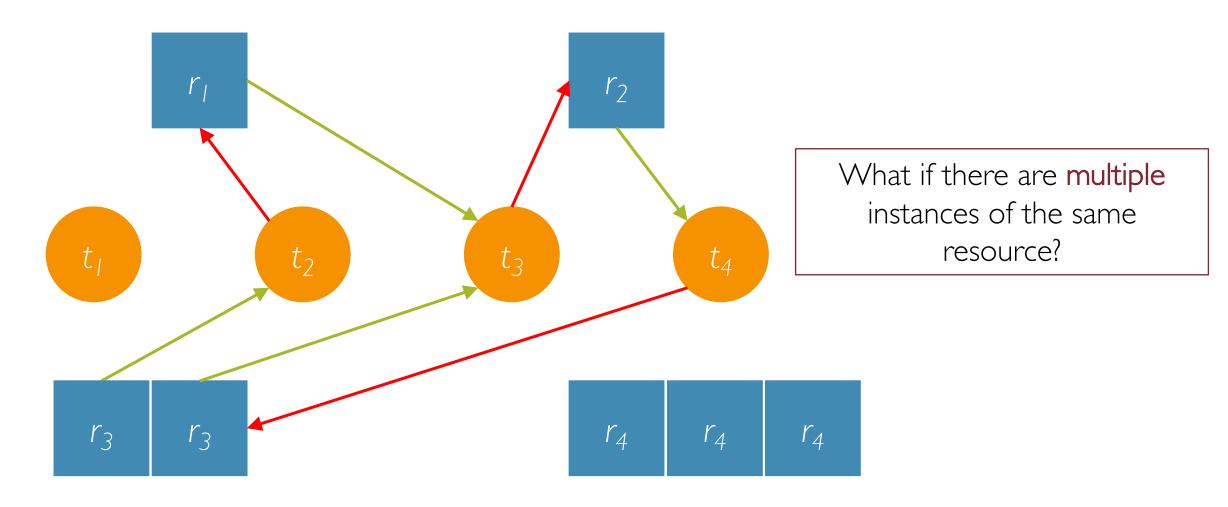




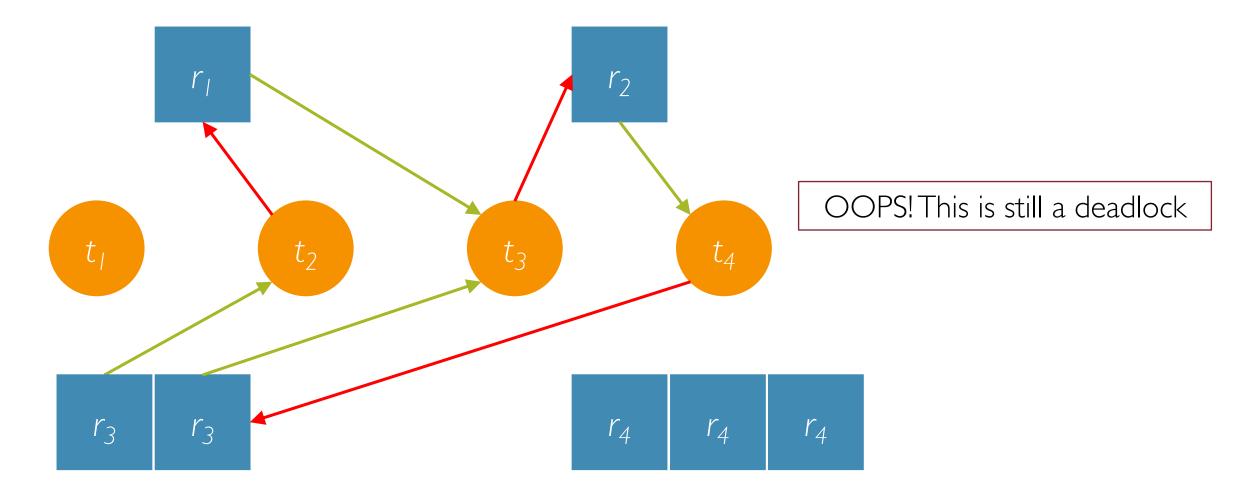
If the graph has cycles, deadlock might exist

We are implicitly assuming the multiplicity of each resource is I (i.e., we have one r_1 , one r_2 , etc.)

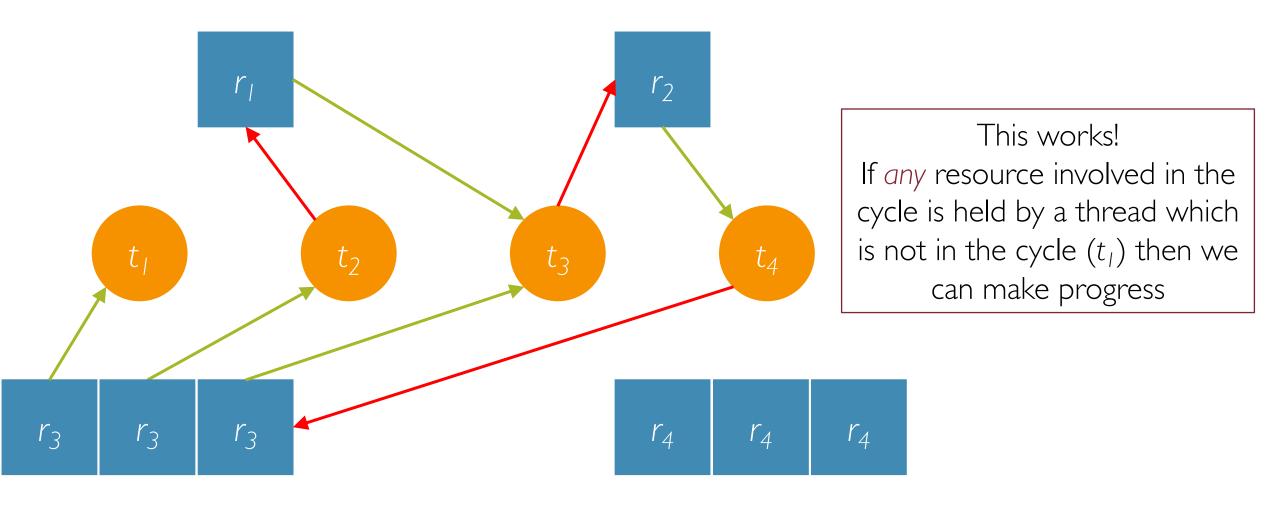
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- How? Several ways of doing it:
 - Kill all the threads in the cycle (quite harsh, ugh?)
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- We would like to be more precise than that...

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- When to run such a detection algorithm?
 - Before granting a resource \rightarrow each granted request will take $O(|V|^2)$
 - When a request cannot be fulfilled \rightarrow each failed request will take $O(|V|^2)$
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- What do modern OSs do? Nothing! They leave it to the programmer!

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 - Circular Wait → impose an ordering (i.e., numbering) on resources and enforce to request them in such order

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Deadlock Avoidance: Resource Reservation

Each thread provides information about the maximum number of resources it might need during execution

> $m_i = maximum$ number of resources that thread i might request $c_i = current$ number of resources that thread i is holding $C = \sum_{i=1}^{\infty} c_i = total$ number of resources currently allocated R = maximum number of resources overall available

Any thread sequence is **safe** if for each thread it holds that:

$$\underbrace{m_i - c_i}_{\text{resources } t_i \text{ might still request}} \leq \underbrace{R - C}_{\text{resources currently available}} + \underbrace{\sum_{j=1}^{i-1} c_j}_{\text{resources currently allocated up to } t_i, j < i}$$

Deadlock Avoidance: Safe State

- A state in which there is a safe sequence for the threads
- An unsafe state does not necessarily mean deadlock (i.e., some threads may not request the maximum number of resources as declared)
- Grant a resource to a thread if the new state is safe, otherwise make it wait even if the resource is available
- This policy ensures no circular-wait condition exists

- 3 threads: t₁, t₂, and t₃ are competing for 12 tape drives (resources)
- Currently, II drives are allocated to the threads, leaving I available

| Thread | m _i | C _i | $m_i - c_i$ |
|----------------|----------------|----------------|-------------|
| t _l | 4 | 3 | 1 |
| t_2 | 8 | 4 | 4 |
| t_3 | 12 | 4 | 8 |

Is the current state safe?

| Thread | m _i | C _i | $m_i - c_i$ |
|----------------|----------------|----------------|-------------|
| t _l | 4 | 3 | I |
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The current state is safe in that there exists a sequence of threads (t_1, t_2, t_3) where each one will get the maximum number of resources without waiting

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t₂ can use the current allocation, plus t₁'s resources and 1 drive left (4 drives)

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t₃ can use the current allocation, plus t₁'s & t₂'s resources and 1 drive left (8 drives)

| Thread | m _i | C _i | $m_i - c_i$ |
|----------------|----------------|----------------|-------------|
| t _l | 4 | 3 | I |
| t_2 | 8 | 4 | 4 |
| t_3 | 12 | 5 | 7 |

Suppose t₃ requests one more drive, then now there are no more available drives

Theoretically, everything might still work (e.g., t₁ may never request another drive)

However, t₃ must wait because allocating that extra drive would lead to an unsafe state, which in turn might lead to deadlock

• An extension of the original definition of resource allocation graph

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- Edges can now be of 3 types:
 - Request Edge \rightarrow a directed edge (t_i, r_j) indicates that t_i has requested r_j , but not yet acquired
 - Claim (dotted) Edge \rightarrow a directed edge (t_i, r_j) indicates that t_i might request r_j in the future
 - Assignment Edge \rightarrow a directed edge (r_j, t_i) indicates that the OS has allocated r_j to t_i

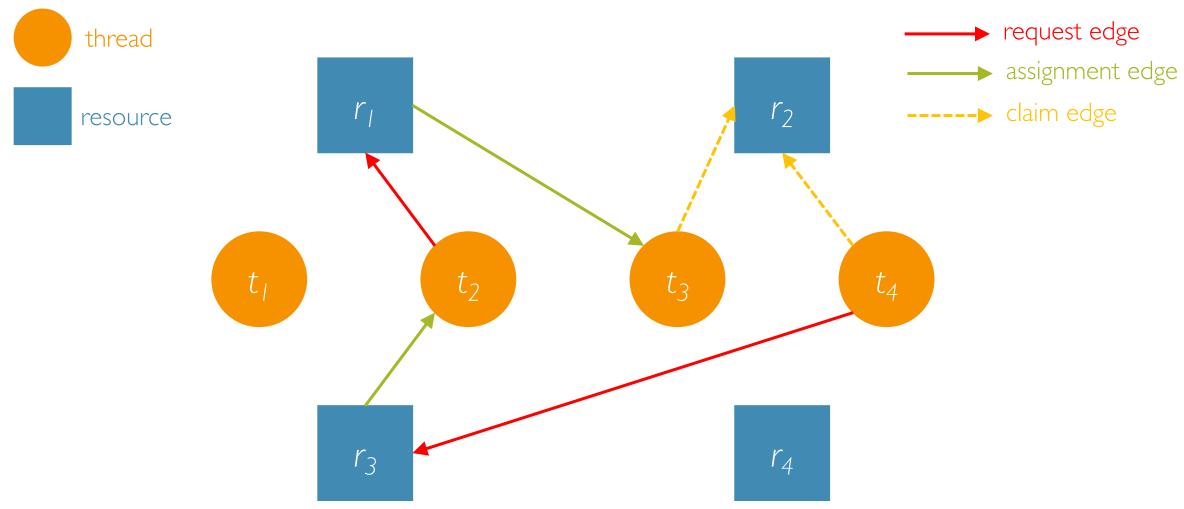
- An extension of the original definition of resource allocation graph
- Edges can now be of 3 types:
 - Request Edge \rightarrow a directed edge (t_i, r_j) indicates that t_i has requested r_j , but not yet acquired
 - Claim (dotted) Edge \rightarrow a directed edge (t_i, r_j) indicates that t_i might request r_j in the future
 - Assignment Edge \rightarrow a directed edge (r_j, t_i) indicates that the OS has allocated r_j to t_i
- Satisfying a request means converting a claim into an assignment edge

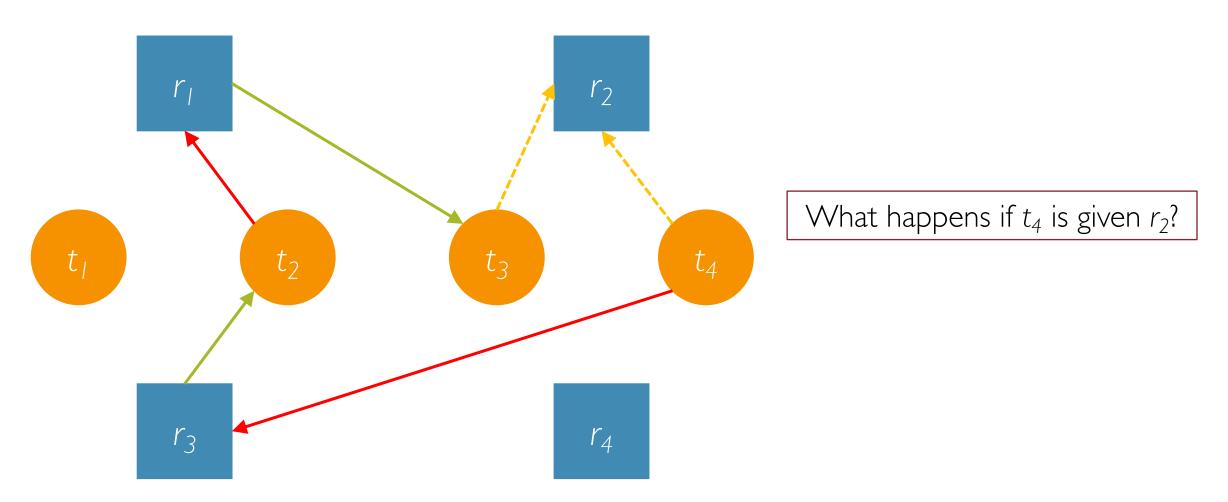
• A cycle in this extended RAG indicates an unsafe state

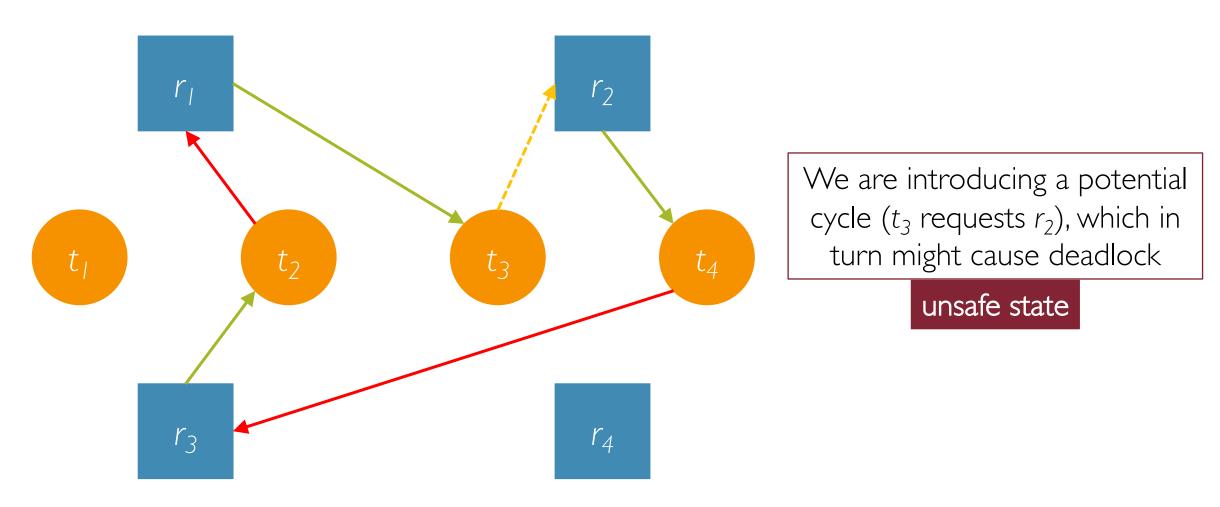
- A cycle in this extended RAG indicates an unsafe state
- If the allocation results in an unsafe state, this will be denied even if the resource is actually available

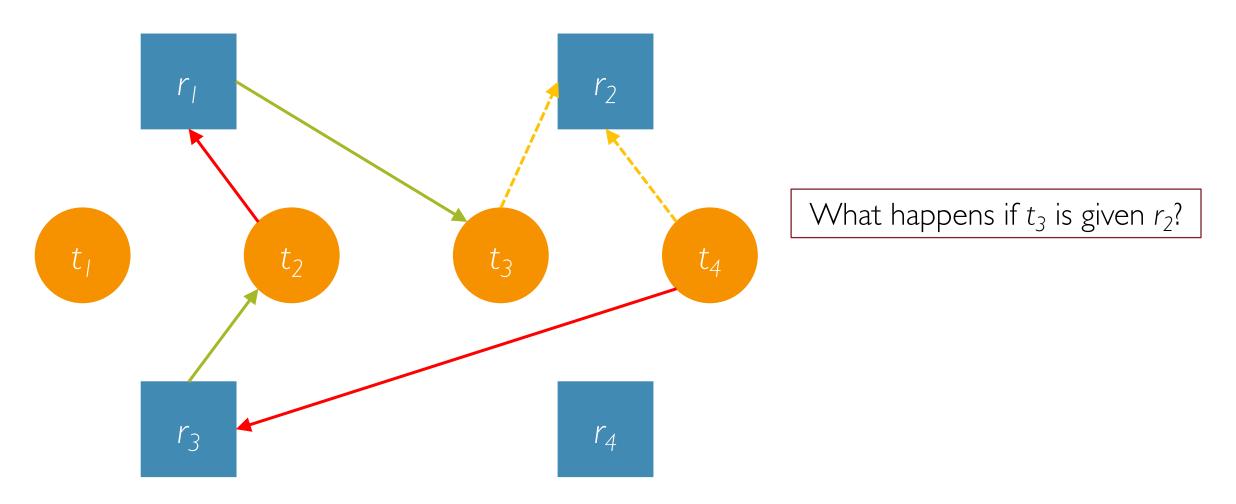
- A cycle in this extended RAG indicates an unsafe state
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- In other words, the claim edge is converted into a request edge and the thread will wait

- A cycle in this extended RAG indicates an unsafe state
- If the allocation results in an unsafe state, this will be denied even if the resource is actually available
- In other words, the claim edge is converted into a request edge and the thread will wait
- <u>NOTE:</u> This solution does not work when there are multiple instances of the <u>same</u> resource

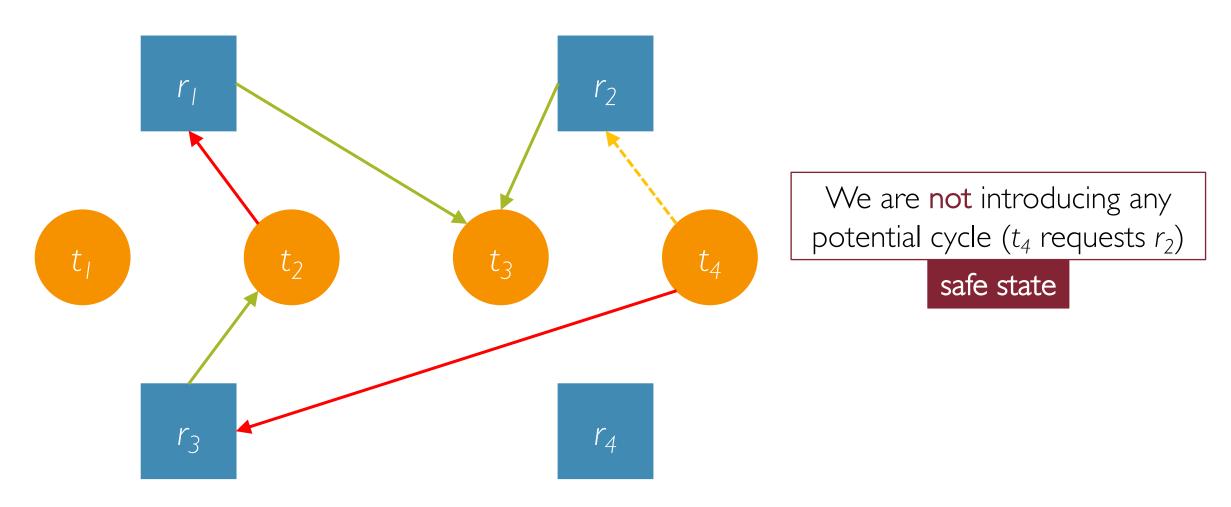




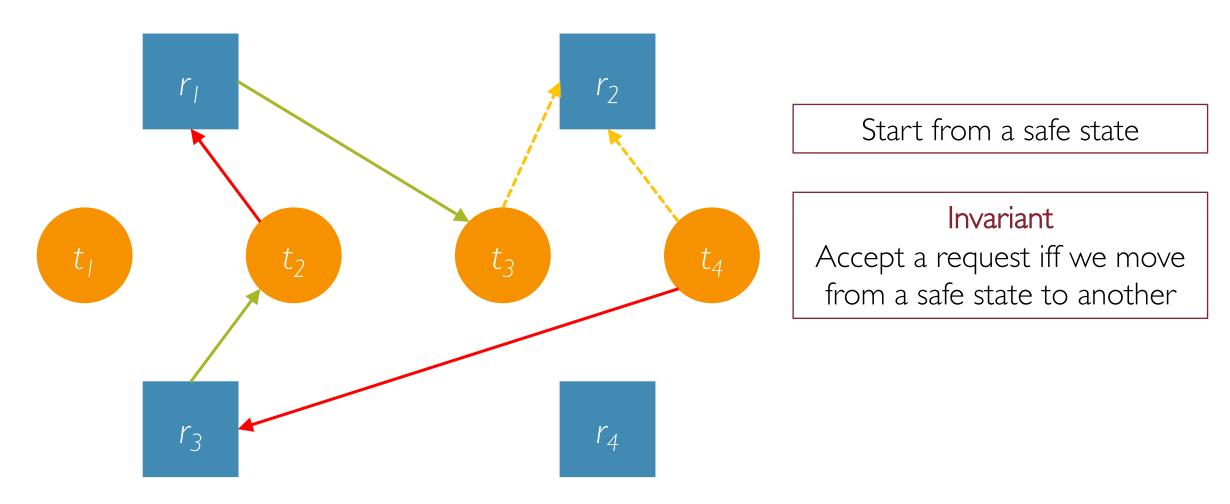




Deadlock Avoidance: Resource Allocation Graph



Deadlock Avoidance: Resource Allocation Graph



Banker's Algorithm

- Handles multiple instances of the same resource
- Forces threads to provide information on what resource they might need, in advance
- The resources requested must not exceed the total available in the system
- The algorithm allocates resources to a requesting thread if the allocation leaves the system in a safe state, otherwise the thread waits

Banker's Algorithm: Data Structures

- n = number of threads; m = number of resource types
- available [1..m]: m-dimensional vector
 - available[j] = k means there are k resources of type j available
- max[1..n, 1..m]:n x m matrix
 - max[i, j] = k means thread i may require at most k resources of type j
- allocation[1..n, 1..m]:nxm matrix
 - allocation[i, j] = k means thread i has allocated k resources of type j
- need[1..n, 1..m]:nxm matrix
 - need[i, j] = max[i, j] allocation[i, j] = k means thread i may need k
 more resources of type j to complete its task

Banker's Algorithm: Idea

- The algorithm is divided in 2 tasks:

 - resourceRequest → given a thread and its resource request decides if such a request can be satisfied

Banker's Algorithm: Idea

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 - resourceRequest → given a thread and its resource request decides if such a request can be satisfied
- A request can be satisfied iff this leads to a safe state!

Banker's Algorithm: Idea

- The algorithm is divided in 2 tasks:
 - isSafeState -> given the current status of allocation of resources, tests if this is a safe state
 - resourceRequest → given a thread and its resource request decides if such a request can be satisfied
- A request can be satisfied iff this leads to a safe state!
- In other words, the second tasks uses the output of the first one in order to make a decision

Banker's Algorithm: isSafeState

- I. Let work and finish be vectors of length m and n, respectively Initialize: work = available; finish[i] = false; for all i
- 2. Find an i such that:

```
finish[i] = false && need[i] ≤ work

If no such i exists, go to step 4.
```

3. Assume thread i executes:

```
work = work + allocation[i]; finish[i] = true; go to step 2.
```

4. If finish[i] == true for all i, the system is in a safe state

Banker's Algorithm: requestResource

Input: i (thread) and request an m-dimensional vector of requests

- I. If request > need[i] raise an error as thread i is attempting to request more resources that it claimed, otherwise go to step 2.
- 2. If request > available thread i must wait since resources are not available, otherwise go to step 3.
- 3. Even if resources are available, test if this allocation will lead to a safe state by simulating it

```
available -= request; allocation[i] += request; need[i] -= request;
isSafeState() ? OK : rollback() and wait()
```

A snapshot of the current state of the system

| | | | | | RES | SOUR | CES | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|
| | | | MAX | | ALL | OCATI | ON | A۱ | VAILAB | LE |
| | | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | |
| R | T ₁ | | 7 | 5 | | 0 | 0 | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | |
| | Total | | | | 2 | 9 | 9 | | 5 | 2 |

QI: How many resources of type A, B, and C are there overall?

| | | | | | RES | SOUR | CES | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|
| | | | MAX | | ALL | .OCATI | ON | A\ | VAILAB | LE |
| | | Α | В | С | Α | В | С | Α | В | С |
| T | T ₀ | 0 | 0 | I | 0 | 0 | | | | |
| R - | T ₁ | I | 7 | 5 | | 0 | 0 | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | |
| | Total | | | | 2 | 9 | 9 | | 5 | 2 |

QI: How many resources of type A, B, and C are there overall?

| | | | | | RES | SOUR | CES | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE |
| | | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | | 0 | 0 | | | | |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | |
| | Total | | | | 2 | 9 | 9 | | 5 | 2 |

$$A = 2 + 1 = 3$$

 $B = 9 + 5 = 14$
 $C = 9 + 2 = 11$

Q2: What is the content of the NEED matrix?

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | A | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | | | |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | | | |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

Q2: What is the content of the NEED matrix?

NEED[i, j] = MAX[i, j] - ALLOCATION[i, j]

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | .OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | I | | | | | | |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | | | |
| | Total | | | | 2 | 9 | 9 | I | 5 | 2 | | | |

Q2: What is the content of the NEED matrix?

NEED[i, j] = MAX[i, j] - ALLOCATION[i, j]

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|-----------------|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | <u>0</u> -0 = 0 | | |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | | | |
| | Total | | | | 2 | 9 | 9 | I | 5 | 2 | | | |

Q2: What is the content of the NEED matrix?

NEED[i, j] = MAX[i, j] - ALLOCATION[i, j]

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|---------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | T | 0 | 0 | | | | | 0 | 0-0 = 0 | |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | | | |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

Q2: What is the content of the NEED matrix?

NEED[i, j] = MAX[i, j] - ALLOCATION[i, j]

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---------|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | I-I = 0 |
| R | T ₁ | I | 7 | 5 | I | 0 | 0 | | | | | | |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | | | |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | | | |
| | Total | | | | 2 | 9 | 9 | I | 5 | 2 | | | |

Q2: What is the content of the NEED matrix?

NEED[i, j] = MAX[i, j] - ALLOCATION[i, j]

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | .OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | То | 0 | 0 | 1 | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

Q3: Is the system in a safe state? Why?

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A\ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

Let's start with T₀

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | T | 0 | 0 | l | | | | 0 | 0 | 0 |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | l | 5 | 2 | | | |

Eventually, T_0 finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | I | | | | 0 | 0 | 0 |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | I | 5 | 2 | | | |

 T_1 can't execute as it still might NEED (0, 7, 5) and AVAILABLE = (1, 5, 3)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 8 | -1 | 5 | 3 | | | |

 T_2 can execute as it still might NEED (1,0,0) and AVAILABLE = (1,5,3)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | .OCATI | ON | A۱ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T _I | 1 | 7 | 5 | - 1 | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T_2 | 2 | 3 | 5 | I | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 8 | - | 5 | 3 | | | |

 T_2 can execute as it still might NEED (1,0,0) and AVAILABLE = (1,5,3)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | LOCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | A | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T_2 | 2 | 3 | 5 | 2 | 3 | 5 | | | | 0 | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 3 | 9 | 8 | 0 | 5 | 3 | | | |

T₂ eventually finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | 1 | - | - | - | | | | - | - | - |
| R | T_1 | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T_2 | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | | 6 | 3 | 2 | 8 | 8 | | | |

 T_3 can execute as it still might NEED (0, 0, 2) and AVAILABLE = (2, 8, 8)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|----|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | -1 | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | - | 6 | 3 | 2 | 8 | 8 | | | |

 T_3 can execute as it still might NEED (0, 0, 2) and AVAILABLE = (2, 3, 6)

| | | | | | RES | SOURC | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|---------|----|---|------|---|
| | | | MAX | | ALL | LOCATI | ON | A' | VAILABI | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | 1 | 7 | 5 | - 1 | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 5 | | | | 0 | 0 | 0 |
| | Total | | | | - 1 | 6 | 5 | 2 | 8 | 6 | | | |

T₃ eventually finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | A | В | С | A | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | 1 | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | - | - | - | | | | - | - | - |
| | Total | | | | - | 0 | 0 | 2 | 14 | 11 | | | |

 T_1 can now execute since NEED (0, 7, 5) and AVAILABLE = (2, 14, 11)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T _I | 1 | 7 | 5 | | 7 | 5 | | | | 0 | 0 | 0 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | - | - | - | | | | - | - | - |
| | Total | | | | | 7 | 5 | 2 | 7 | 6 | | | |

We have found a sequence of execution T_0 , T_2 , T_3 , T_1 which leads to safe state!

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | - | - | - | | | | - | - | - |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | - | _ | _ | | | | - | - | - |
| | Total | | | | - | - | - | 3 | 14 | 11 | | | |

Q4: If T_1 issues a REQUEST (0, 5, 2), can this be granted immediately?

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | .OCATI | ON | A\ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | A | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

We have to ask ourselves: I. if the request can be satisfied; 2. if it will lead to a safe state

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|-----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A۱ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | T | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T ₁ | 1 | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | - 1 | 5 | 2 | | | |

To answer I. check if: a. REQUEST <= NEED and b. REQUEST <= AVAILABLE

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A\ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

I.a. REQUEST <= NEED?

| | RESOURCES | | | | | | | | | | | | |
|---------------------------------|----------------|-----|---|---|------------|---|---|-----------|---|---|------|---|---|
| | | MAX | | | ALLOCATION | | | AVAILABLE | | | NEED | | |
| | | | В | С | Α | В | С | A | В | С | Α | В | С |
| T H R E A D S | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

I.a. REQUEST <= NEED?

YES! (0, 5, 2) <= (0, 7, 5)

| | | RESOURCES | | | | | | | | | | | |
|-----------------------|----------------|-----------|---|------------|---|---|-----------|---|---|------|---|---|---|
| | | MAX | | ALLOCATION | | | AVAILABLE | | | NEED | | | |
| | | A B C | | Α | В | С | Α | В | С | Α | В | С | |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | - 1 | | | | 0 | 0 | 0 |
| R E A D S | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | - | 5 | 2 | | | |

I.b. REQUEST <= AVAILABLE?

| | RESOURCES | | | | | | | | | | | | |
|-----------------------|----------------|-------|---|------------|---|---|-----------|---|---|------|---|---|---|
| | | MAX | | ALLOCATION | | | AVAILABLE | | | NEED | | | |
| | | A B C | | Α | В | С | Α | В | С | Α | В | С | |
| T H | T ₀ | 0 | 0 | l | 0 | 0 | I | | | | 0 | 0 | 0 |
| R E A D S | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | I | 5 | 2 | | | |

I.b. REQUEST <= AVAILABLE?

YES! $(0, 5, 2) \le (1, 5, 2)$

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A\ | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T _I | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

To answer 2. we simulate the request is granted and see if we are still in a safe state

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T ₁ | I | 7 | 5 | | 0 | 0 | | | | 0 | 7 | 5 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 9 | 9 | 1 | 5 | 2 | | | |

To answer 2. we simulate the request is granted and see if we are still in a safe state

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | - 1 | | | | 0 | 0 | 0 |
| R | T ₁ | I | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 14 | 11 | | 0 | 0 | | | |

Let's start with T₀

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ON | A' | VAILAB | LE | | NEED | |
| | | А | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | 0 | 0 | | | | | 0 | 0 | 0 |
| R | T ₁ | 1 | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 14 | | l | 0 | 0 | | | |

Eventually, T_0 finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCAT | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 14 | 10 | I | 0 | 1 | | | |

 T_1 can't execute as it still might NEED (0, 2, 3) and AVAILABLE = (1, 0, 1)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | ı | 7 | 5 | I | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | I | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 14 | 10 | I | 0 | I | | | |

 T_2 can execute as it still might NEED (1,0,0) and AVAILABLE = (1,0,1)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | .OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | _ | _ | - | | | | - | - | - |
| R | T _I | I | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | | 3 | 5 | | | | I | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 2 | 14 | 10 | I | 0 | - | | | |

 T_2 can execute as it still might NEED (1,0,0) and AVAILABLE = (1,0,1)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | I | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T_2 | 2 | 3 | 5 | 2 | 3 | 5 | | | | 0 | 0 | 0 |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | 3 | 14 | 10 | 0 | 0 | I | | | |

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T₂ eventually finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | - 1 | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | | 11 | 5 | 2 | 3 | 6 | | | |

 T_3 can execute as it still might NEED (0, 0, 2) and AVAILABLE = (2, 3, 6)

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | А | В | С | Α | В | С | Α | В | С | A | В | С |
| T H | T ₀ | 0 | 0 | T | - | - | - | | | | - | - | - |
| R | T ₁ | 1 | 7 | 5 | I | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 3 | | | | 0 | 0 | 2 |
| | Total | | | | - 1 | 11 | 5 | 2 | 3 | 6 | | | |

 T_3 can execute as it still might NEED (0, 0, 2) and AVAILABLE = (2, 3, 6)

| | | | | | RE | SOURC | CES | | | | | | |
|--------|----------------|---|-----|---|-----|--------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | _OCATI | ION | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| R | T _I | 1 | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | 0 | 6 | 5 | | | | 0 | 0 | 0 |
| | Total | | | | ı | Ш | 7 | 2 | 3 | 4 | | | |

T₃ eventually finishes and releases all its resources

| | | | | | RES | SOUR | CES | | | | | | |
|--------|----------------|---|-----|---|-----|-------|-----|----|--------|----|---|------|---|
| | | | MAX | | ALL | OCATI | ON | A' | VAILAB | LE | | NEED | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H | T ₀ | 0 | 0 | 1 | - | - | - | | | | - | - | - |
| R | T ₁ | I | 7 | 5 | | 5 | 2 | | | | 0 | 2 | 3 |
| E A | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| D S | T ₃ | 0 | 6 | 5 | - | - | - | | | | - | - | - |
| | Total | | | | | 5 | 2 | 2 | 9 | 9 | | | |

 T_1 can now execute since NEED (0, 2, 3) and AVAILABLE = (2, 9, 9)

| | RESOURCES | | | | | | | | | | | | |
|---------------------------------|----------------|-----|---|------------|---|---|-----------|---|---|------|---|---|---|
| | | MAX | | ALLOCATION | | | AVAILABLE | | | NEED | | | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С |
| T H R E A D S | T ₀ | 0 | 0 | I | - | - | - | | | | - | - | - |
| | T ₁ | I | 7 | 5 | I | 7 | 5 | | | | 0 | 0 | 0 |
| | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - |
| | T ₃ | 0 | 6 | 5 | _ | - | - | | | | - | - | - |
| | Total | | | | | 7 | 5 | 2 | 7 | 6 | | | |

We have found a sequence of execution T_0 , T_2 , T_3 , T_1 which leads to safe state!

| | RESOURCES | | | | | | | | | | | | | |
|---------------------------------|----------------|---|-----|---|---|------------|---|---|-----------|----|---|------|---|--|
| | | | MAX | | | ALLOCATION | | | AVAILABLE | | | NEED | | |
| | | Α | В | С | Α | В | С | Α | В | С | Α | В | С | |
| T H R E A D S | T ₀ | 0 | 0 | I | _ | - | _ | | | | - | - | - | |
| | T ₁ | I | 7 | 5 | - | - | - | | | | - | - | - | |
| | T ₂ | 2 | 3 | 5 | - | - | - | | | | - | - | - | |
| | T ₃ | 0 | 6 | 5 | _ | _ | _ | | | | - | - | - | |
| | Total | | | | - | - | - | 3 | 14 | 11 | | | | |

 Deadlock → a situation in which a set of threads/processes cannot proceed because each one requires resources held by another

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- Detection and Recovery → recognize deadlock after it has occurred and break it
- Prevention → design resource allocation strategies which guarantee at least one of the 4 necessary deadlock conditions never holds
- Avoidance -> runtime checks to avoid deadlock online
- In practice, most OSs don't do anything and leave it all to applications