

NTE1936 Integrated Circuit Positive Voltage Regulator, 12V, 2A

Features:

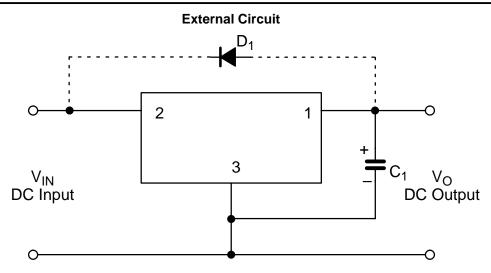
- 3 Pin Plastic Package TO3P
- Only Output Capacitor is Externally Required
- Precise Setting Voltage of ±2%
- Wide Input Voltage Range (~45V)
- Built-in Current Foldback Protection
- Ideal Combination of Passivated Power Transistor and High Reliability Flip-Chip Circuit

Absolute Maximum Ratings: (T_A = +25°C unless otherwise specified)

DC Input Voltage, V _{IN}	45V
Power Dissipation, P _C (T _C =+ 25°C)	50W
(No Fin)	2W
Thermal Resistance, Junction–to–Case, R _{thJC}	2°C/W
Junction Temperature Range, T _i	–30° to +125°C
Operating Ambient Temperature Range, Top	–20° to +80°C
Storage Temperature Range, T _{stg}	–30° to +125°C

Electrical Characteristics: $(T_A = +25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
DC Input Voltage	V _{IN}	I _O = 2A	15	_	35	V
Output Voltage	Vo	$V_{IN} = 19V, I_O = 0.5A$	11.8	12.0	12.2	V
Output Current	Io		0	_	2	Α
Line Regulation	ΔV_{LINE}	$V_{IN} = 16V \text{ to } 22V, I_O = 0.5A$	_	10	30	mV
Load Regulation	ΔV_{LOAD}	$V_{IN} = 19V$, $I_O = 0$ to 2A	_	80	200	mV
Temperature Coefficient	K _t		_	±1.5	_	mV/°C
Ripple Rejection		100 to 120H _Z	_	60	_	dB
Foldback Current	I _{S1}		2.4	_	_	Α
Short–Circuit Current	I _{S2}		_	_	0.6	Α



- Note 1. Output capacitor C_1 (47 to 100 μ f) shall be connected directly to output terminal (Pin1) and GND terminal (Pln3) as shown above.
- Note 2. When wiring between the regulator and the load is long, another capacitor (47 to $100\mu f$) shall be added in parallel with the load.
- Note 3. If there is a possibility of reverse biasing between input and output, a protection diode (D_1) is to be added. The recommended diode for D_1 is NTE116.

