CSE 433 Project 2 - FSM Number Analyzer

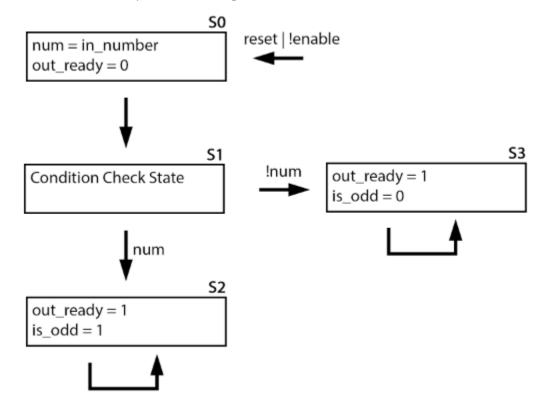
Finite State Machine

To simplify design process, I designed 3 properties of number analyzer as 3 different finite state machines. After implemented 3 separate machines as Verilog modules, I then simply wired all three together in Number Analyzer module.

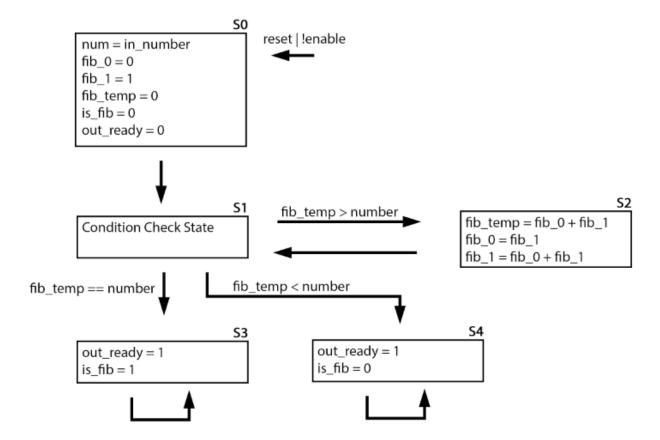
```
odd_number_analyzer ona(in_number[0], clock, reset, enable, odd_ready, is_odd);
fib_number_analyzer fna(in_number, clock, reset, enable, fib_ready, is_fibonacci);
pal_number_analyzer pna(in_number, clock, reset, enable, pal_ready, is_palindrome);
assign out_ready = odd_ready & fib_ready & pal_ready;
```

From all states in all modules, reset signal can trigger next state as SO. Also if enable signal is not high, fsm will always stay at SO.

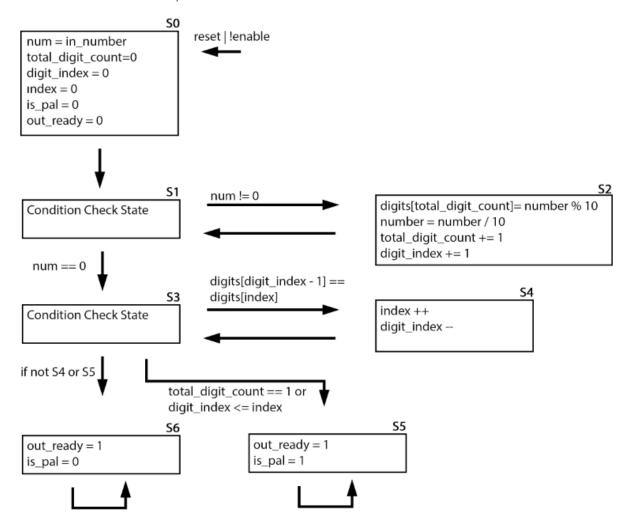
Odd Number Analyzer State Diagram



Fibonacci Number Analyzer



Palindrome Number Analyzer



Simulation Results

Testbench has an array of 32 bit numbers. It starts with number 0 and tests all test values whenever number analyzer sends an outputs ready signal. It sends reset signal alongside with each new value to reset number analyzer module. Array size can be changed to support more values to test in a single run.

We can see different modules outputs in different times. For example for number 1345269, is odd output is really fast but is Fibonacci result comes much later. And once it is ready, all outputs are ready signal is received from the top level module. Last column shows that if all three results are ready or not.

```
# Time:
                                        0, Is Odd: x, Is Fibonacci: x, Is Palindrome: 0, Output Ready: 0
                                        0, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time:
           5
                   Number:
# Time: 25
                  Number:
                                        0, Is Odd: 0, Is Fibonacci: 1, Is Palindrome: 0, Output Ready: 1
                 Number:
                               1346269, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 225
# Time: 245 Number: 1346269, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 845 Number: 1346269, Is Odd: 1, Is Fibonacci: 1, Is Palindrome: 0, Output Ready: 1
# Time: 1045 Number: 1187811, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 1065 Number: 1187811, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 1295 Number: 1187811, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 1, Output Ready: 0
# Time: 1665 Number: 1187811, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 1, Output Ready: 1
# Time: 1865 Number: 832040, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 2465 Number: 832040, Is Odd: 0, Is Fibonacci: 1, Is Palindrome: 0, Output Ready: 1
                                13469, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 2665 Number:
# Time: 2685 Number: 13469, Is Odd: 0, Is Fibonacci: 0, Is Falindrome: 0, Output Ready: 0
# Time: 3105 Number: 13469, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 1
# Time: 3305 Number: 1669, Is Odd: 0, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 3325 Number: 1669, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 0
# Time: 3665 Number: 1669, Is Odd: 1, Is Fibonacci: 0, Is Palindrome: 0, Output Ready: 1
```