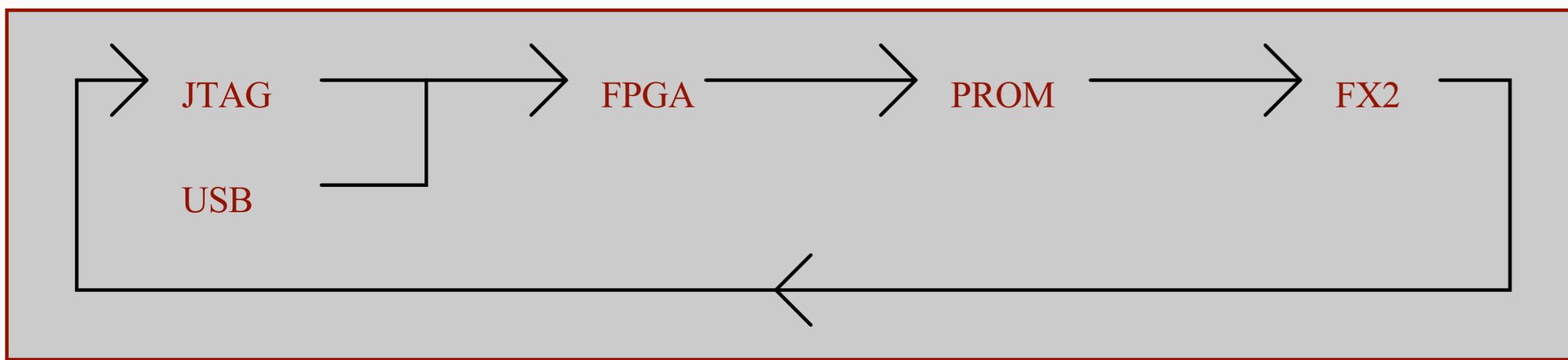


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Title

Nexys IICircuit
FX2 and Pmod Connectors

Doc# 500-134

Engineer NEA

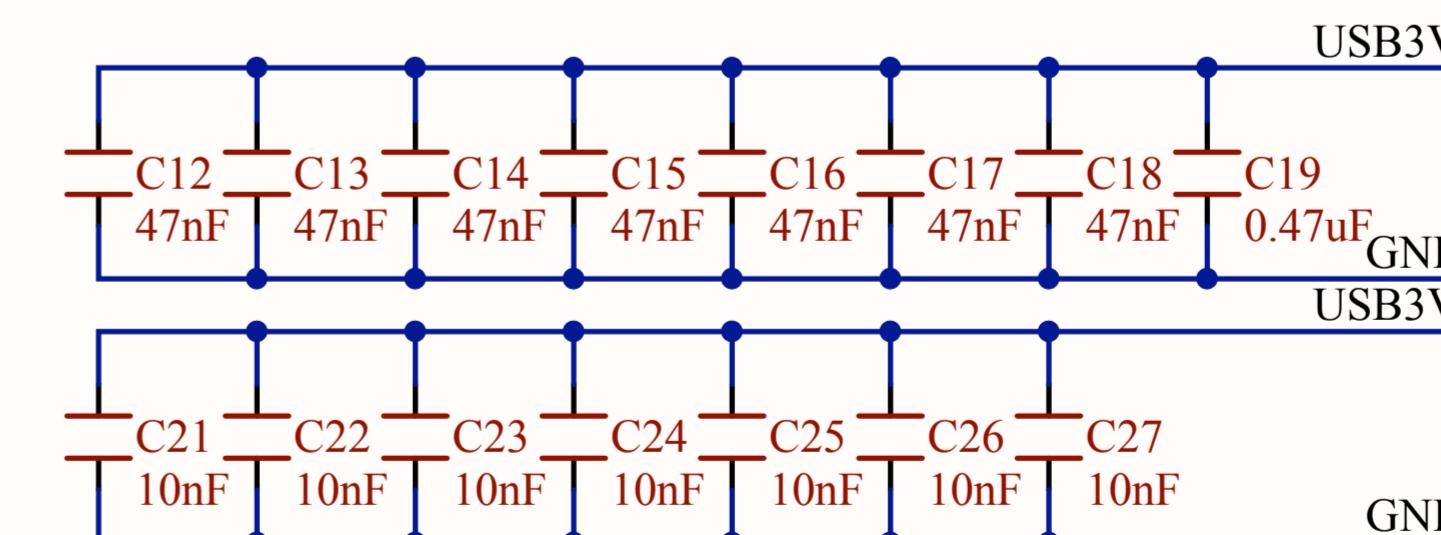
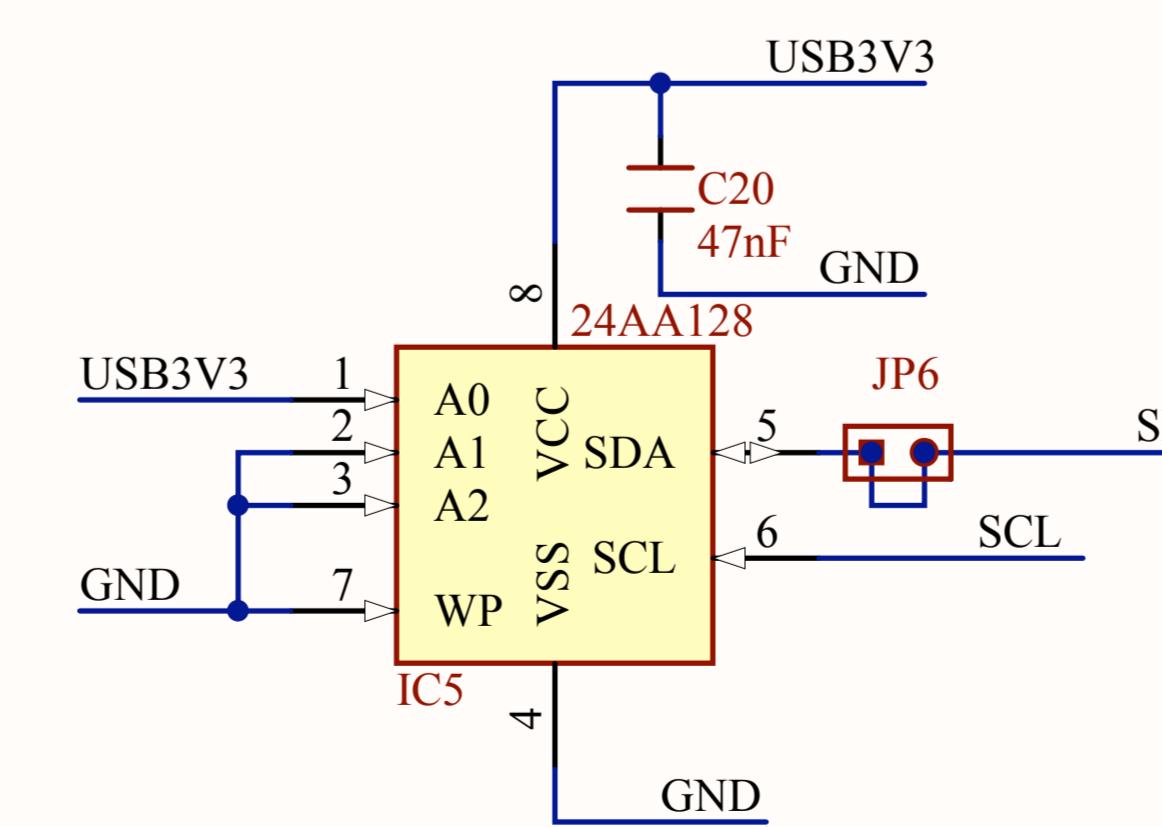
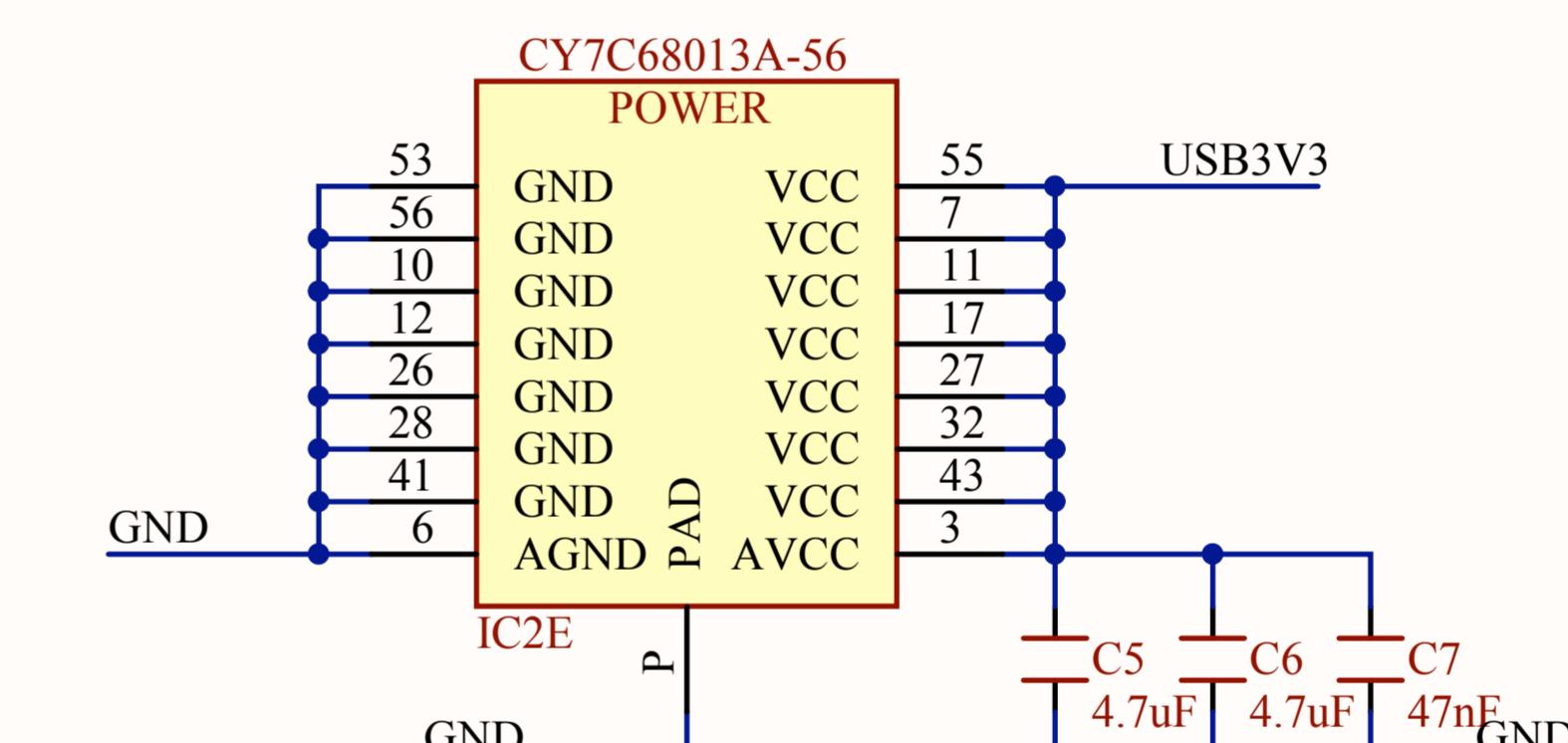
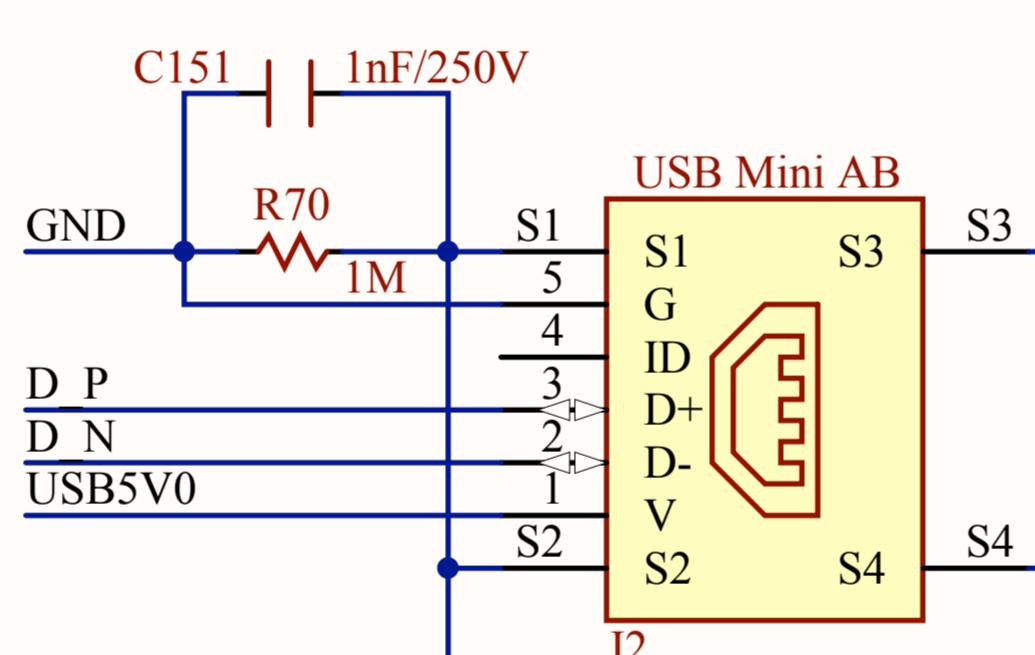
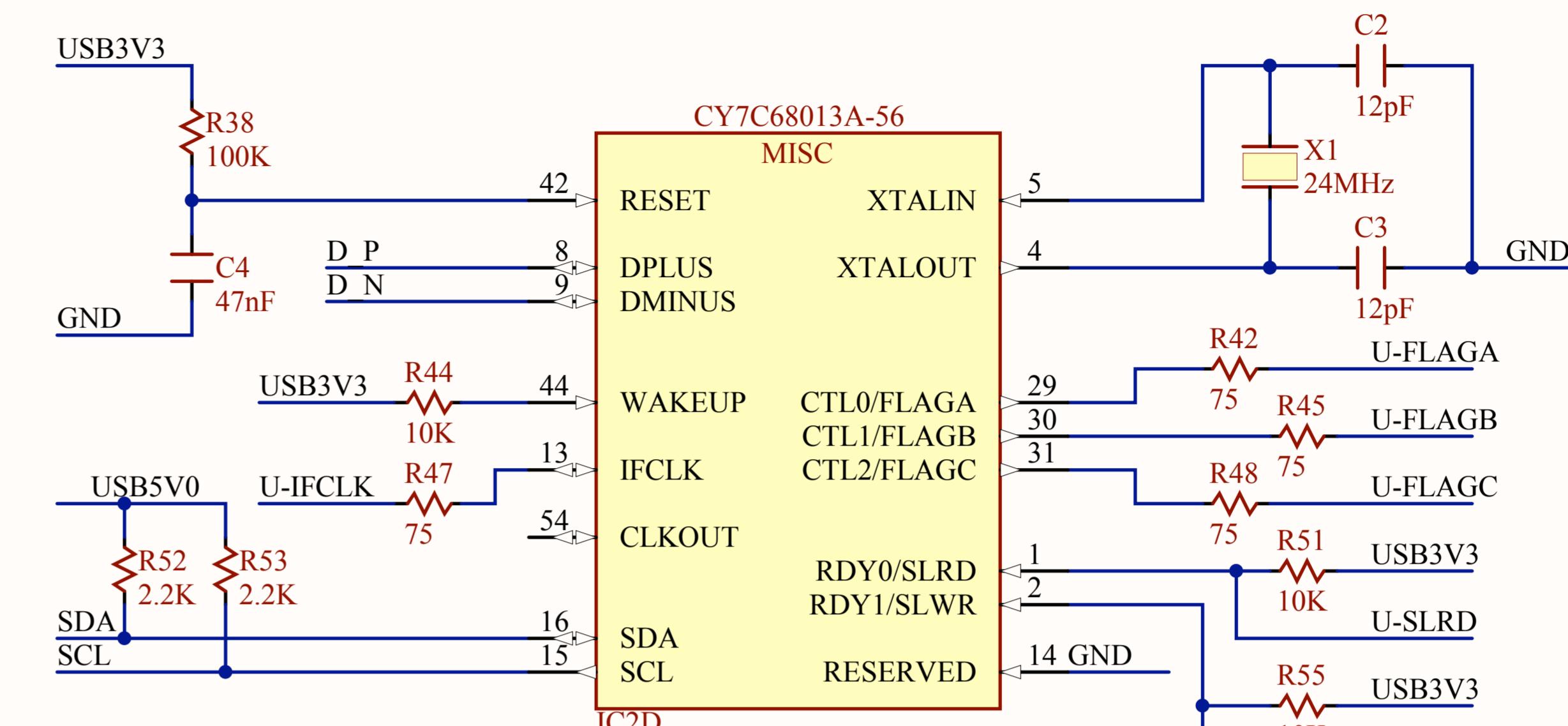
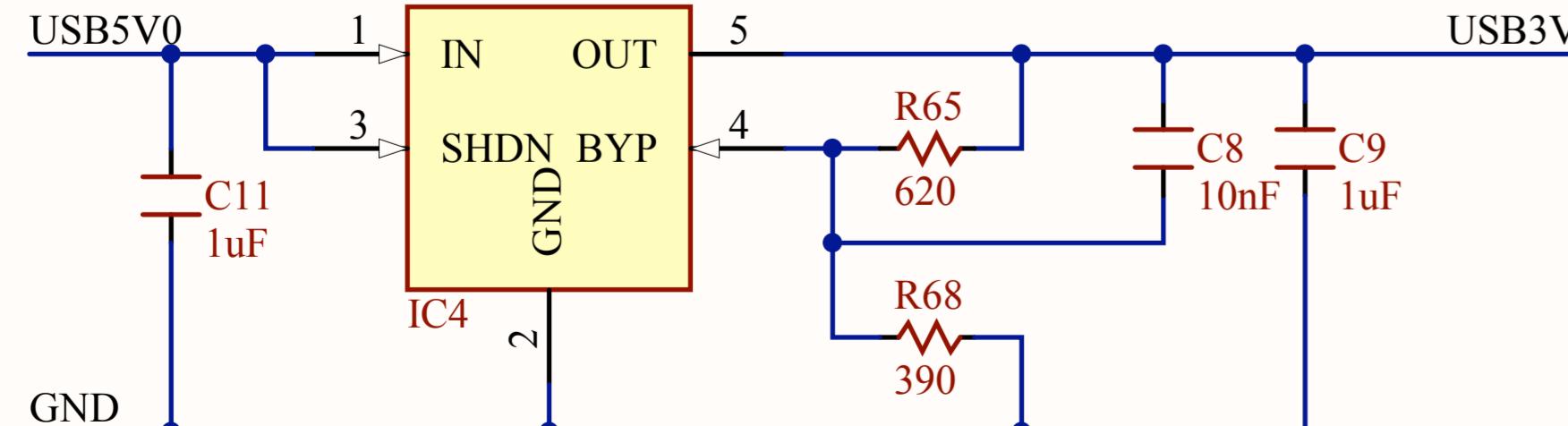
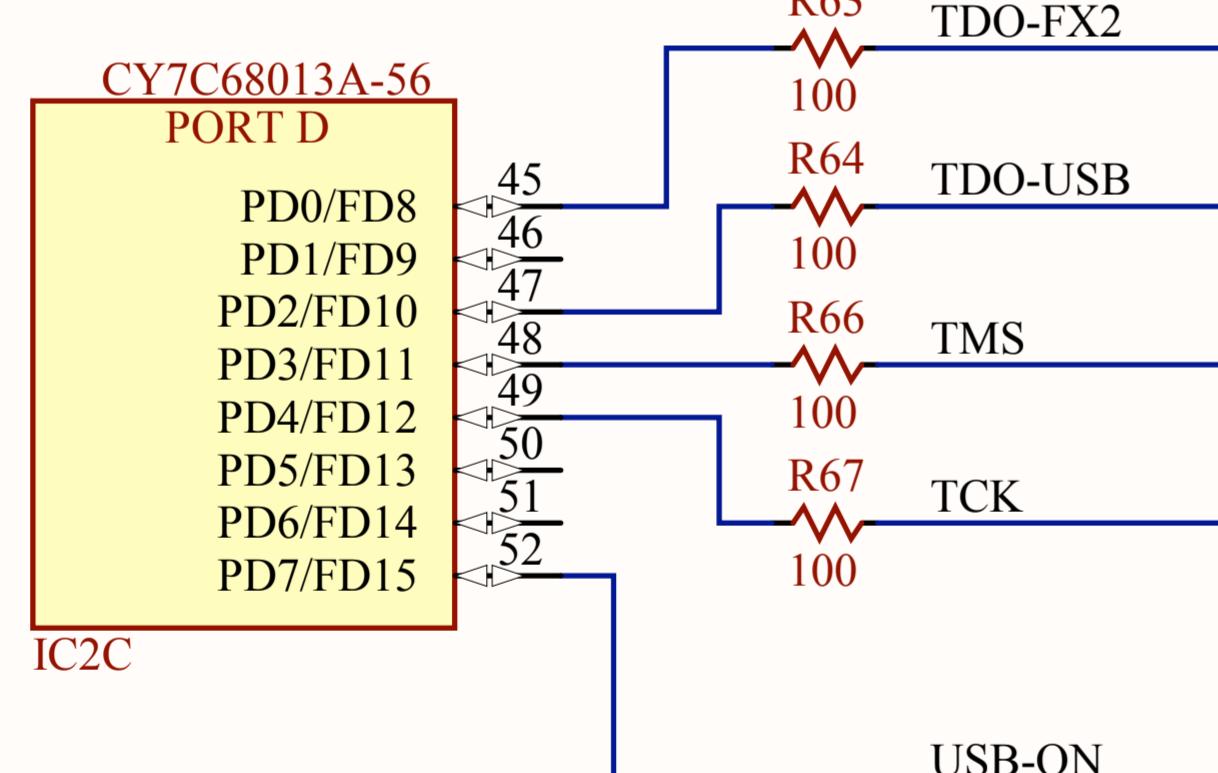
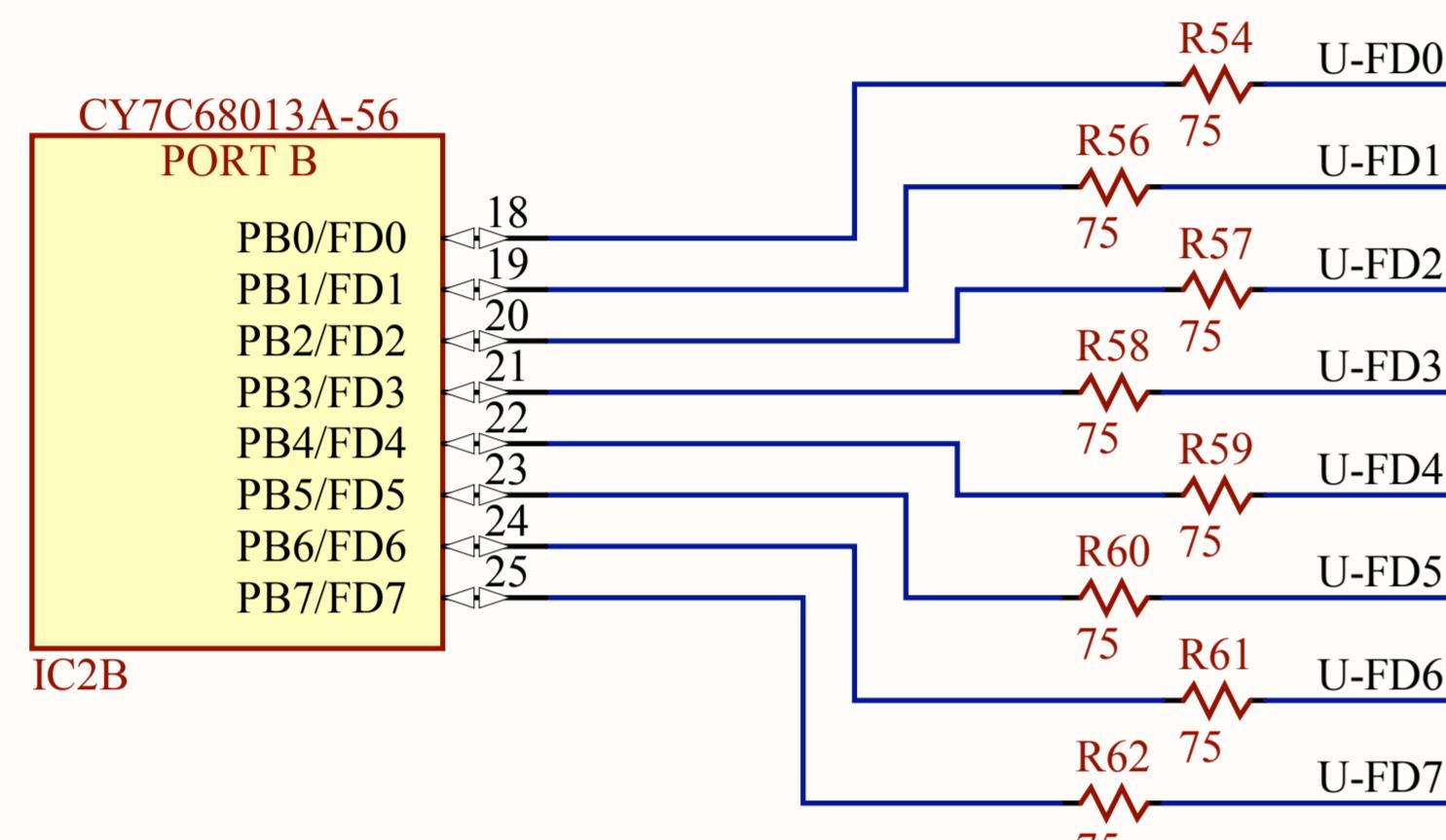
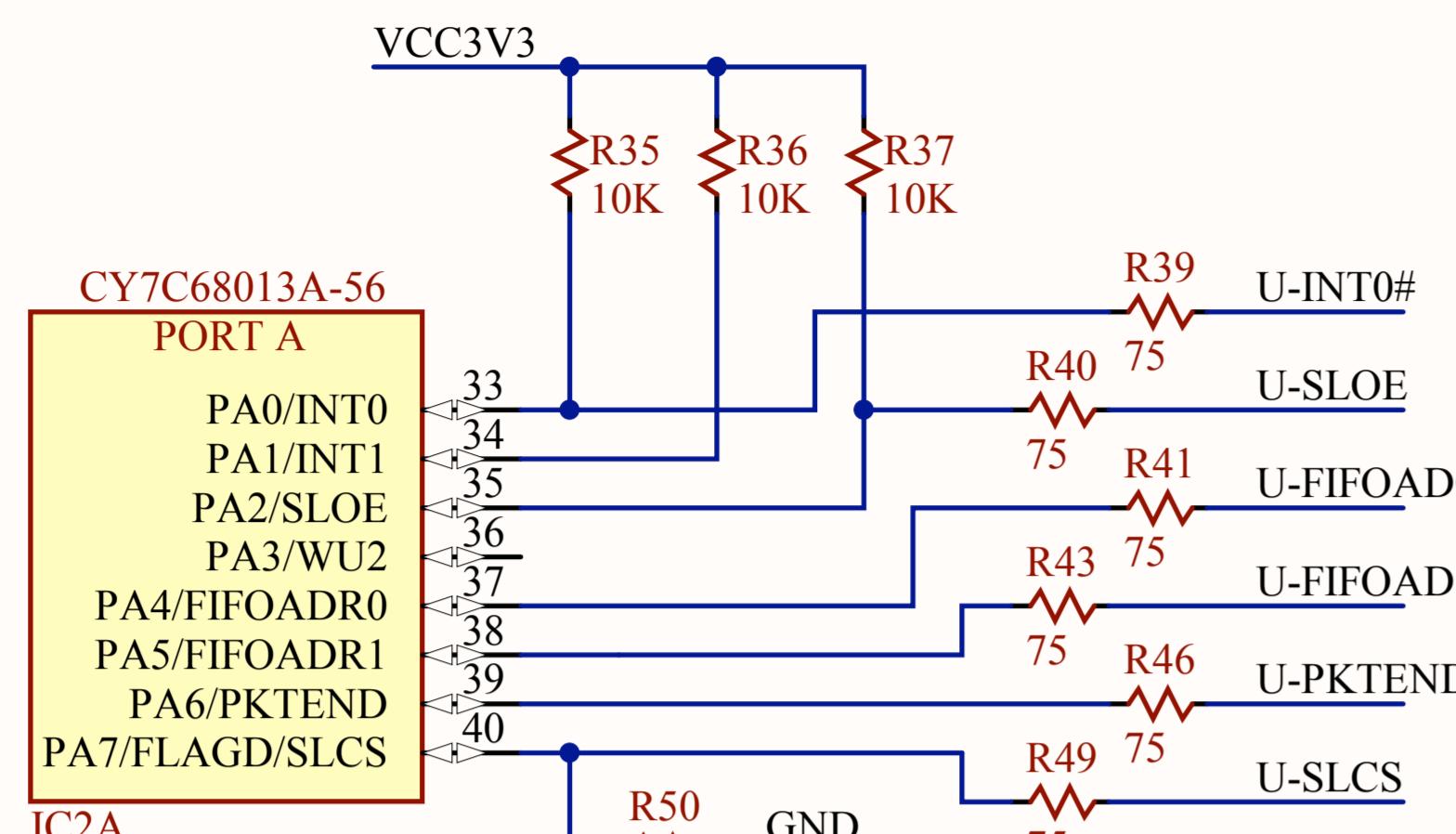
Author GMA

Date 7/19/2007

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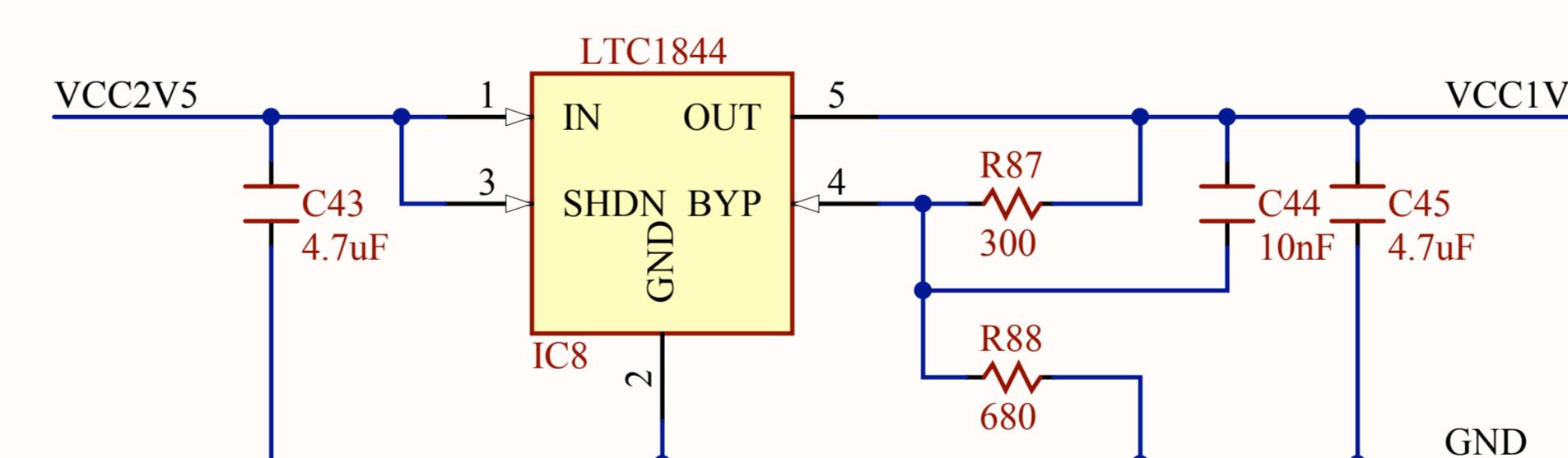
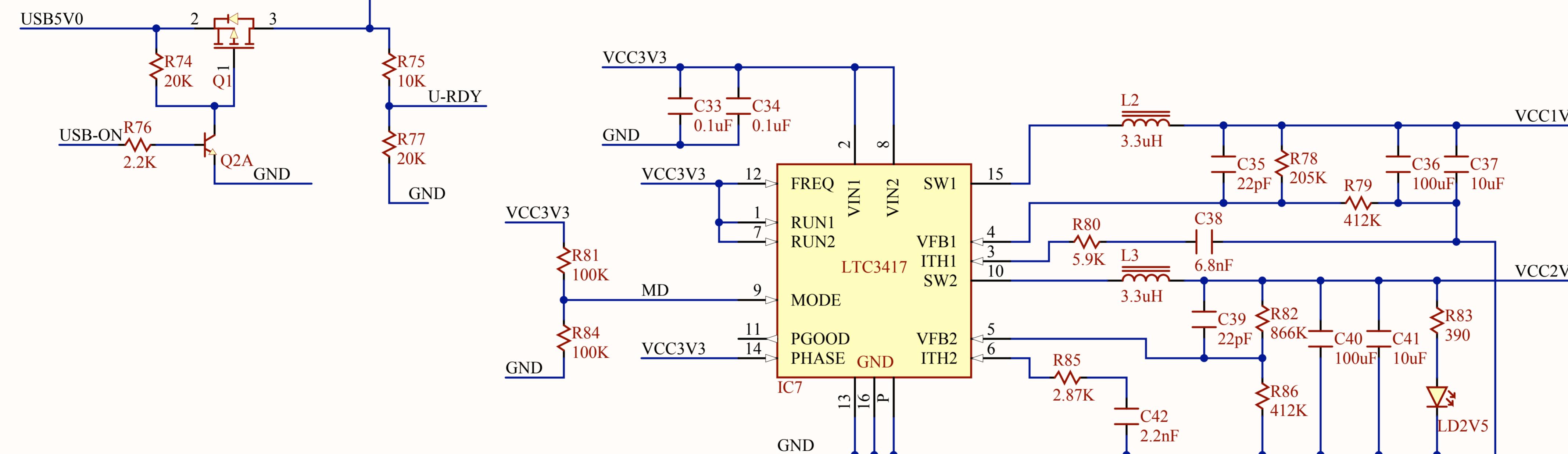
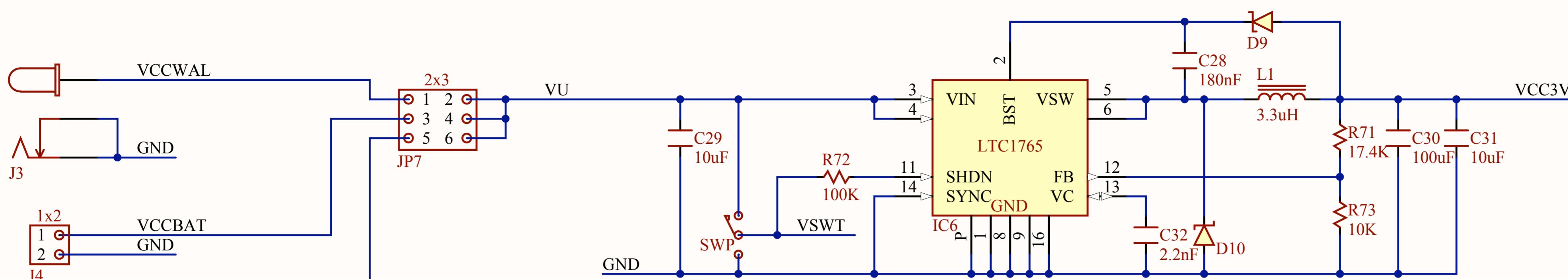


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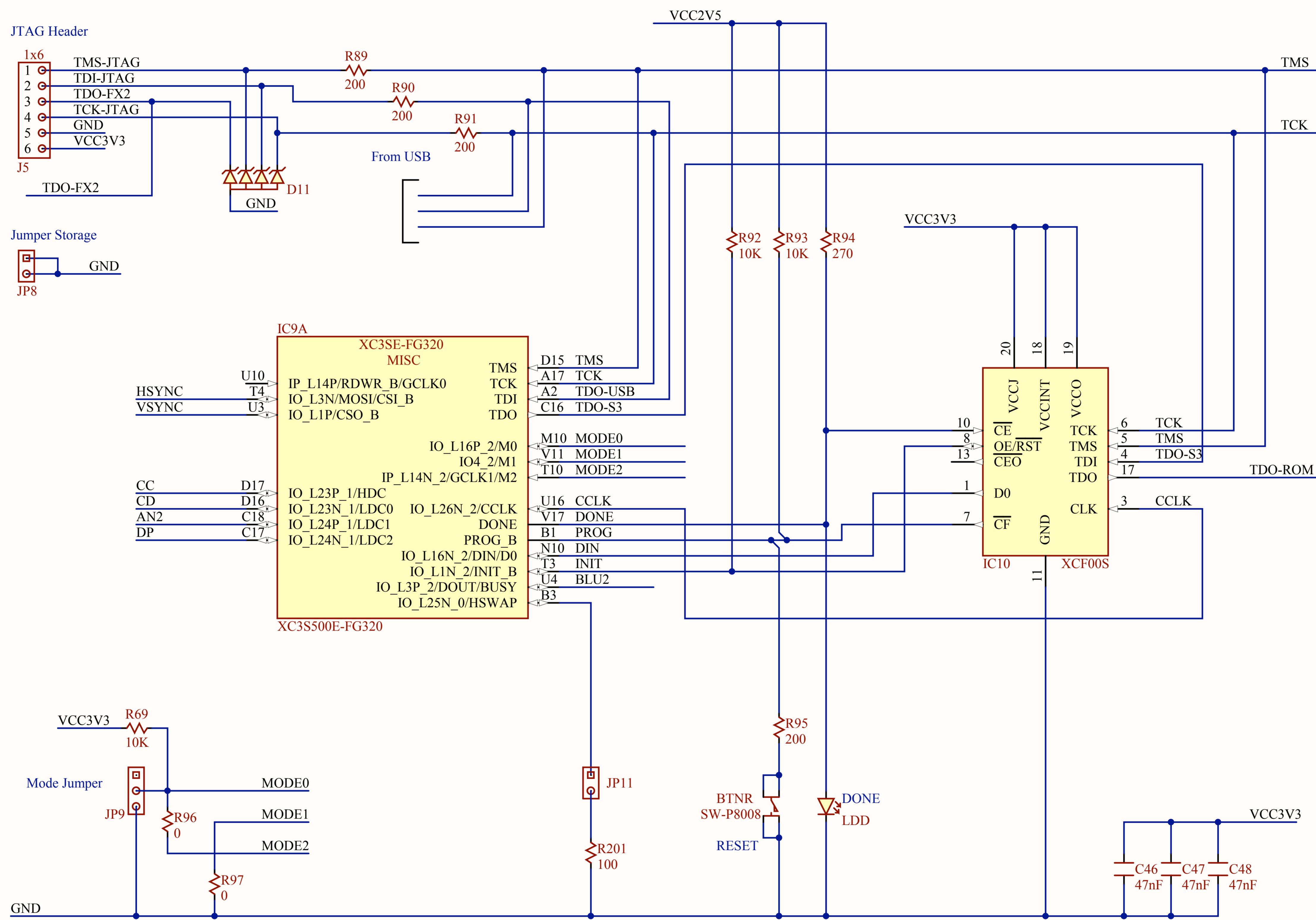


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Circuit
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Engineer NEA
Author GMA
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Title

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Engineer NEA

Author GMA

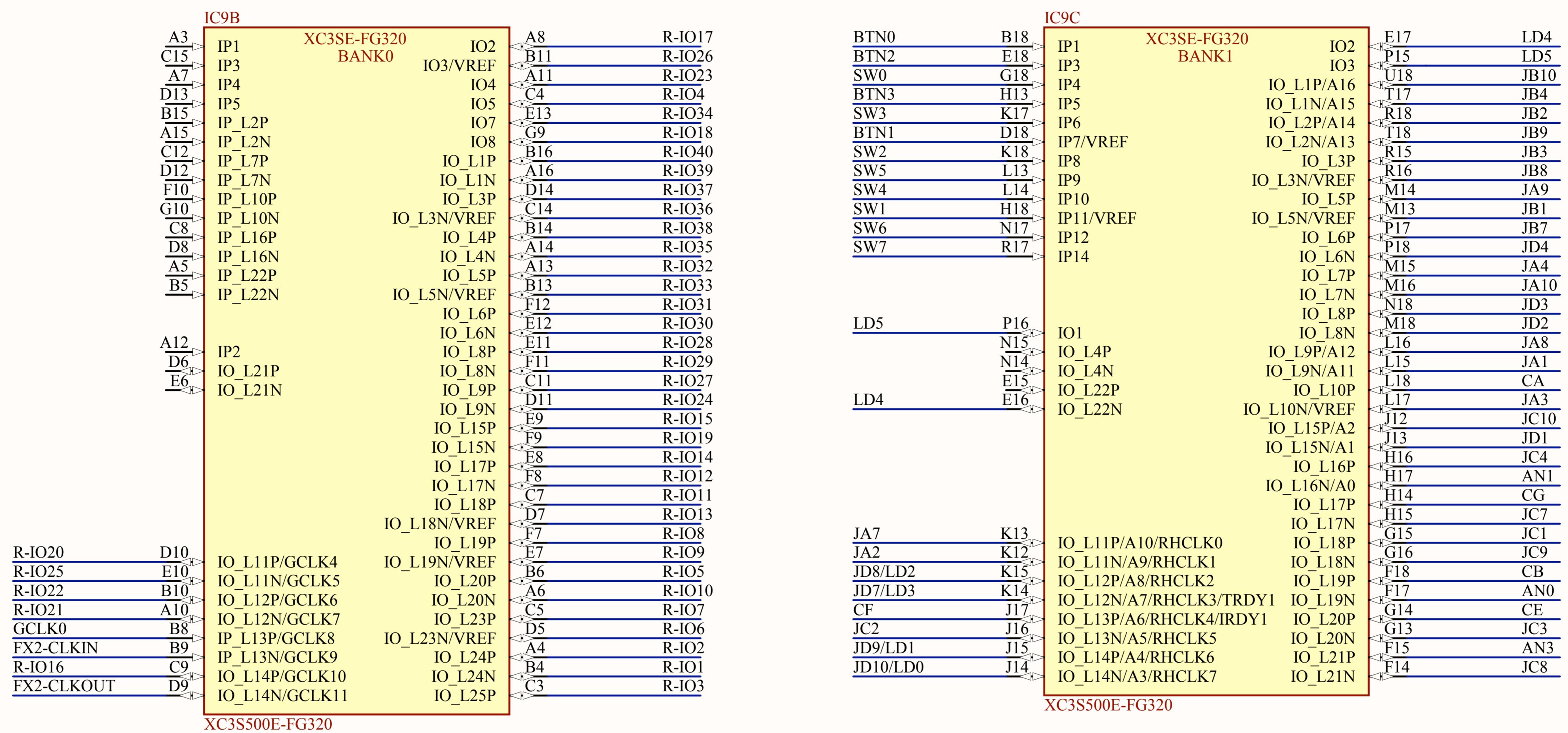
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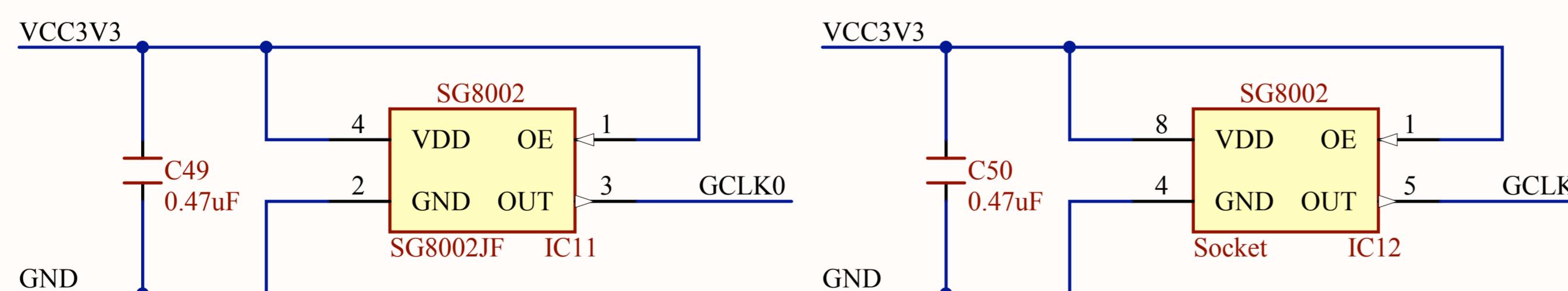
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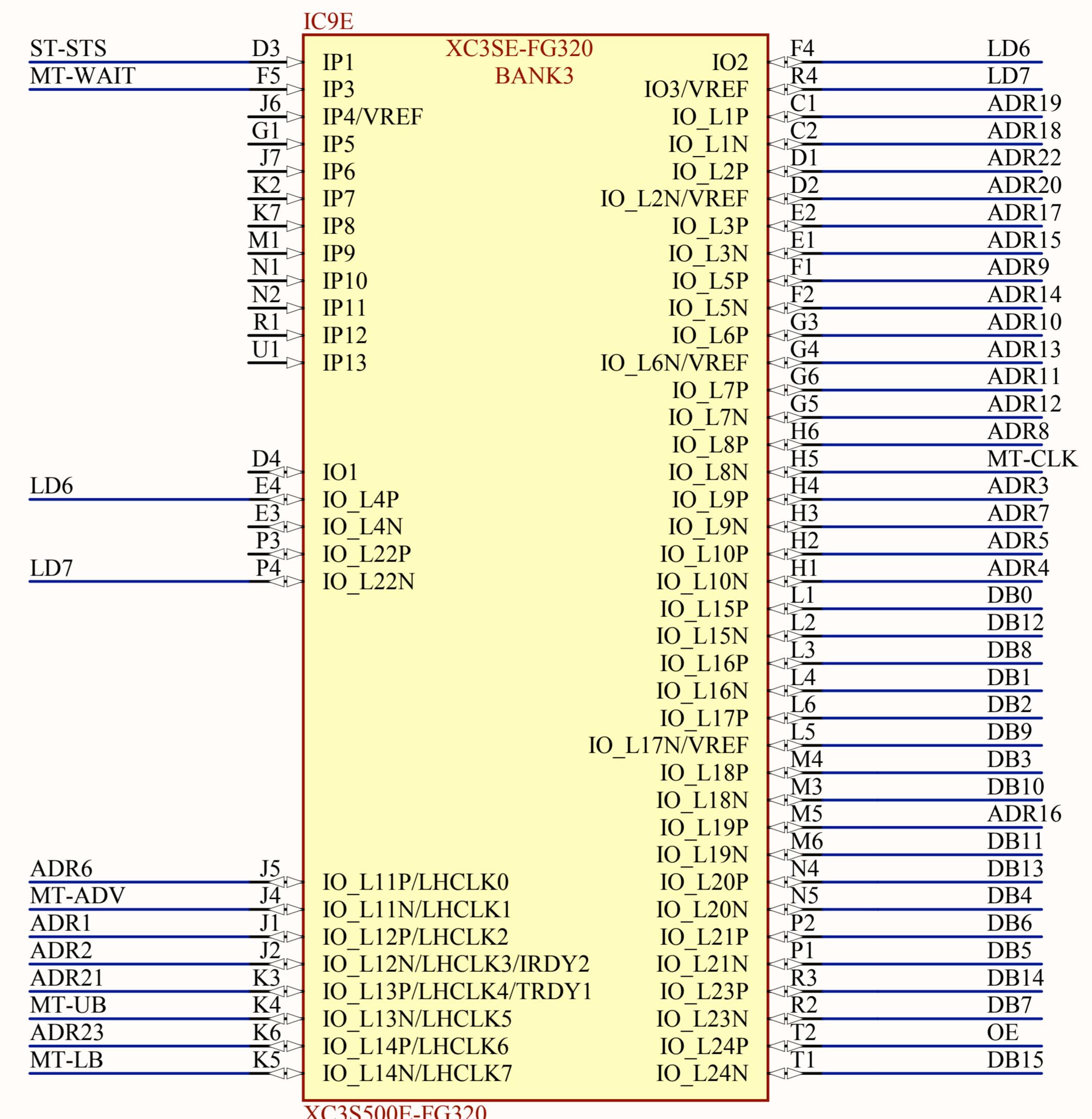
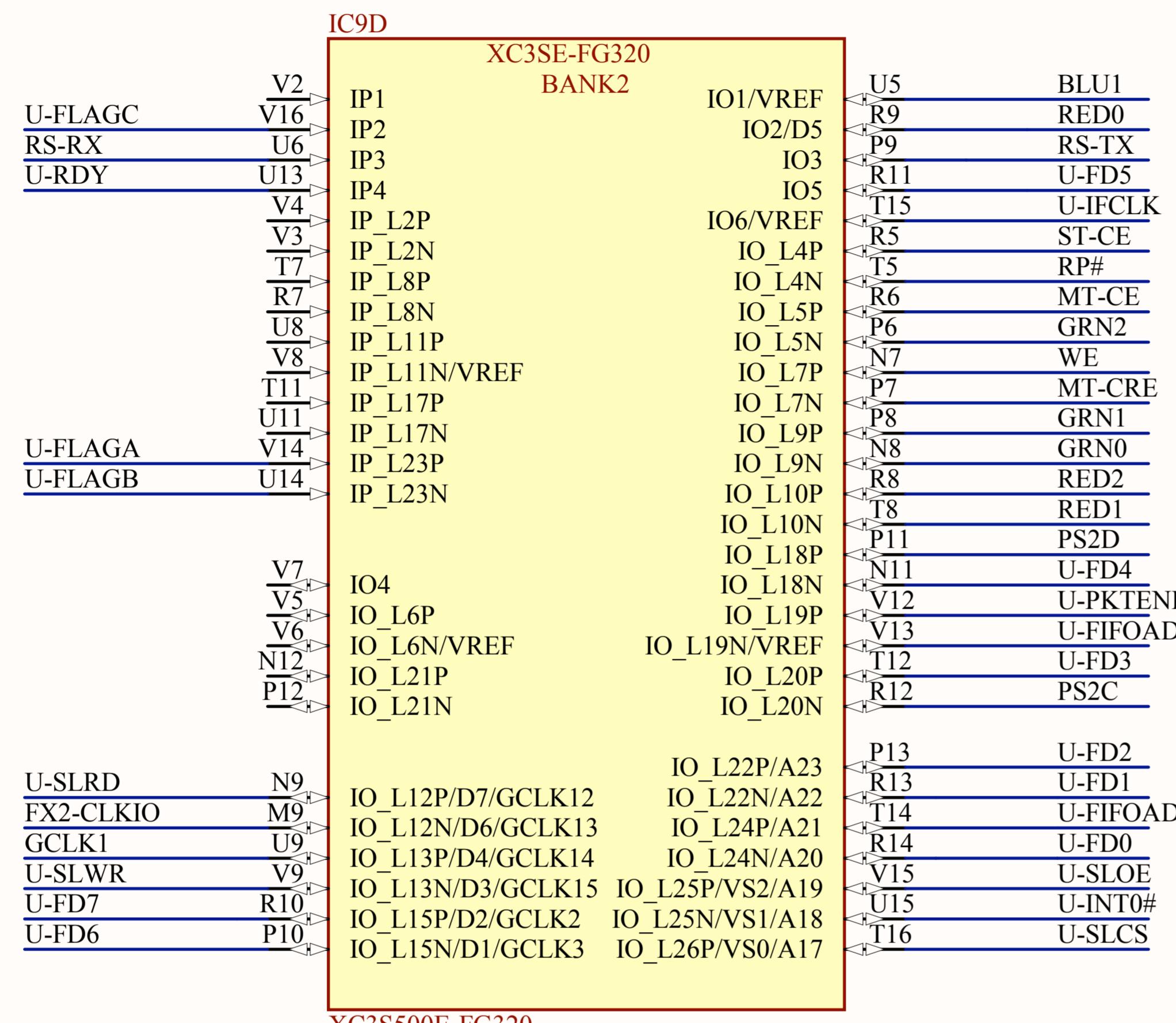
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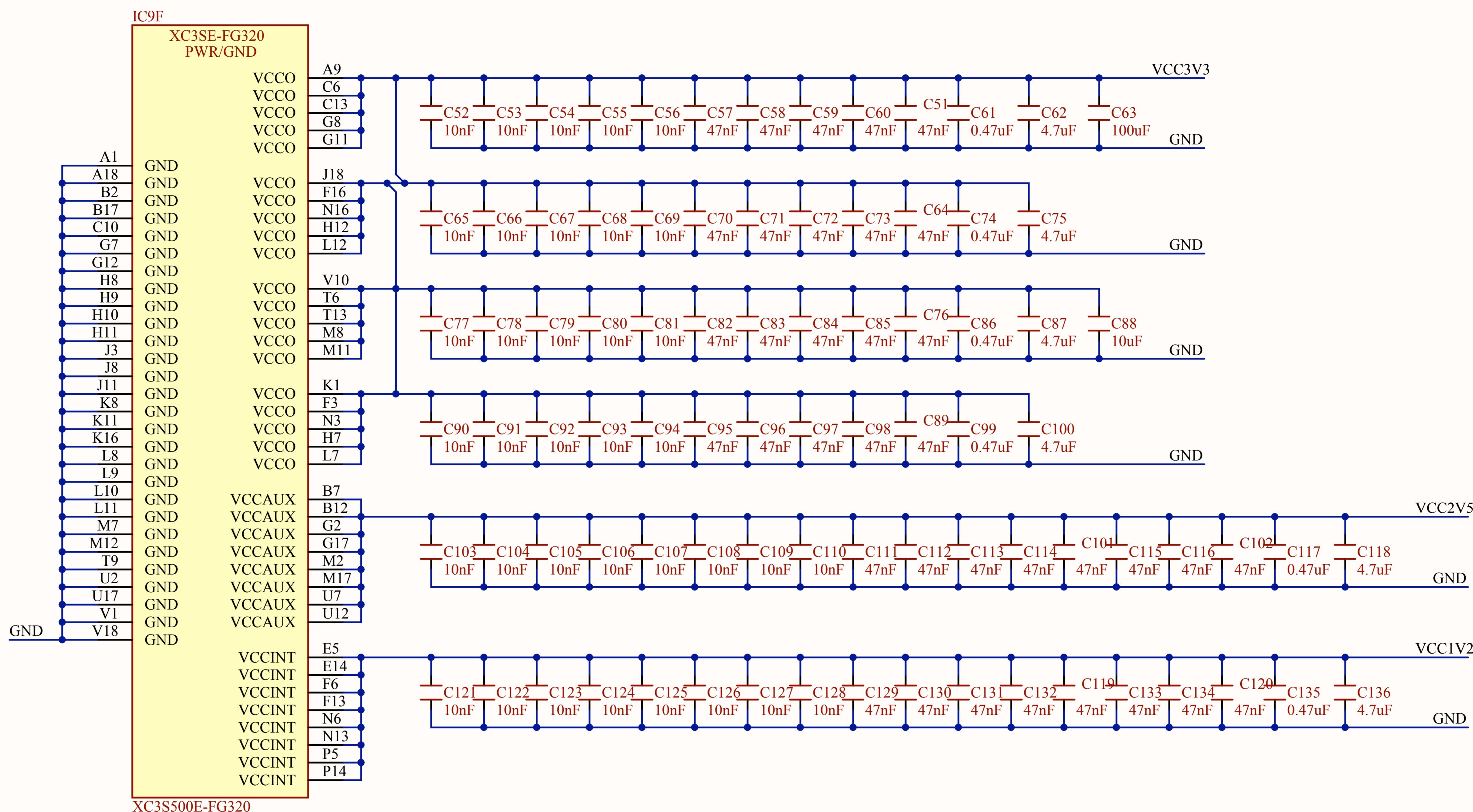
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Nexys II

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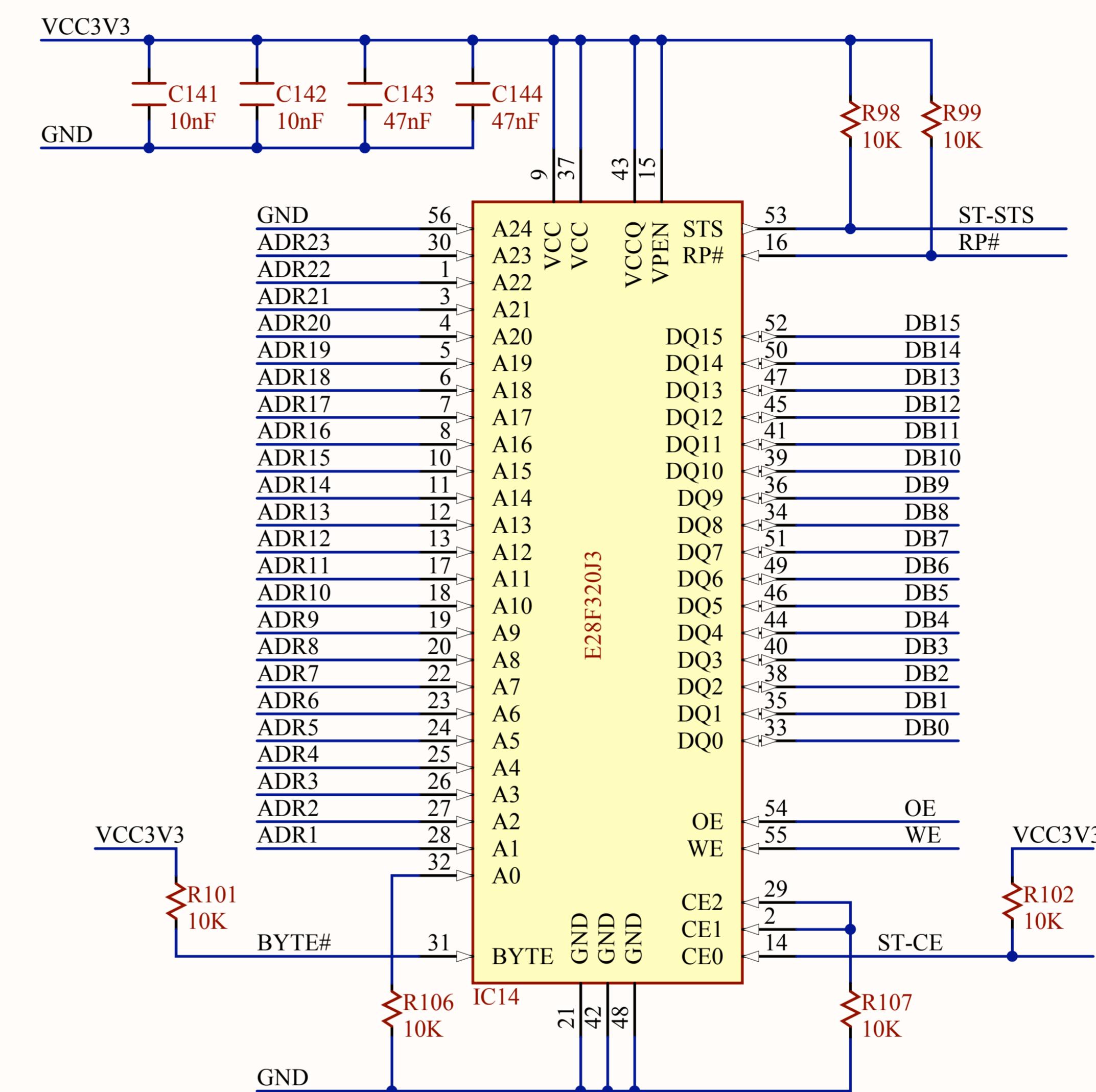
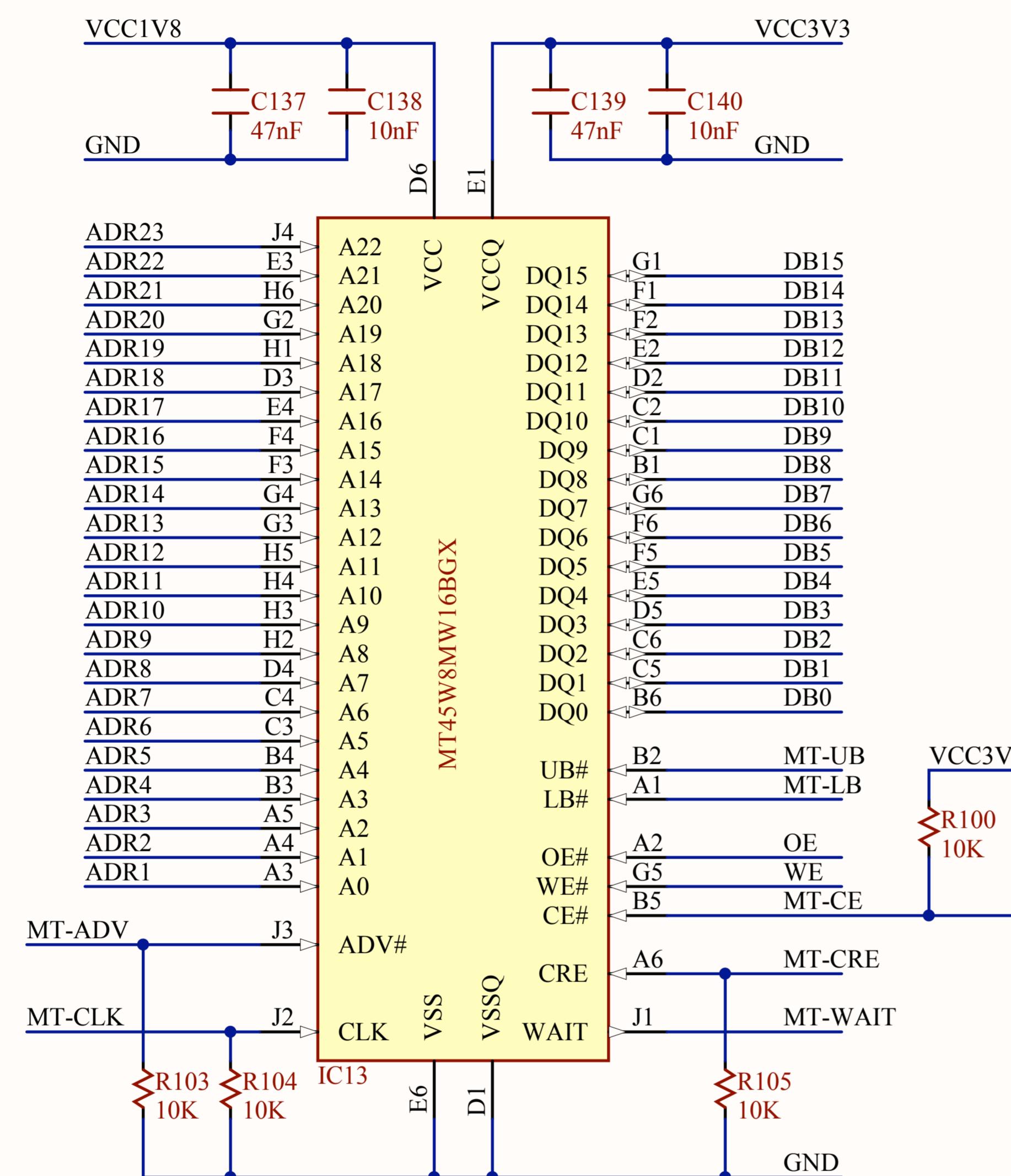
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FPGA Power	
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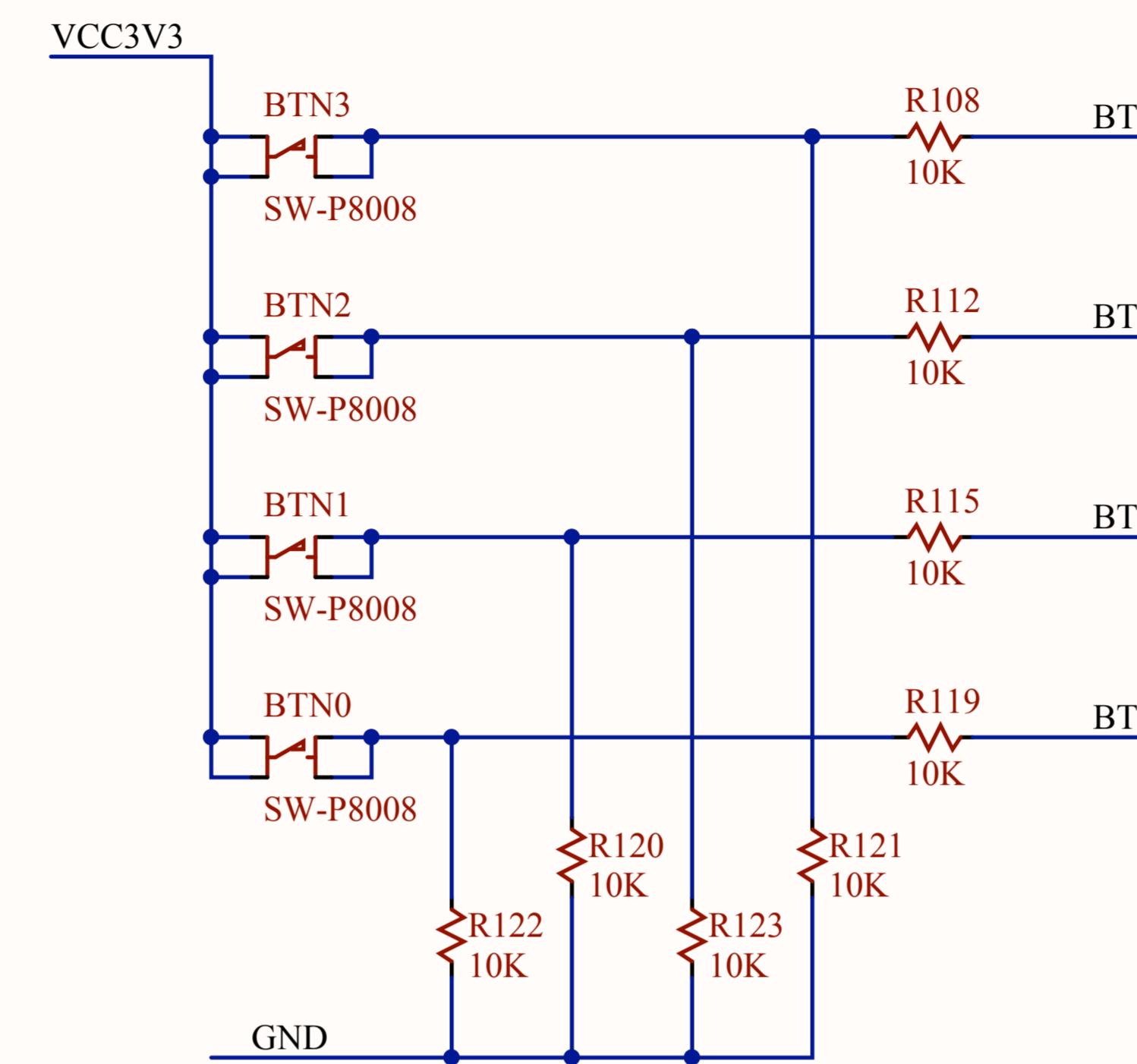
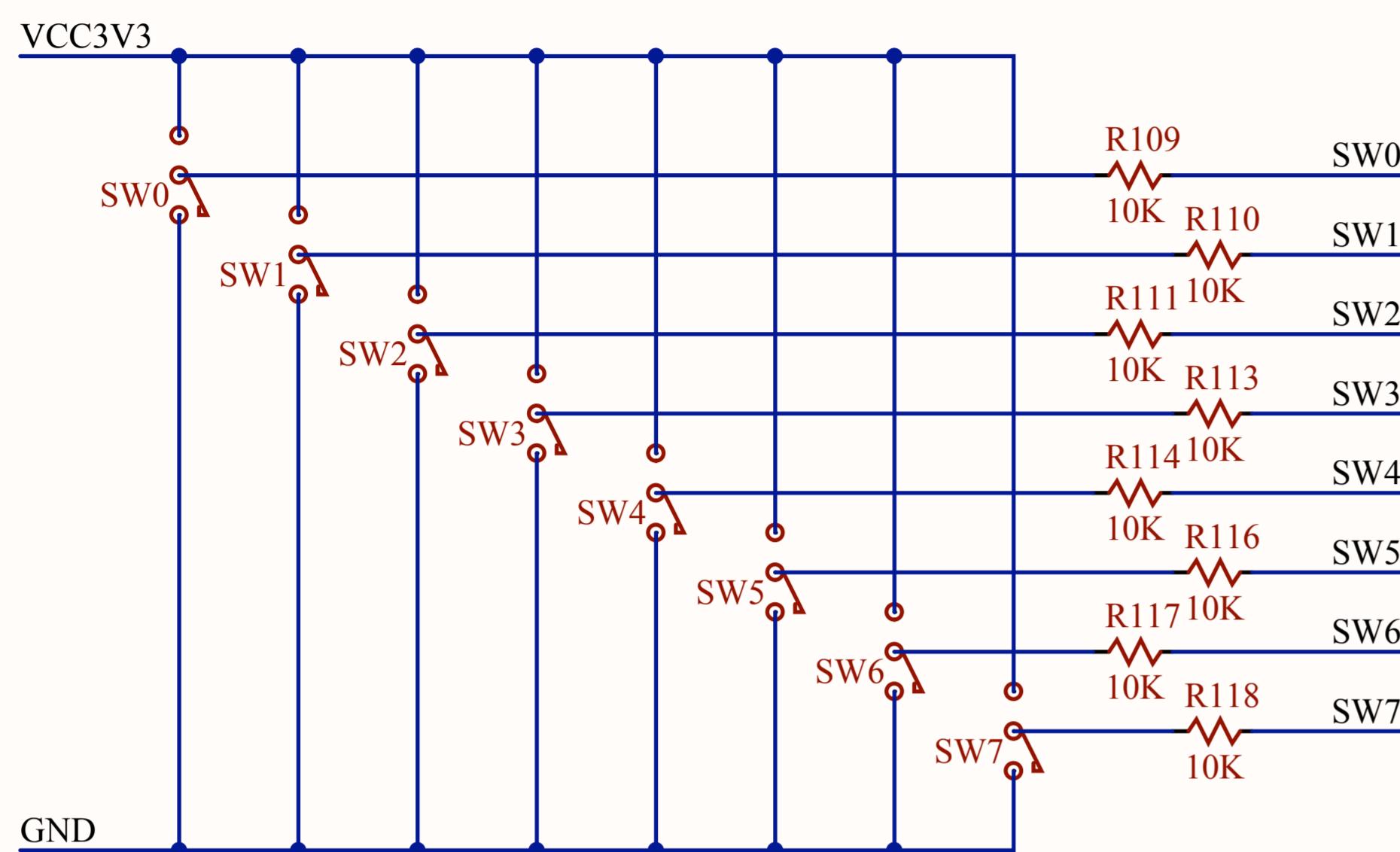
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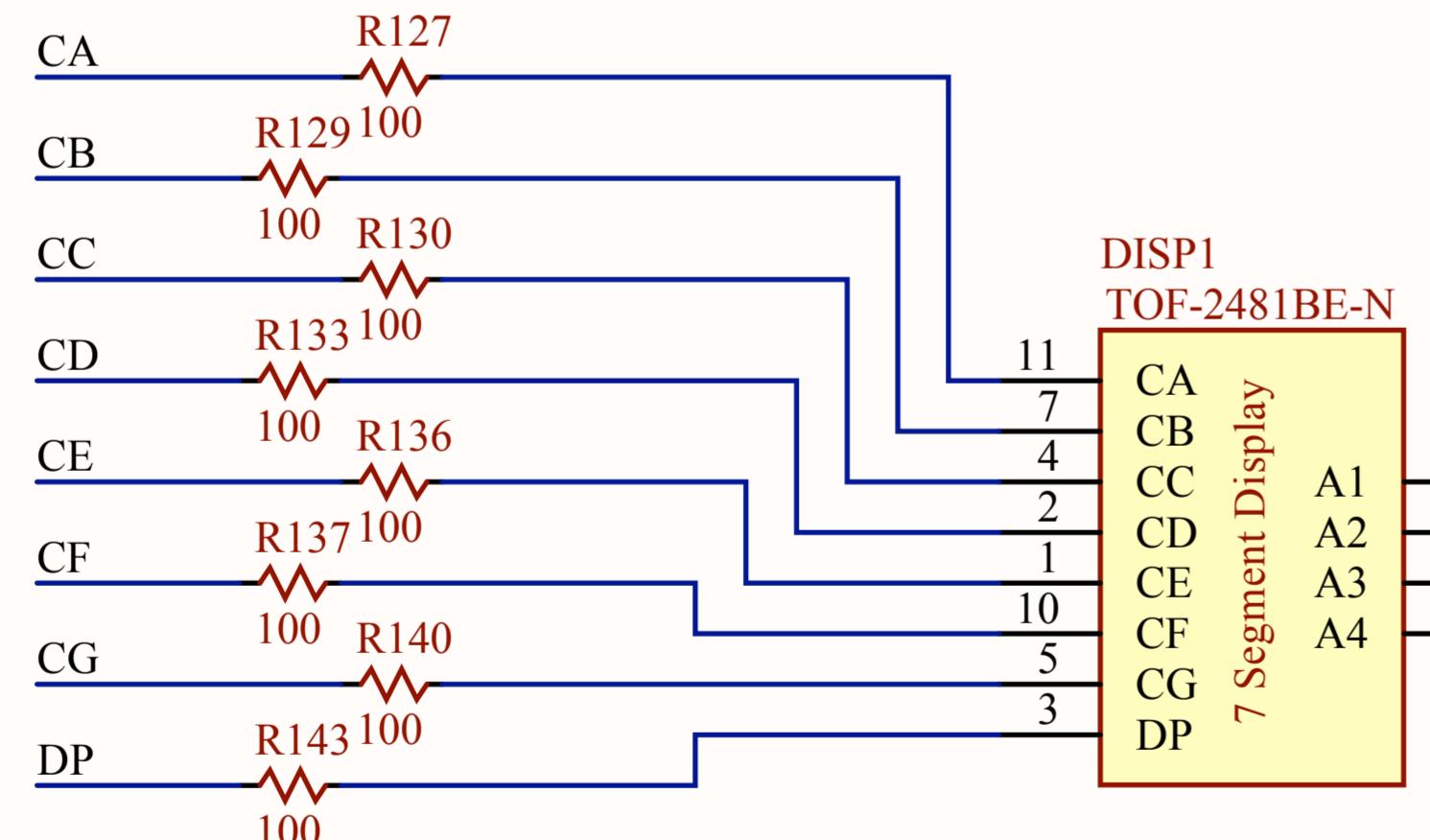
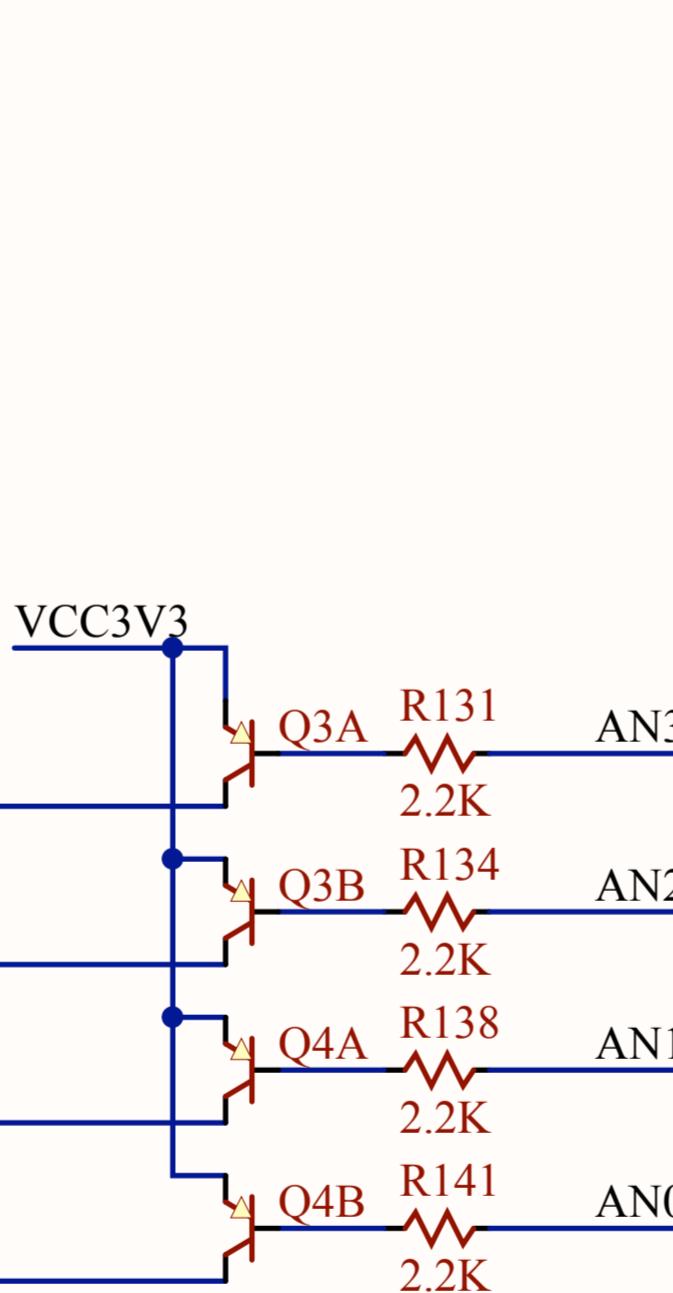
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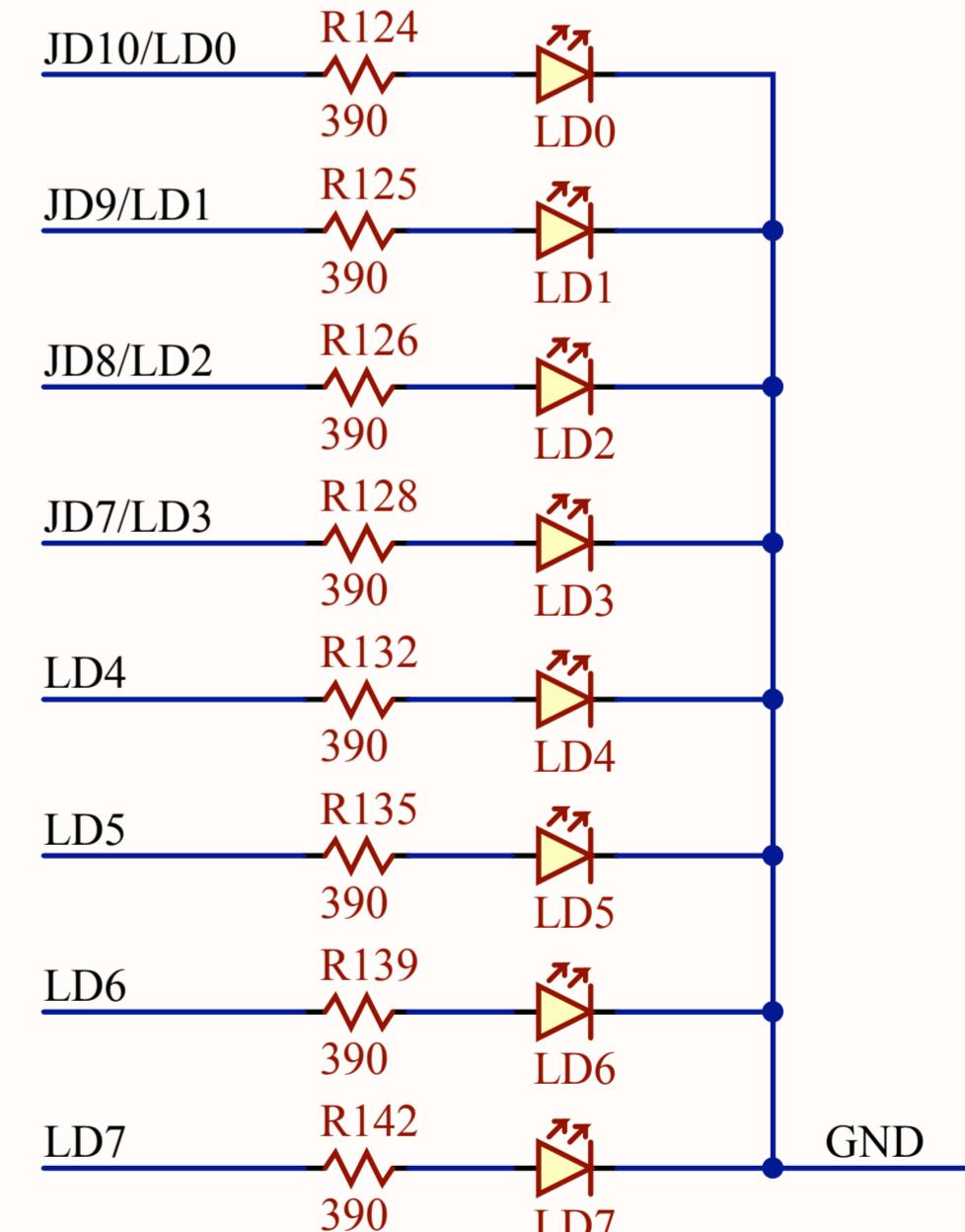
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Circuit

IO and Display

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Engineer NEA

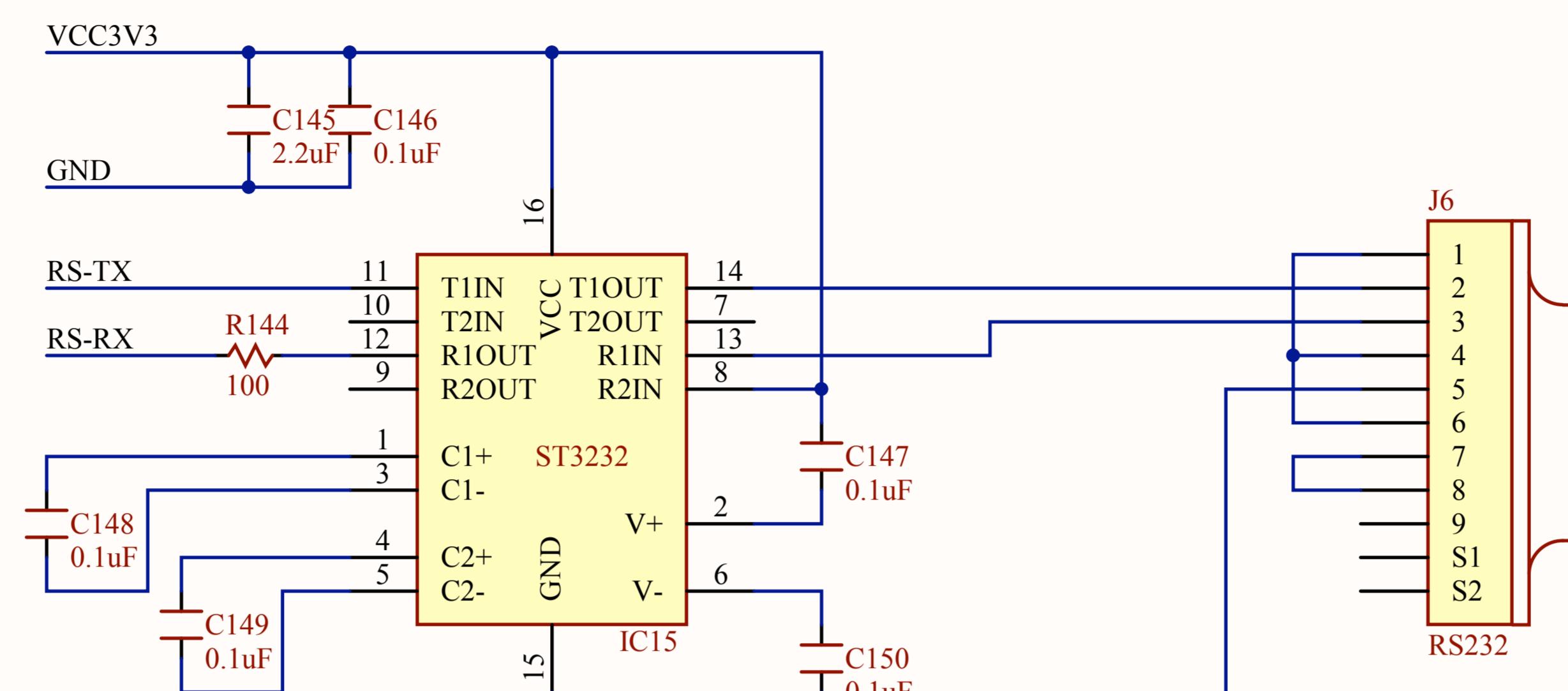
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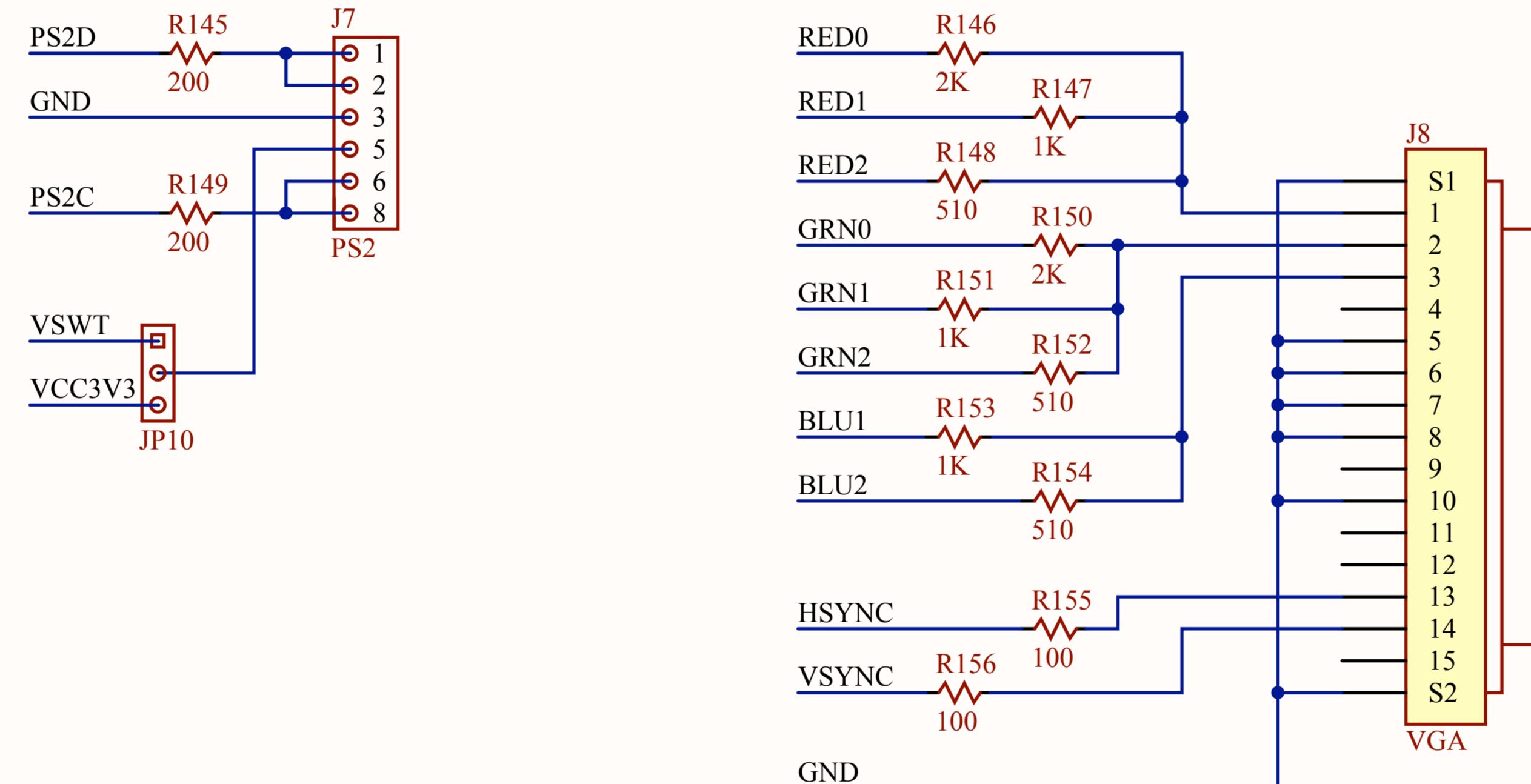


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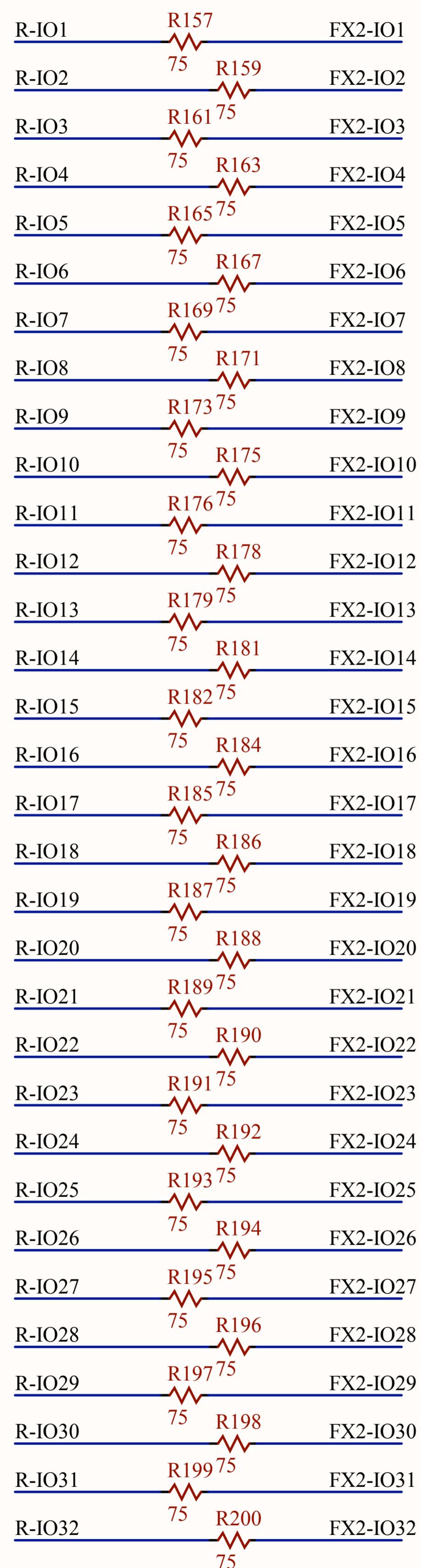
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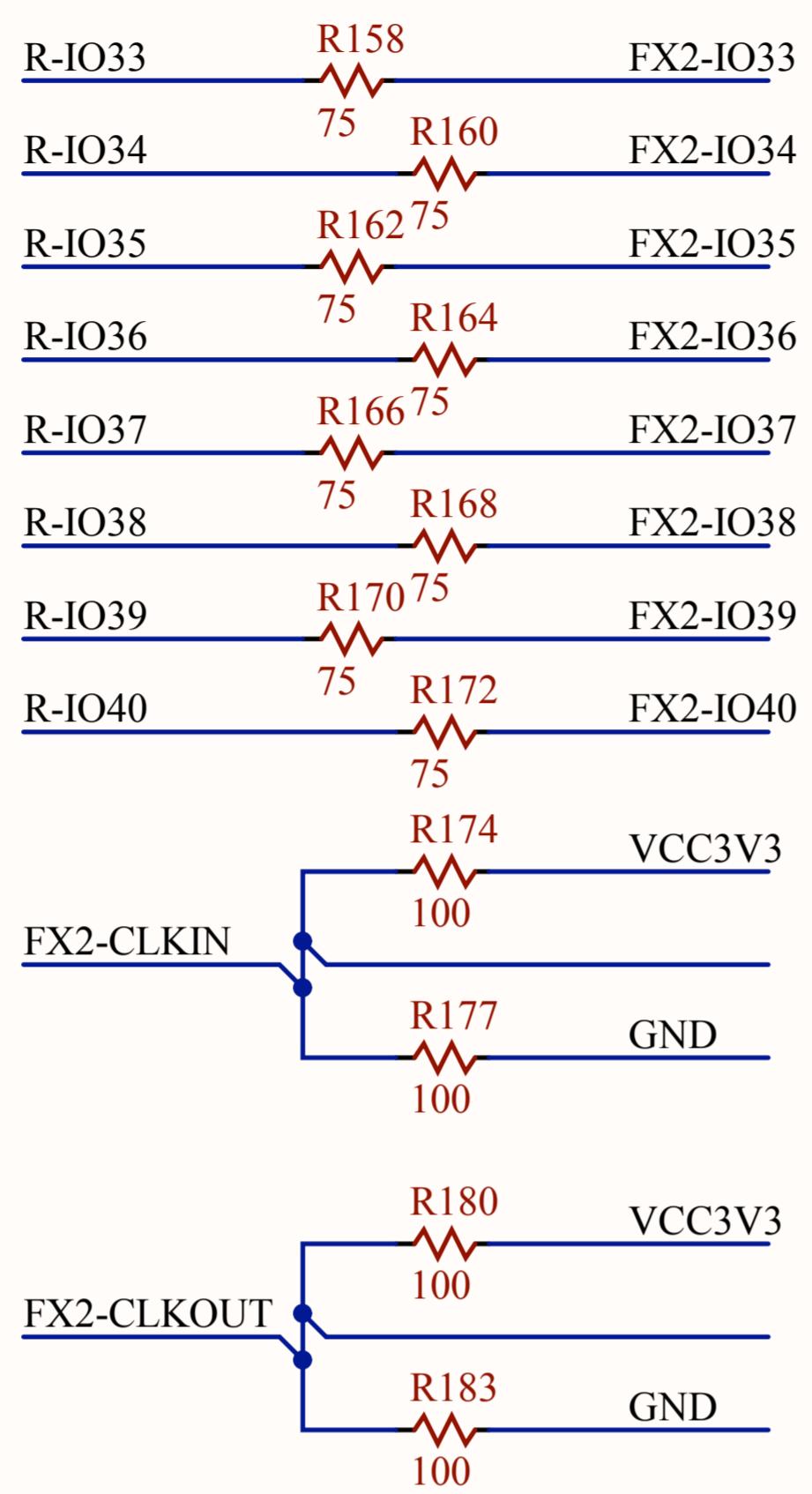
Circuit	RS232, VGA, and PS/2
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A



B



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Author	GMA		
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