Latches, Spinlocks, and Lock Free Data Structures

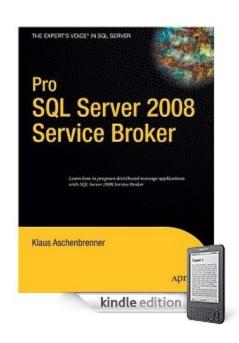


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About me

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 - http://www.SQLpassion.at/academy
 - Free Newsletter, Training Videos



Caution!

If you are latched or spinlocked by the session, there is always a way to back-off: apply a lock-free operation!

Caution!



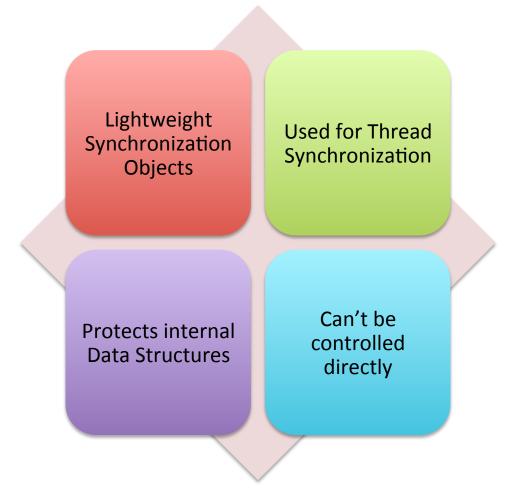
Agenda

- Latches
- Spinlocks
- Lock Free Data Structures

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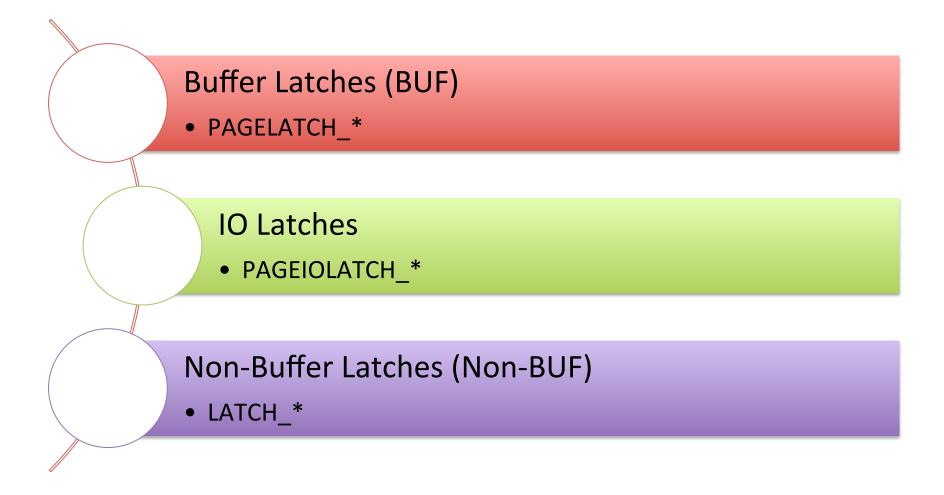
Latches – what are they?



Locks vs. Latches

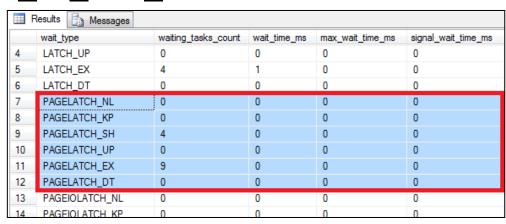
	Locks	Latches
Controls	Transactions	Threads
Protects	Database content	In-Memory Data Structures
During	Entire transaction	Critical section
Modes	Shared, Update, Exclusive, Intention	Keep, Shared, Update, Exclusive, Destroy
Deadlock	Detection & Resolution	Avoidance through careful coding techniques
Kept in	Lock Manager's Hashtable	Protected Data Structure

Latch Types



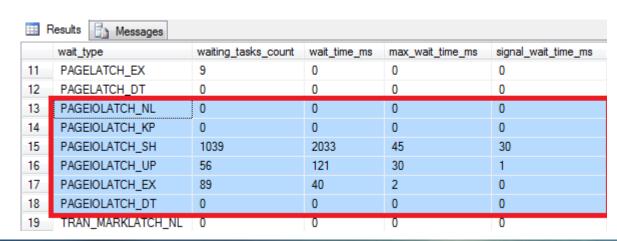
BUF-Latches

- Protect all kinds of pages when they are accessed from the Buffer Pool
 - Data Pages/Index Pages
 - PFS/SGAM/GAM Pages
 - IAM Pages
- PAGELATCH_*
- Accessible through sys.dm_os_wait_stats



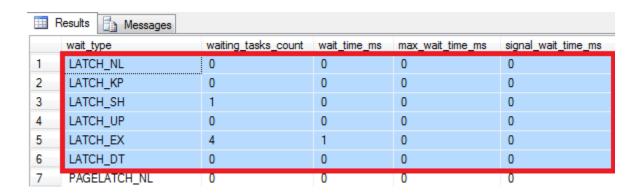
I/O Latches

- Subset of BUF Latches
- Used when outstanding I/O operations are done against pages in the Buffer Pool
 - Disk to Memory Transfers (Reading)
 - Memory to Disk Transfers (Writing)
- SQL Server is waiting on the I/O subsystem
- PAGEIOLATCH_*



Non-BUF Latches

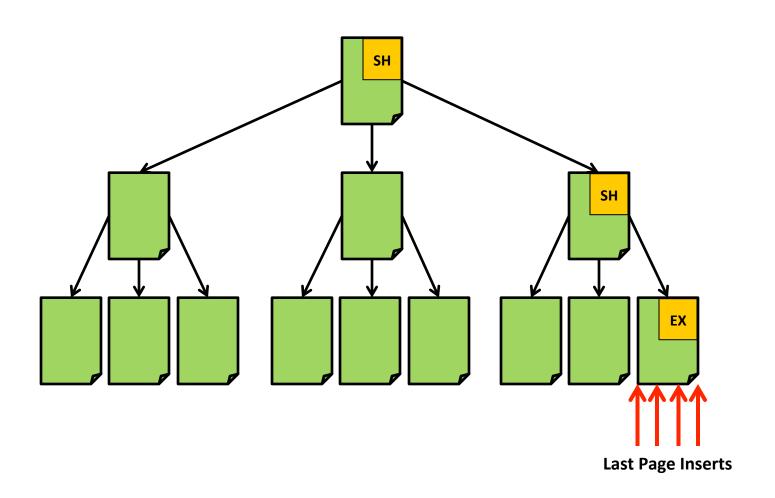
- Guarantees the consistency of any other in-memory structures other than Buffer Pool pages
- LATCH *
- Detailed breakdown in sys.dm_os_latch_stats



Demo

Exploring Latches

Last Page Insert Latch Contention



Current Solutions

- Random Clustered Keys
 - UNIQUEIDENTIFIER
 - Distributes the INSERTs across the Leaf Level
 - Larger Lookup Values in Non-Clustered Indexes...
 - Index Fragmentation
- Hash Partitioning
 - Distribute INSERTs across different partitions
 - Every CPU core has its own partition
 - You can't additionally partition your table...
 - Partition Elimination is almost impossible...
- In-Memory OLTP
 - SQL Server 2014+

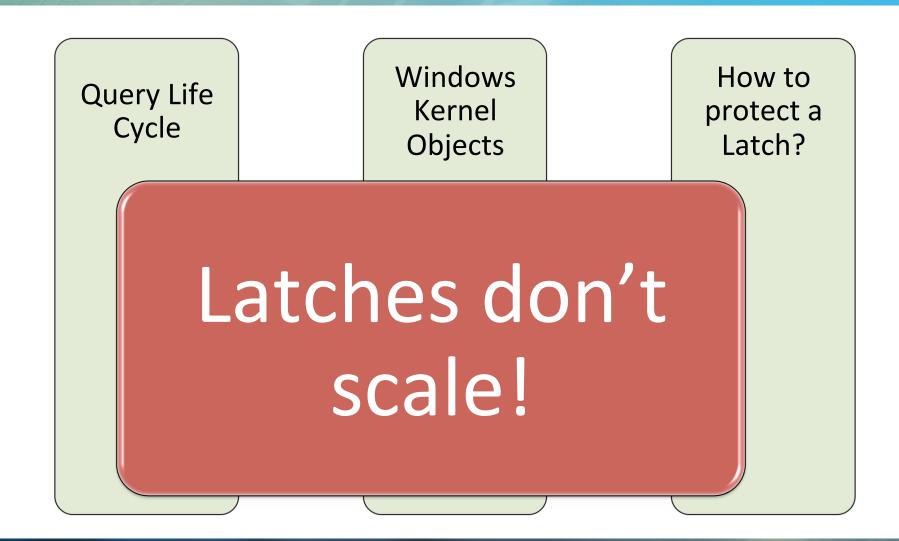
Demo

Last Page Insert Latch Contention

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Why Spinlocks?



Spinlock Internals

- It's a Mutex (Mutual Exclusion)
 - No waiting list
 - No compatibility matrix
 - You hold the spinlock, or not!
- Used to protect "busy" data structures
 - Read or written very frequently
 - Held for a short amount of time
 - E.g. Lock Manager (LOCK_HASH)

Spinlock Contention

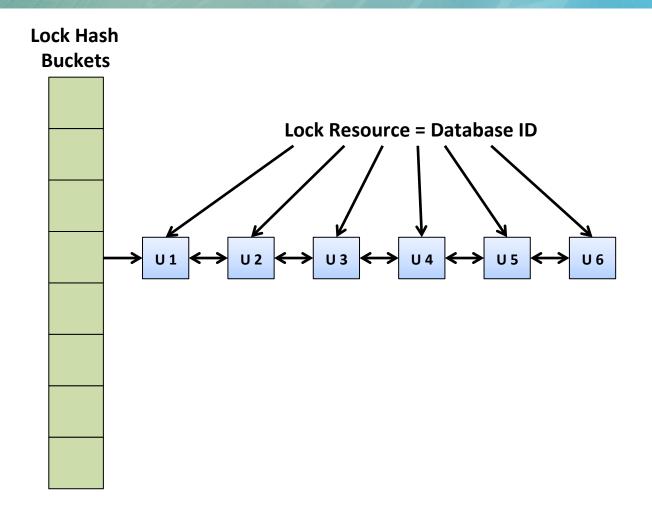
Problem

- Tight spinning around a busy data structure
- Short waits are expected!
- Exponential back-off since SQL Server 2008 R2+

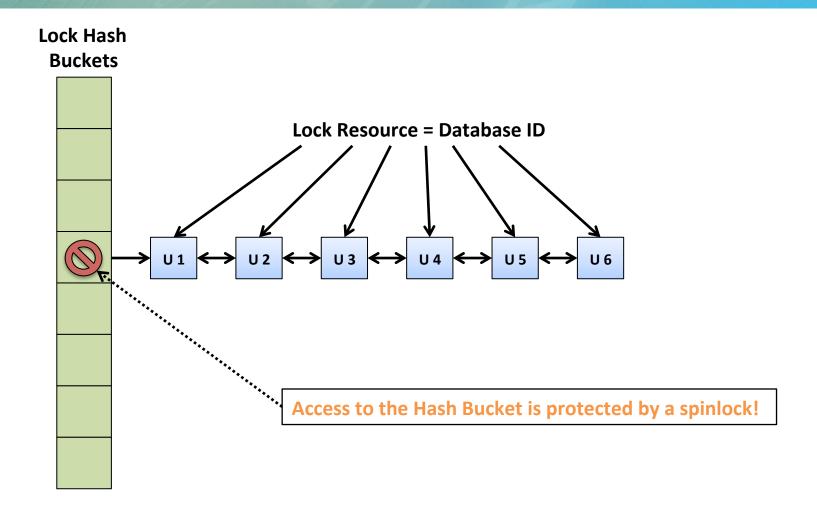
Symptoms

- High CPU usage without performing useful work
- High "backoffs" in sys.dm_os_spinlock_stats

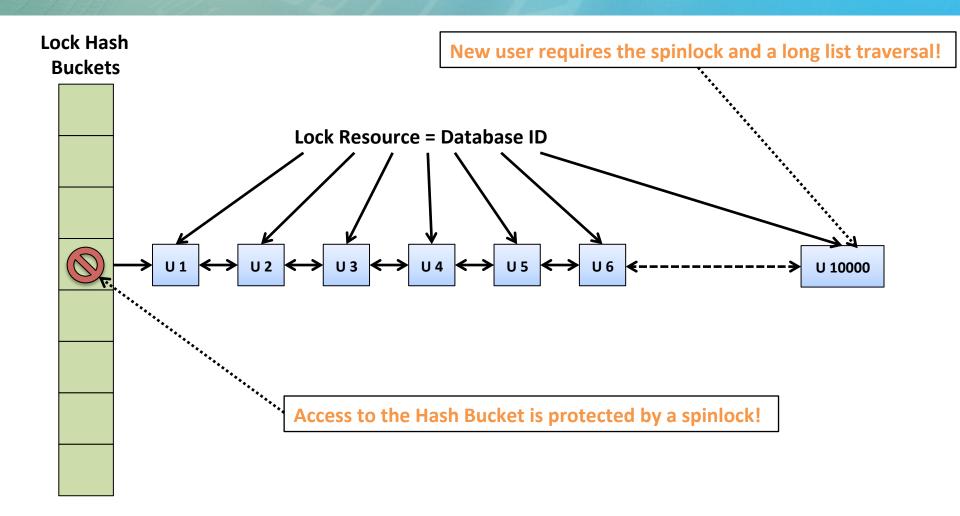
LOCK_HASH Example



LOCK_HASH Example



LOCK_HASH Example



Demo

Debugging Spinlock Contention

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Non-Blocking Algorithms

"A non-blocking algorithm ensures that threads competing for a shared resource do not have their execution indefinitely postponed by mutual exclusion. A non-blocking algorithm is lock-free if there is guaranteed system-wide progress regardless of scheduling."

Source: http://en.wikipedia.org/wiki/Non-blocking_algorithm

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
   if (*value == expected)
      *value = newValue;
   return temp;
void Foo() {
   do {
      while (compare and swap(&lock, UNLOCKED, LOCKED) != 0)
         ; /* Do nothing */
      /* Critical section */
      val = val + 5;
      lock = UNLOCKED;
   } while (true);
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
   if (*value == expected)
      *value = newValue;
   return temp;
void Foo() {
   do {
      while (compare and swap(&lock, UNLOCKED, LOCKED) != 0)
         ; /* Do nothing */
      /* Critical section */
                                           We want to execute this code in a thread-
      val = val + 5;
                                           safe manner!
      lock = UNLOCKED;
   } while (true);
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
                                      Implemented through one atomic
   if (*value == expected)
                                      hardware instruction: CMPXCHG
      *value = newValue;
   return temp;
void Foo() {
   do {
      while (compare and swap(&lock, UNLOCKED, LOCKED) != 0)
         ; /* Do nothing */
      /* Critical section */
      val = val + 5;
      lock = UNLOCKED;
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   return temp;
void Foo() {
   do {
      while (compare and swap(&lock, UNLOCKED, LOCKED) != 0)
         ; /* Do nothing */
      /* Critical section */
                                                             There is a shared
      val = val + 5;
                                                             resource involved!
      lock = UNLOCKED;
   } while (true);
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
                                       Implemente/
   if (*value == expected)
                                                      If one thread holds the
                                       hardware/
      *value = newValue;
                                                        spinlock, and gets
                                                     suspended, we get stuck
   return temp;
                                                           in the loop!
void Foo() {
   do {
      while (compare and swap(&lock, UNLOCKED, LOCKED) != 0)
         ; /* Do nothing */
      /* Critical section */
                                                              There is a shared
      val = val + 5;
                                                              resource involved!
      lock = UNLOCKED;
   } while (true);
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
   if (*value == expected)
      *value = newValue;
   return temp;
void Foo() {
   do {
      val = *addr;
   while (compare and swap(&addr, val, val + 5) != 0)
}
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
   if (*value == expected)
      *value = newValue;
  return temp;
void Foo() {
   do {
     val = *addr;
   while (compare and swap(&addr, val, val + 5) != 0)
```

We just check if someone has modified "addr" before we make the atomic addition

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
                               There is no shared
   if (*value == expect
                               resource, no other
      *value = newValu
                               thread can block us
                                                                We just check if
   return temp;
                                   anymore!
                                                                someone has
                                                                modified "addr"
                                                                before we make the
void Foo() {
                                                                atomic addition
   do {
      val = *addr;
   while (compare and swap(&addr, val, val + 5) != 0)
```

```
int compare and swap(int *value, int expected, int newValue) {
   int temp = *value;
                                There is no shared
   if (*value == expect
      *value = newValu
                                resource, no
                                                  In-Memory OLTP installs
                               thread can
                                                    page changes in the
   return temp;
                                    anymo
                                                 mapping table of the Bw-
                                                  Tree with this technique
                                                                              ke the
void Foo() {
                                                                         adition
   do {
      val = *addr;
   while (compare and swap(&addr, val, val + 5) != 0)
```

Summary

- Latches
- Spinlocks
- Lock Free Data Structures

SQL Server Query Tuning Workshop

- Date & Location
 - October 20 23 in London
- Agenda
 - How to write high performance T-SQL queries
 - Logical & physical query processing
 - Execution Plan Troubleshooting
 - Applying Indexing Strategies
 - Using In-Memory Technologies
- Further information
 - http://www.SQLpassion.at/academy