

Article

# Decawave UWB Clock Drift Correction and Power Self-Calibration

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**Abstract:** The position accuracy based on Decawave Ultra-Wideband (UWB) is affected mainly by three factors: hardware delays, clock drift, and signal power. This article discusses the last two factors. The general approach to clock drift correction uses the phase-locked loop (PLL) integrator, which we show is subject to signal power variations, and therefore, is less suitable for clock drift correction. The general approach to the estimation of signal power correction curves requires additional measurement equipment. This article presents a new method for obtaining the curve without additional hardware and clock drift correction without the PLL integrator. Both correction methods were fused together to improve two-way ranging (TWR).

**Keywords:** ultra-wideband (UWB); time of arrival (TOA); navigation

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## 1. Introduction

In the last century, autonomous systems became omnipresent in almost every field of the industry. One of the most important tasks in robotics is the interaction between a robot and its environment. This task can only be accomplished if the location of the robot with respect to its environment is known. Visual sensors are very common for localization [1,2]. In some cases, estimating the position in non-line-of-sight conditions is required. Radio-frequency-based (RF) sensors are able to operate in such conditions, but the outcome depends highly on measurement principles, such as received signal strength indicator (RSSI) [3], fingerprinting [4], FMCW [5] and UWB [6], as well as on techniques such as the angle of arrival [7], time of arrival [8] or time difference of arrival [9]. Indoor positioning is, in general, a challenge for RF-based localization systems. Reflections could cause interference with the main signal. In contrast to narrowband signals are ultra-wideband (UWB) signals, which are more robust to fading [10,11]. A common UWB system is the Decawave UWB transceiver [12], which is low cost and provides centimeter precision. The accuracy and precision of this chip are affected by three factors: hardware delays, clock drift, and signal power [13,14]. This article discusses clock drift correction and signal power error, which is specific to the Decawave UWB transceiver and affects the accuracy of the position significantly. The general approach to estimating signal power dependency is to use ground truth data, which are provided by additional measurement equipment [15]. The clock drift error is caused by the different frequencies of the transceiver clocks. The general approach to Decawave UWB clock drift correction is to use the integrator of the phase-locked loop (PLL) [16–19]. In the following section, we explain that the general approach to clock drift correction is not suitable because the PLL is also affected by the signal power. Therefore, a more accurate method for clock drift correction is presented. The middle sections of this article discuss the estimation of the signal power

correction curve without the need for additional hardware. As far as we know, nobody has obtained a signal power correction curve by self-calibration before. The last part of this article presents a two-way ranging (TWR) method that is able to use the correction methods for distance estimation.

## 2. Decawave UWB

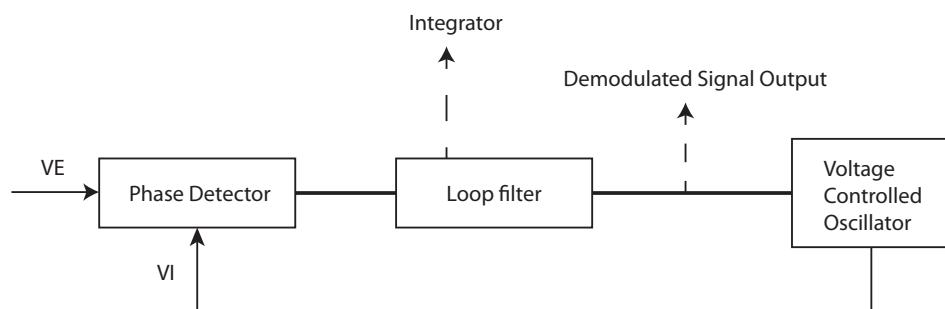
Decawave transceivers are based on UWB technology and are compliant with IEEE802.15.4-2011 standards [20]. They support six frequency bands with center frequencies from 3.5 GHz to 6.5 GHz and data rates of up to 6.8 Mb/s. The bandwidth varies with the selected center frequencies from 500 up to 1000 MHz. With higher bandwidth, the send impulse becomes shorter. The timestamps for the positioning are provided by an estimation of the channel impulse response, which is obtained by correlating a known preamble sequence against the received signal and accumulating the result over a period of time. In contrast to narrowband signals, UWB is more resistant to multipath fading. Reflections would cause an additional peak in the impulse response. The probability that two peaks interfere with each other is small. The sampling of the signal is performed by an internal 64 GHz chip with 15 ps event-timing precision (4.496 mm). Because of general regulations, the transmit power density is limited to  $-41.3 \text{ dBm/MHz}$ . These regulations are due to the high bandwidth occupied by the UWB transceiver. The following experiments were carried out with the Decawave EVK1000. This board mainly consists of a DW1000 chip and an STM32 ARM processor.

## 3. Clock Drift Correction

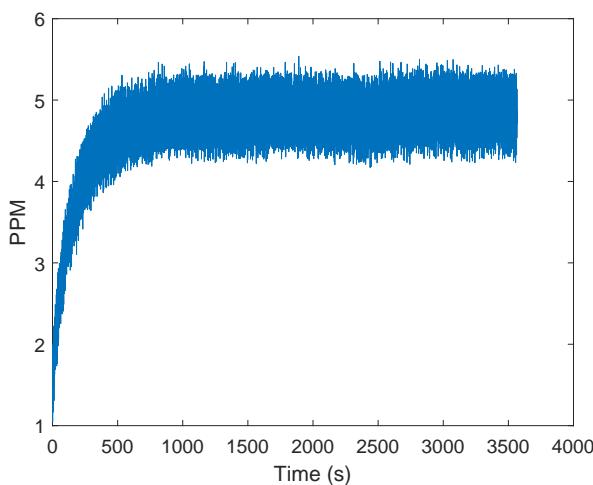
In practice, it is not possible to manufacture exactly the same clock generators, so every transceiver has a different clock frequency. Clock drift correction represents the difference between clock frequencies but not current time values.

### 3.1. General Approach

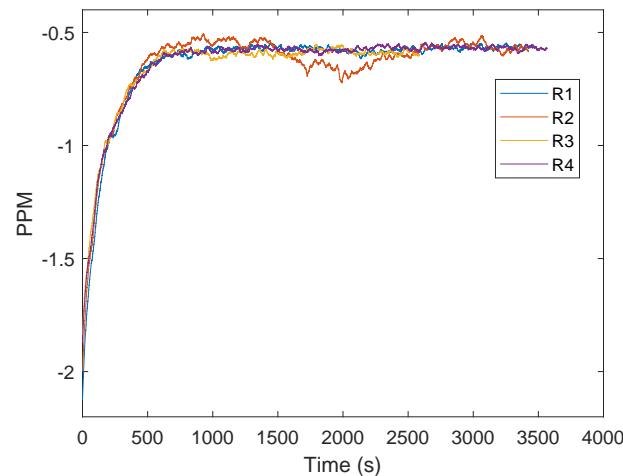
The general approach to clock drift correction is to use the PLL integrator [16–19]. Figure 1 shows an example of frequency demodulation by a PLL. The voltage-controlled oscillator (VCO) is set to the mid-position and the loop is locked in at the frequency of the carrier wave. Modulations on the carrier would cause the VCO frequency to follow the incoming signal, so changes in the voltage correspond to the applied modulations. The difference between the received carrier frequency (VE) and the internal loop frequency (VI) can be observed in the integrator of the loop filter. In Figure 2, the integrator output is presented. The test scenario is based on measurements obtained at every 50 ms between two stationary transceivers. The difference between the two frequencies is about five parts per million. Reaching the final condition took up to 15 min. The tests were repeated four times with another two stationary stations. Figure 3 shows the filtered results of the obtained curves provided by a 500-point moving average filter. The curve progression is deterministic. Decawave indicates that the logarithmic increase of the integrator at the beginning is due to the warm-up when the crystal oscillator is activated, graphically represented in Figure 4. This oscillator follows from the combination of a quartz crystal and the circuitry within the DW1000-based design.



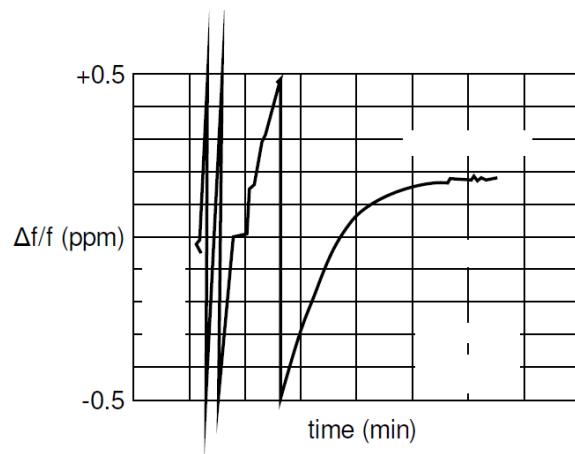
**Figure 1.** Example of the phase locked loop (PLL).



**Figure 2.** Frequency difference between the received carrier and the internal phase locked loop (PLL) in parts per million (PPM). The curve is obtained by reading the carrier integrator value of the DW1000 chips. The logarithmic increase of the curve is due to the warm-up of the crystal oscillator.

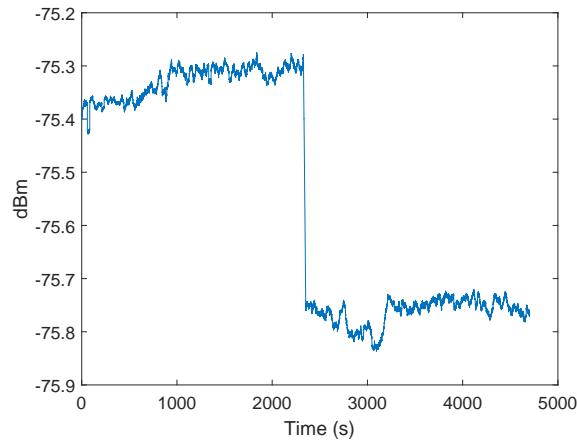


**Figure 3.** Filtered frequency difference between the received carrier and the internal phase locked loop (PLL) in parts per million (PPM). The colors represent different measurement obtained successively. It can be seen that the curves are deterministic.

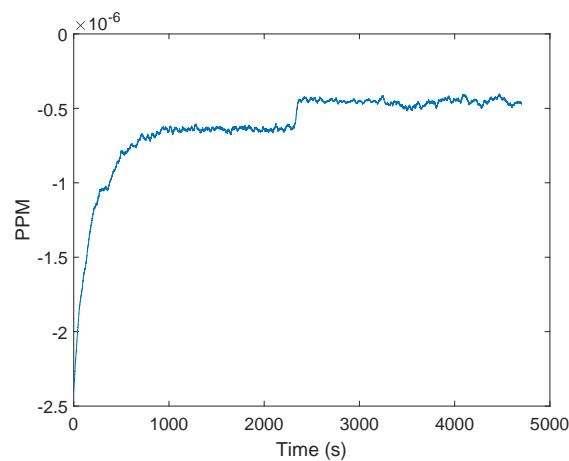


**Figure 4.** Frequency difference during the warm-up phase of the DW1000 crystal oscillator in parts per million (PPM) with respect to the time in minutes [15]. The figure is used with permission [15].

In the following test scenario, the effect of the signal power on the integrator was investigated. Both the transmitter and receiver stations were stationary. Figure 5 shows the measured signal strength at the receiving station. After about 2340 s, we arranged the transmitter to reduce the signal power. The integrator of the receiver jumped after the signal power changed to a new level (Figure 6), indicating that distance changes between the transmitter and receiver would affect the integrator, and so, affect the clock drift correction as well.



**Figure 5.** Signal power in dBm of the received blink message. The curve shows the filtered results of the received signal power over time, measured by the DW1000 chip. After 4600 measurements the transmitting signal power was reduced.



**Figure 6.** Filtered frequency differences between the received carrier and the internal phase locked loop (PLL) in parts per million (PPM) after changing the transmitting signal power after about 2340 s.

The reason for this dependency could be the analog phase detectors of the PLL, in which the loop gain  $K_D$  is a function of amplitude, which affects the error signal  $v_e(t) = K_D[\Phi_{Out}(t) - \Phi_{In}(t)]$ , and so, affects the pull-in time (total time taken by the PLL to lock) as well.

### 3.2. Proposed Approach for the Clock Drift Correction

In this section, we present an alternative method for the clock drift correction, which is independent of the signal power. The measurement setup is presented in Figure 7. All measurements and calibrations were conducted with Decawave EVK1000 boards. The station with the identification number (id) 2 is the transmitting station (TX). The receiving station (RX) has the identification number 1. The receiving signal power, as well as the timestamps, were obtained by reading the register provided

by the transceivers [14,15]. The general settings for the hardware setup can be found in Table 1 and the notations in Table 2.



**Figure 7.** The measurement setup consist of two transceivers (EVK100) with the identification number 1 and 2.

**Table 1.** Test settings.

Parameter	Value
Center Frequency	3993.6 MHz
Bandwidth	499.2 MHz
Pulse repetition frequency	64 MHz
Preamble length	128
Data rate	6.81 Mbps

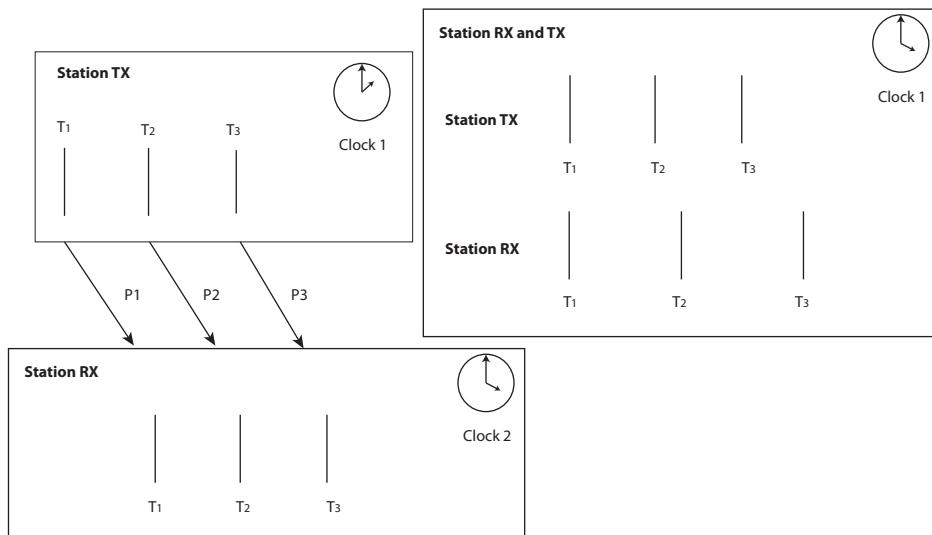
**Table 2.** Notations used.

Notations	Definition
$T_i$	Timestamp
$\Delta T_{n,m}$	Clock drift with respect to the timestamps n and m
$E_i$	Timestamp error due to signal power
Z	Hardware delay and signal power correction offset

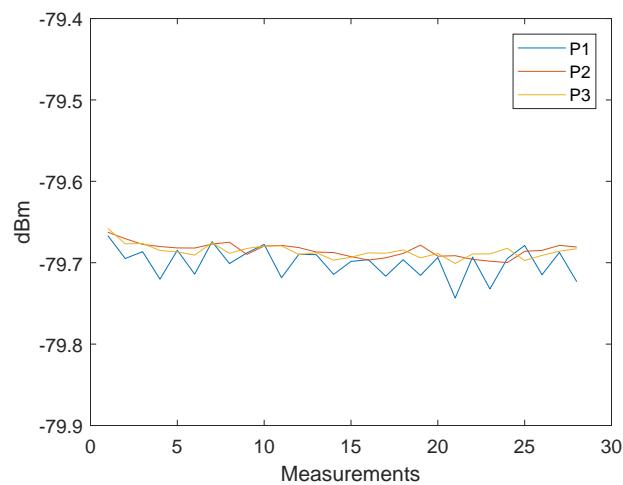
Figure 8 shows a schematic diagram of the approach. TX is sending three signals at times  $T_1$ ,  $T_2$ , and  $T_3$ . The clocks of the transmitter and receiver are not synchronous. If the clocks have no drift, then both clocks should have the same frequency and the difference between  $\Delta T_{1,2} = T_2 - T_1$  should be the same for the transmitter and the receiver; otherwise,  $\Delta T_{1,2}^{RX} \neq \Delta T_{1,2}^{TX}$ . The same applies to  $\Delta T_{1,3}$ . If the clock of RX is running faster than that of TX, then  $\Delta T_{1,3}^{RX} > \Delta T_{1,3}^{TX}$  and the clock drift error becomes  $C_{1,2} = \Delta T_{1,2}^{RX} - \Delta T_{1,2}^{TX}$ .

Previously, the frequency difference between the two clocks was presented by the integrator of the PLL. After the warm-up time, the clocks reached their final frequencies. The clock error now increased linearly. For short measurement periods the clock drift error can be assumed to be linear even during the oscillator's warm-up.

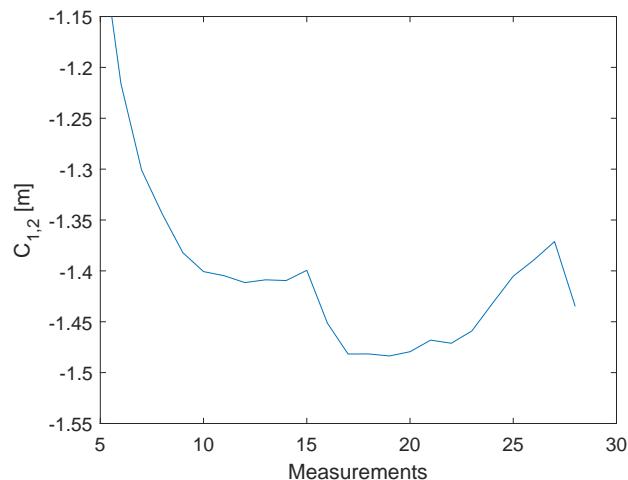
The main idea is for the clock drift error  $C_{1,3} = \Delta T_{1,3}^{RX} - \Delta T_{1,3}^{TX}$  to be used for correcting the timestamp  $T_2$  by simple linear interpolation. In Figure 9, three messages, P1, P2, and P3, with constant signal powers have been sent. The delay between every message was about 2 ms. The values are already filtered; hence, every point consists of the mean of 2000 measurements. The Figure 10 shows the clock drift error  $C_{1,2} = \Delta T_{1,2}^{RX} - \Delta T_{1,2}^{TX}$ . Because of the long delay, the distance error resulting from the clock drift is about 1 m.



**Figure 8.** Schematic for the presented clock drift correction based on three transmitting messages. See text for explanation.



**Figure 9.** Filtered received signal power measurements of the three messages: P1, P2 and P3.

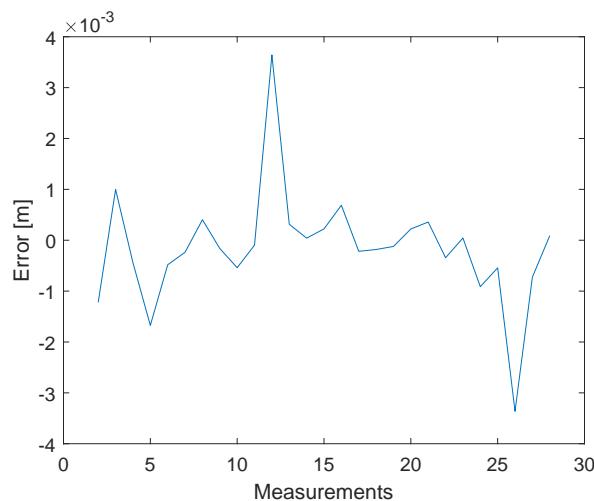


**Figure 10.** The error in meters caused by the clock drift. The curve is changing, due to the clock warm-up.

In the next step, the clock drift error  $C_{1,2}$  is corrected by the linear interpolation of  $C_{1,3}$ .

$$C'_{1,2} = C_{1,2} - \frac{C_{1,3}}{\Delta T_{1,3}^{TX}} \cdot \Delta T_{1,2}^{TX} \quad (1)$$

The results are shown in Figure 11. The correction requires only three messages and the remaining average offset is about  $-1.915 \cdot 10^{-5}$  m. The linear interpolation is also suitable for the warm-up phase. The implementation of the presented clock drift correction for the TWR is presented in the last section. A position error caused by a constant velocity of the object is also corrected by the linear interpolation, due to the linear increase of the position error (pseudo clock drift). In practice, it is possible to obtain  $\Delta T_{1,3}^{TX} = 1$  ms. An acceleration high enough to cause an error greater than 5 mm would require almost  $1000 \text{ g} \left(10^4 \frac{\text{m}}{\text{s}^2}\right)$ .



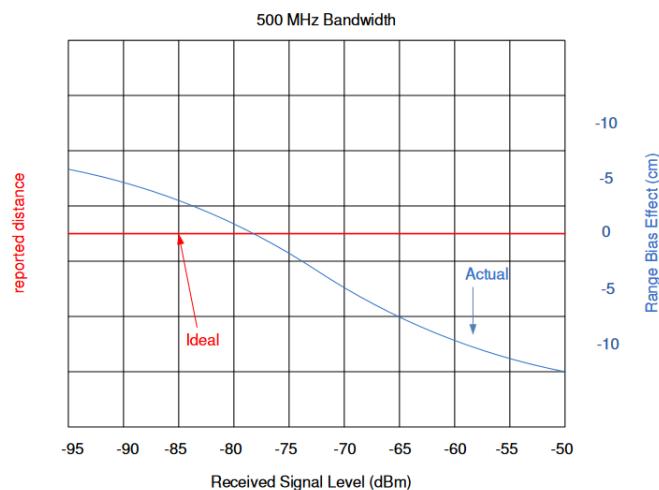
**Figure 11.** The error in meters after the clock drift correction.  $C'_{1,2}$ .

#### 4. Signal Power Correction

The next section discusses the signal power correction. It is known that the time stamp of the DW1000 is affected by the signal power, in which an increase causes a negative shift of the time stamp and vice versa.

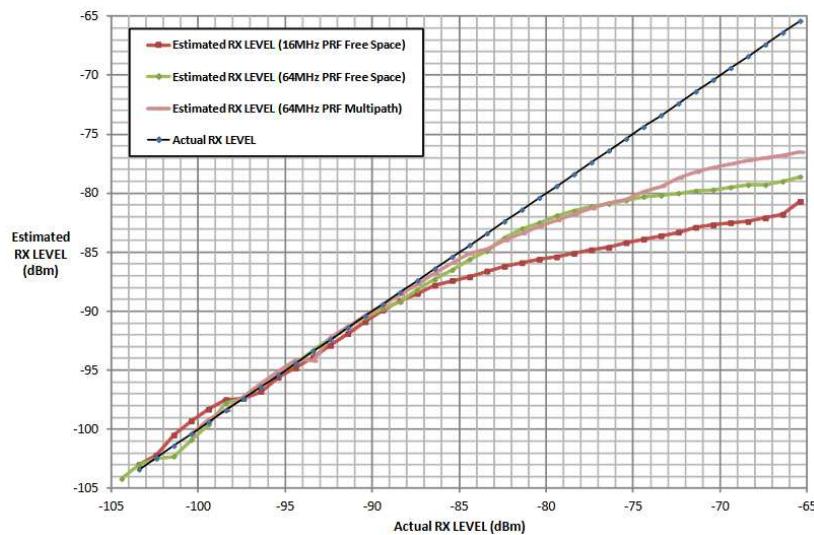
##### 4.1. General Approach

Figure 12 illustrates the reported distance error with respect to the received signal power. At a certain signal strength, the range bias effect should be zero. In Figure 12 the bias vanishes between  $-80$  and  $-75$  dBm. The correction curve is affected by the system design elements, such as printed circuit boards, antenna gain, and pulse repetition frequency (PRF). The general approach to correction curve estimation is to compare the distance measurements with the ground truth distances. This method has two disadvantages. First of all, additional measurement equipment is necessary. Second, every created curve applies to two stations but not every individual station.



**Figure 12.** The effect of the received signal power on the distance measurement [15], used with permission. The red line represents the correct distance measurements. The blue line shows the range bias caused by different signal powers [15].

Figure 13 shows the relationship between the measured and correct signal strengths for different PRF. The measured signal power is correct only for measurements smaller than  $-85$  dBm. The knowledge of the difference between the measured and correct signal strengths can be used for additional measurement techniques, such as the RSSI, for distance estimation.

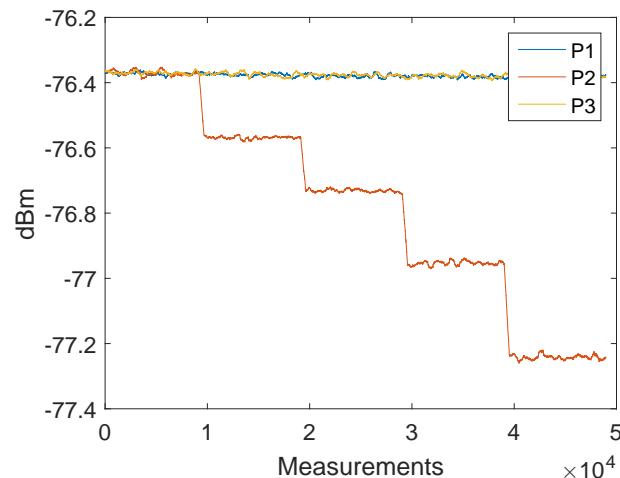


**Figure 13.** Measured received signal power at the DW1000 chip, with respect to the correct signal power. The blue line represent the reference line, every value of the x-axis has the same value on the y-axis. The other lines are the results of the estimated received signal power with different settings. The estimated received signal power equates the correct signal power for low signal strength [14].

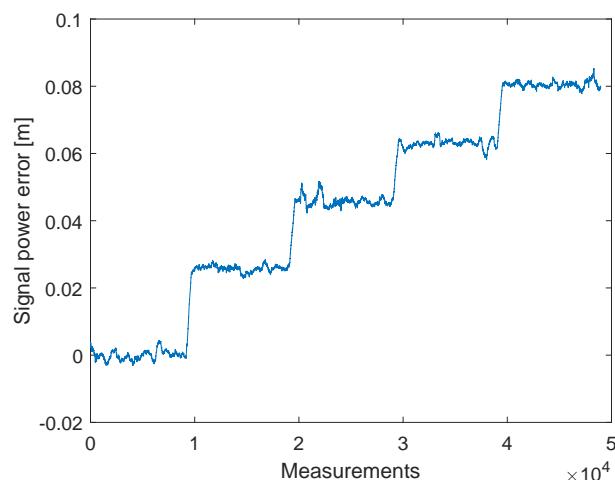
#### 4.2. Proposed Approach for the Signal Power Correction

In the previous section, we discussed an alternative approach to clock drift correction with three messages (P1, P2, and P3). The following method is based on this concept, but the TX station changes the signal strength of the second message (P2). Figure 14 shows how the signal strengths of the first and last messages (P1 and P3) remain constant and only the signal strength of the second signal (P2) decreases after 1000 measurements. Every measurement point is the result of the mean of 2000 signals. The tests were conducted with a cable connection of 10 cm and the transmitter decreased the signal gain with a step size of 3 dB. The transmit power settings can be adjusted by changing the gain of the

transmit Driver Amplifier (DA) and the transmit Mixer. These changes are not equivalent to the output power. Figure 11 shows that, after the clock drift correction, the remaining error of  $C'_{1,2}$  (1) is close to zero. With the decreasing signal strength of P2, the error of  $C_{1,2}$  is increasing, see Figure 15. Hence, it is possible to create a dependency between the measured signal strength and the timestamp error.

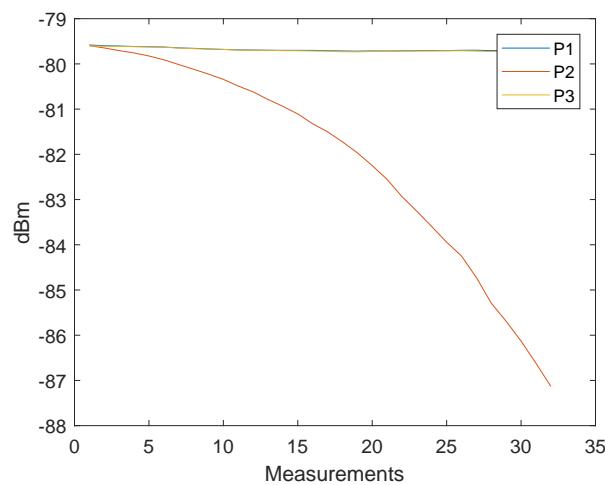


**Figure 14.** Filtered received signal power measurements of the three messages P1, P2 and P3. The measurements have been obtained with a cable connection between the transceivers. The transmitted signal power of the second signal P2 is reduced with a step size of 3 dB, while the signal power for P1 and P3 remains constant.

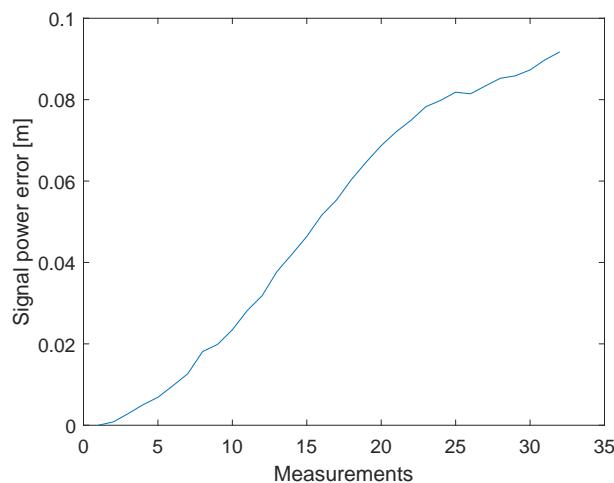


**Figure 15.** Filtered timestamp error changes due to the received signal power. The measurements have been obtained with a cable connection between the transceivers. The error is changing systematically with decreasing signal power.

In the following test scenario, the power calibration was repeated with an antenna and a distance of 1.5 m between the RX and TX stations. The gain step size was reduced to 0.5 dB. Figures 16 and 17 shows the results of the filtered signal power calibration curve. The main difference between Decawave's curve, as shown in Figure 12, and our curve is that the zero line is unknown. This line marks the signal power at which the timestamp error is zero. The step size of the decreasing transmitting signal power gain was constant, but the measured decreasing signal power curve for P2 was nonlinear because the measured signal power did not equate to the correct signal power for high signal strength, as shown in Figure 12.



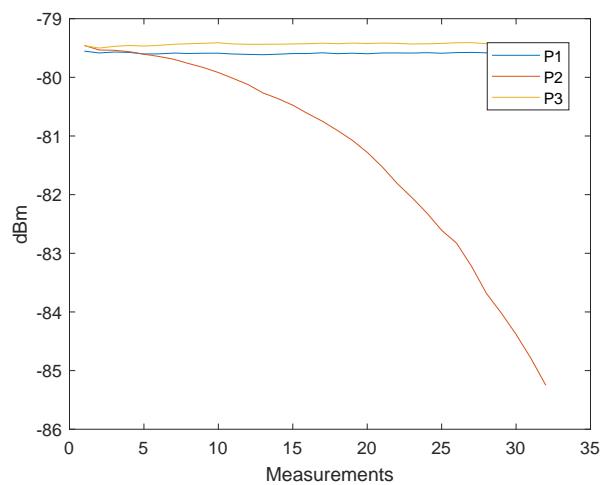
**Figure 16.** Filtered received signal power measurements of the three messages P1, P2 and P3. The measurements have been obtained with a wireless connection between the transceivers. The transmitted signal power of the second signal P2 is reduced with a step size of 0.5 dB, while the signal power for P1 and P3 remains constant.



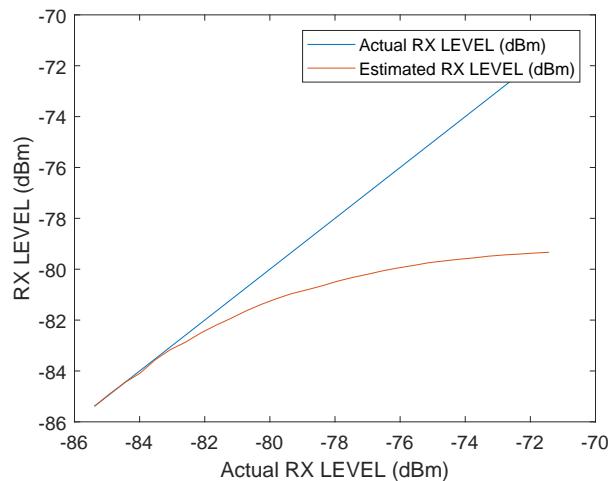
**Figure 17.** Filtered timestamp error changes due to the received signal power. The measurements have been obtained with a wireless connection between the transceivers. The error is changing with decreasing signal power.

It is necessary to pay attention to the timing between the messages. With short delays between the messages, it is possible that they affect each other. This effect can be seen by the offset between P1 and P3 in Figure 18. In Figure 16 a delay of 2 ms was used between the messages and in Figure 18 a delay of 150  $\mu$ s was used.

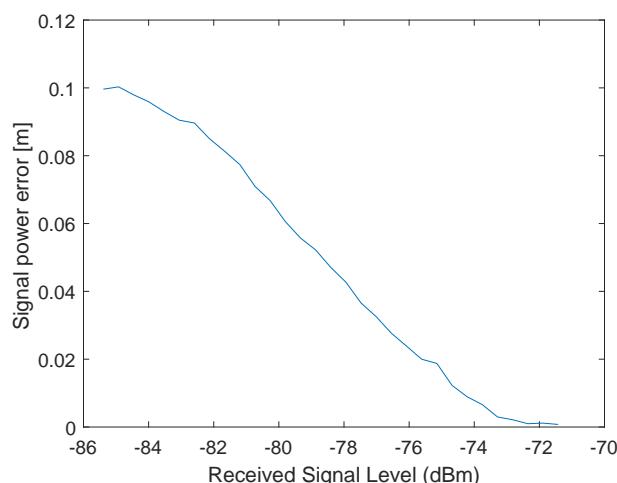
It was previously mentioned that the measured signal strength equals the correct signal power only for small signal powers. Therefore, it is possible to use the very first measurements with small signal strengths to estimate the slope. Figure 19 shows an estimated line based on the estimated slope. The results are the same as the curve obtained by Decawave except that no additional measurement equipment is required and our curves can be obtained individually for every station. Figure 20 illustrates the correction curve with respect to the signal power.



**Figure 18.** Interference between the received messages P1, P2 and P3. The transmitted signal power of the message P1 and P3 are the same. Due to the short update time are the signals interfering.

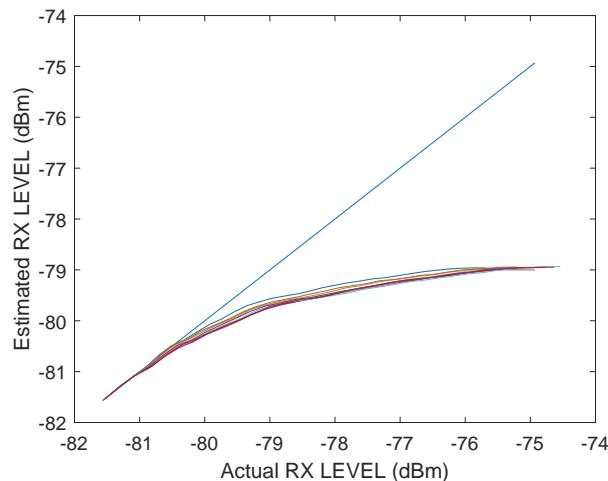


**Figure 19.** Estimated correction curve between the measured received signal power and the ideal signal power curve.

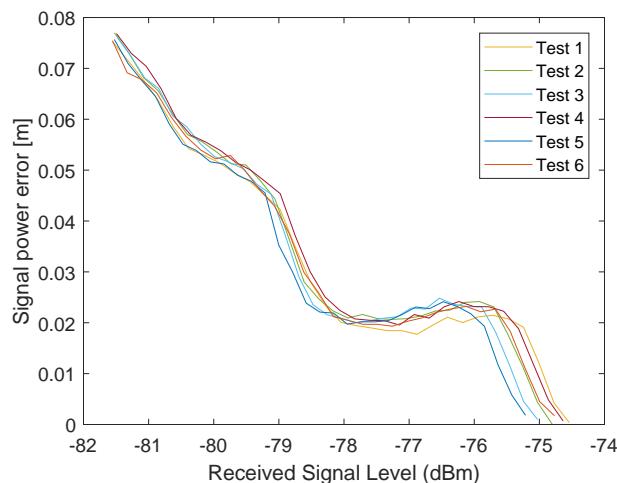


**Figure 20.** Correction value of the error caused by the signal power as a function of the received signal power.

Even for the same hardware design, it is possible that the shape of the correction curve differs. In Figures 21 and 22, the final results of the power correction curve are obtained from another station. The calibration was repeated six times. The shapes of the curves are deterministic but different from those of the station above. Therefore, it makes sense to repeat the calibration for every individual station.



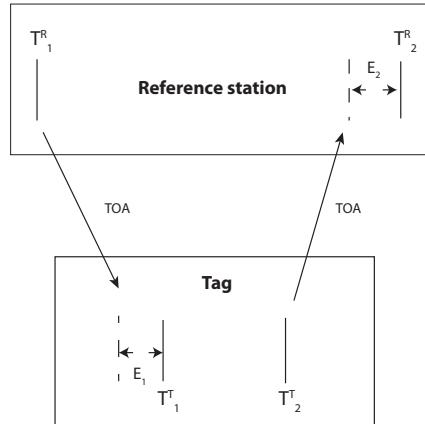
**Figure 21.** Power correction curve for the received signal power.



**Figure 22.** Correction value of the error caused by the signal power as a function of the received signal power for a different station with six restarts.

## 5. Two Way Ranging

The following section describes how the presented clock drift and signal power correction can be used for precise TWR. Figure 23 shows the concept for the TWR. The initial message is sent by the reference station at  $T_1^R$  and received by the tag. The timestamp  $T_2^T$  is affected by the signal power and causes an error E1. After some delay caused by internal processing, the tag sends a response message at  $T_2^T$ . The reference station receives the response from the tag and saves the timestamp  $T_2^R$ , which is affected by the signal power error E2. In this example, the delay due to the hardware offset is not considered.



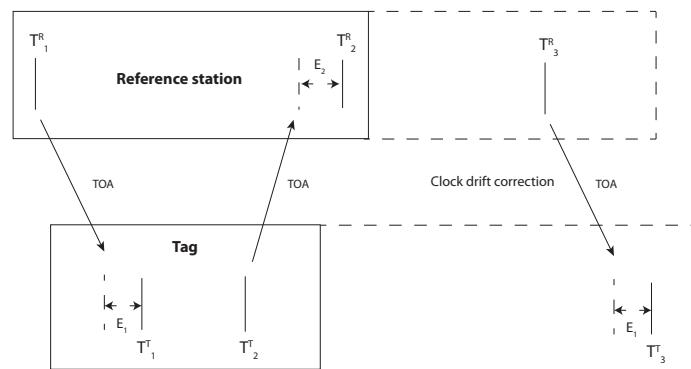
**Figure 23.** Schematic for the signal power correction for two-way ranging.

The time of flight between the reference station and the tag can be determined by the following formula. It is assumed that the distance between the two devices does not change between time stamp  $T_2^R$  and  $T_1^R$ .

$$T_{TOA} = \frac{(T_2^R - T_1^R) - (T_2^T - T_1^T) - E_2 - E_1}{2} \quad (2)$$

The values  $E1$  and  $E2$  can be obtained from the signal power correction curve. It should be taken into account that the signal power affects the tag and reference station differently. The time difference  $\Delta T_{1,2}^R$  increases with decreasing signal power. The zero lines for both the signal power and hardware offset are unknown but constant; hence, both values are represented by the variable  $Z$ . In the previous section, we explained that the clock drift could be corrected by three messages. Figure 24 shows how this principle can be adapted for TWR. The last message was used to obtain the clock drift error  $C_{1,3} = \Delta T_{1,3}^R - \Delta T_{1,3}^T$ . The signal power  $E1$  had no effect on the time stamp difference  $\Delta T_{1,3}^T$ . The final time of the flight equation with the clock drift correction becomes:

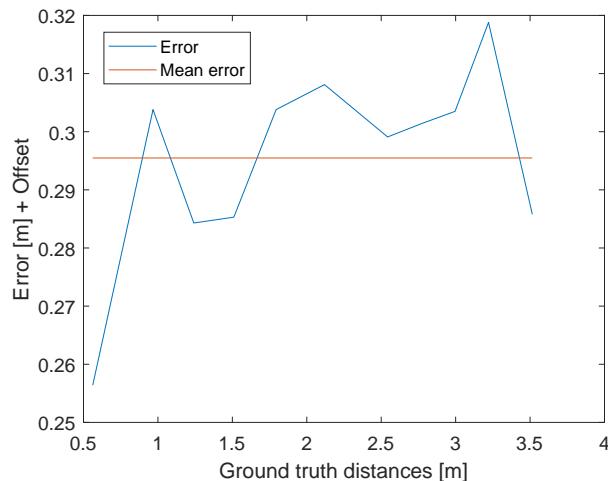
$$T_{TOA} = 0.5 \cdot \left( \Delta T_{1,2}^R - \Delta T_{1,2}^T - \frac{C_{1,3}^{RT}}{\Delta T_{1,3}^T} \cdot (\Delta T_{1,2}^T + E1) \right) + 0.5 \cdot (-E_2 - E_1) + Z$$



**Figure 24.** Two-way ranging with clock drift correction between reference station and tag.

The results of the TWR with signal power and clock drift correction are illustrated in Figure 25. The blue line represents the difference between laser distance measurements (ground truth) and distances provided by the TWR. The 11 distances extend from 3.515 m to 0.562 m. Every point results from the mean of 2000 measurements. The unknown hardware offset, which causes the 0.3 m offset, is not relevant in this example. The signal power error depends on the distance and the clock drift on time. If both effects are corrected properly, the resulting difference between the mean

error and every measurement error should be as small as possible. The standard deviation of the error is 0.015 m. The small error difference between the blue and red line shows that the signal power and clock drift correction are both sufficient. The antenna area was  $0.0012 \text{ m}^2$ ; therefore, it is not possible to obtain ground truth data with a precision higher than a few centimeters.



**Figure 25.** Difference between the measured distances obtained by two-way ranging and the ground truth distances in meter. The constant offset caused by the hardware delay is not compensated but is represented by the red line. The near most constant errors for different distances shows the power correction is correct.

## 6. Conclusions

This article presents a new method for signal power and clock drift correction. It was shown that the curves obtained for the signal power correction could be highly accurate and deterministic, as well as provide individual results for every station. The signal power correction procedure can be performed once as a factory calibration. In addition to the estimation of the signal power correction curve, it was also possible to obtain the relationship between the measured and real signal powers. Knowing the relationship allows for better distance estimations with methods based on the signal strength. In contrast to the general approach, our clock drift correction is independent of the signal power and promises results with centimeter accuracy. The last part of the article explained how the signal power and clock drift correction are fused together to provide highly accurate TWR.

## 7. Patents

Patent pending.

**Author Contributions:** The idea for the power self-calibration algorithm as well as the clock drift correction was done by J.S.; The coauthors V.S., N.S.-N., M.A. and U.H. improved the paper through their comments and corrections about the layout, content and the obtained results. Conceptualization, methodology, software, validation, visualization, formal analysis, investigation and writing-original draft preparation was done by J.S.; Supervision, project administration was done by N.S.-N. and U.H.; Funding acquisition was done by M.A.

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**Conflicts of Interest:** The authors declare no conflict of interest.

## Abbreviations

The following abbreviations are used in this manuscript:

UWB	Ultra-wideband (UWB)
TOA	Time of Arrival (TOA)
PLL	Phase-locked loop
TWR	Two-way ranging
FMCW	Frequency modulated continuous wave
VCO	Voltage-controlled oscillator
VE	Received carrier frequency
VI	Internal loop frequency
PPM	Parts per million
TX	Transmitting station
RX	Receiving station
PRF	Pulse repetition frequency
RSSI	Received signal strength indication

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