

A

A

B

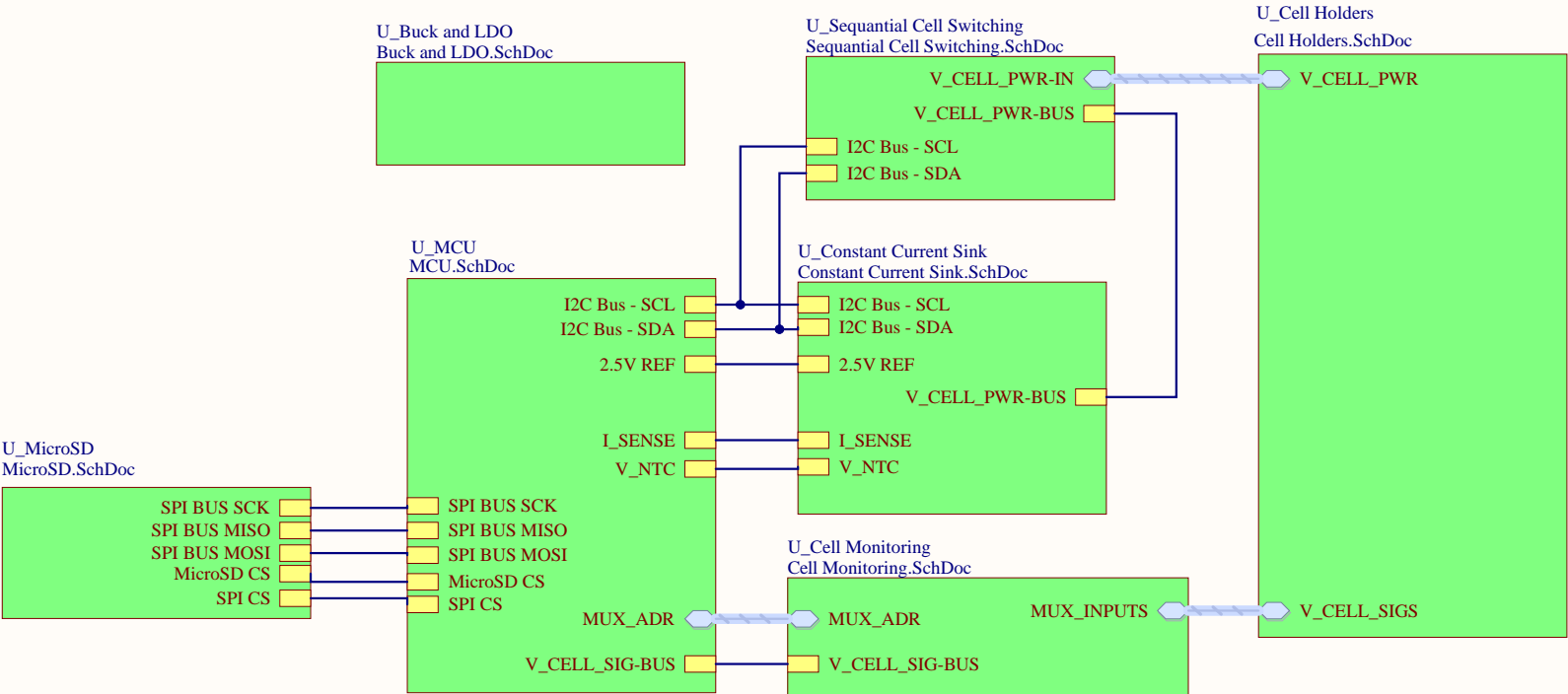
B

C

C

D

D

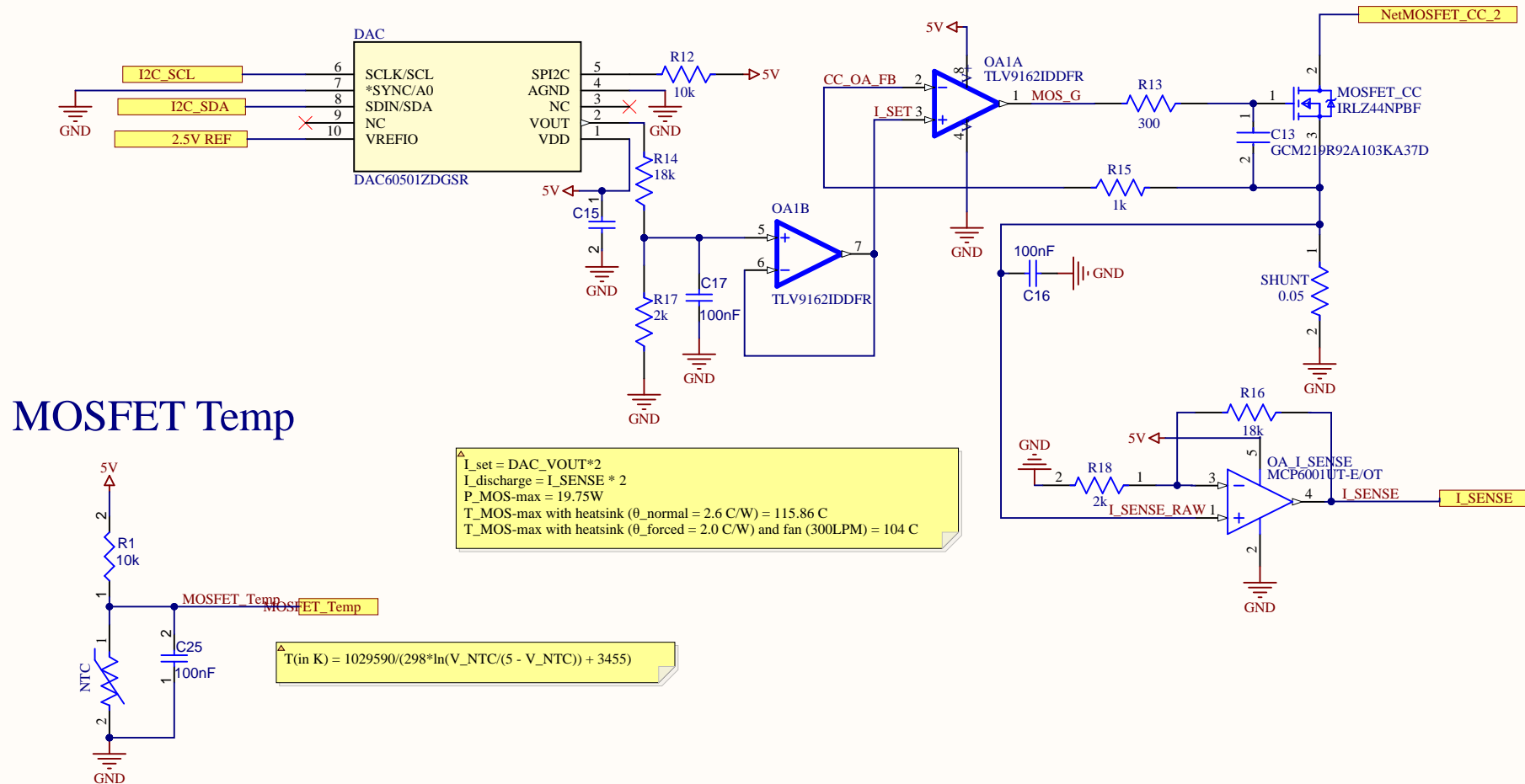


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A

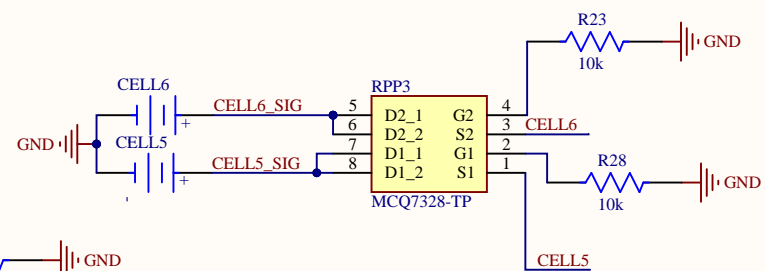
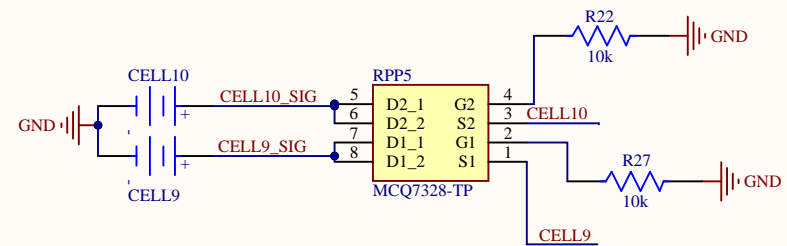
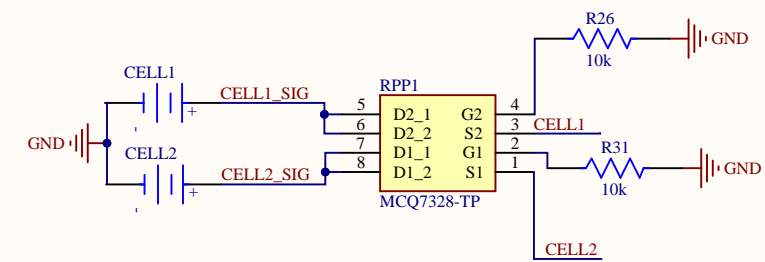
D

CONSTANT CURRENT SINK

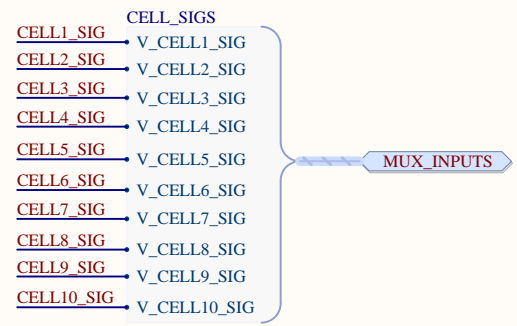
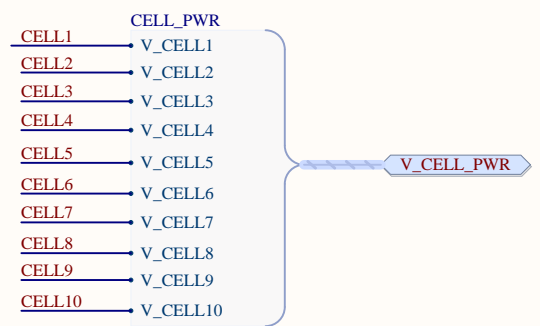
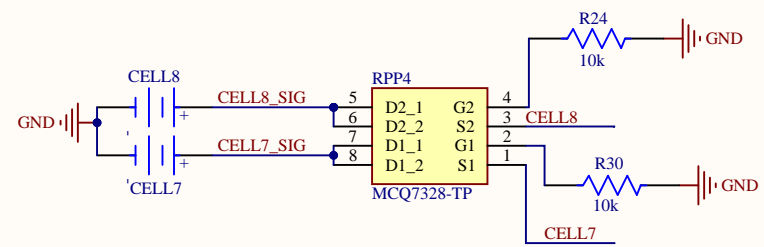
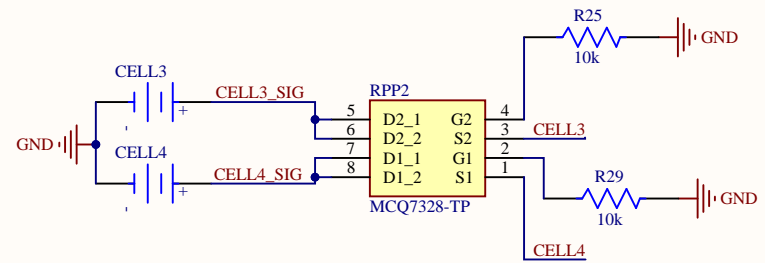


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CELL HOLDERS



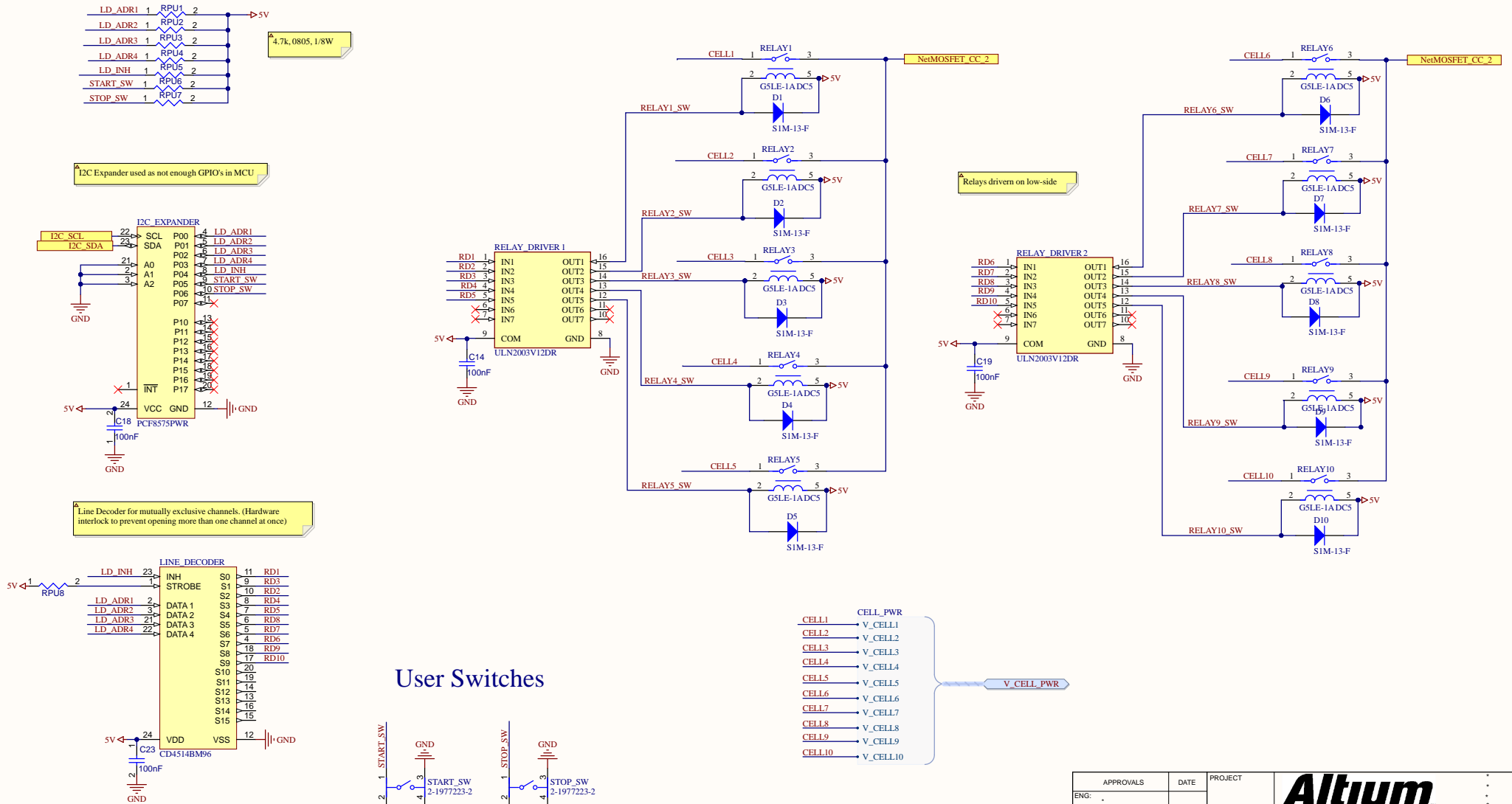
^A RPP1-5 are for reverse polarity protection. Approx voltage drop across D-S @ 5A is 140mv. SIG wires are added before RPP for kelvin sense



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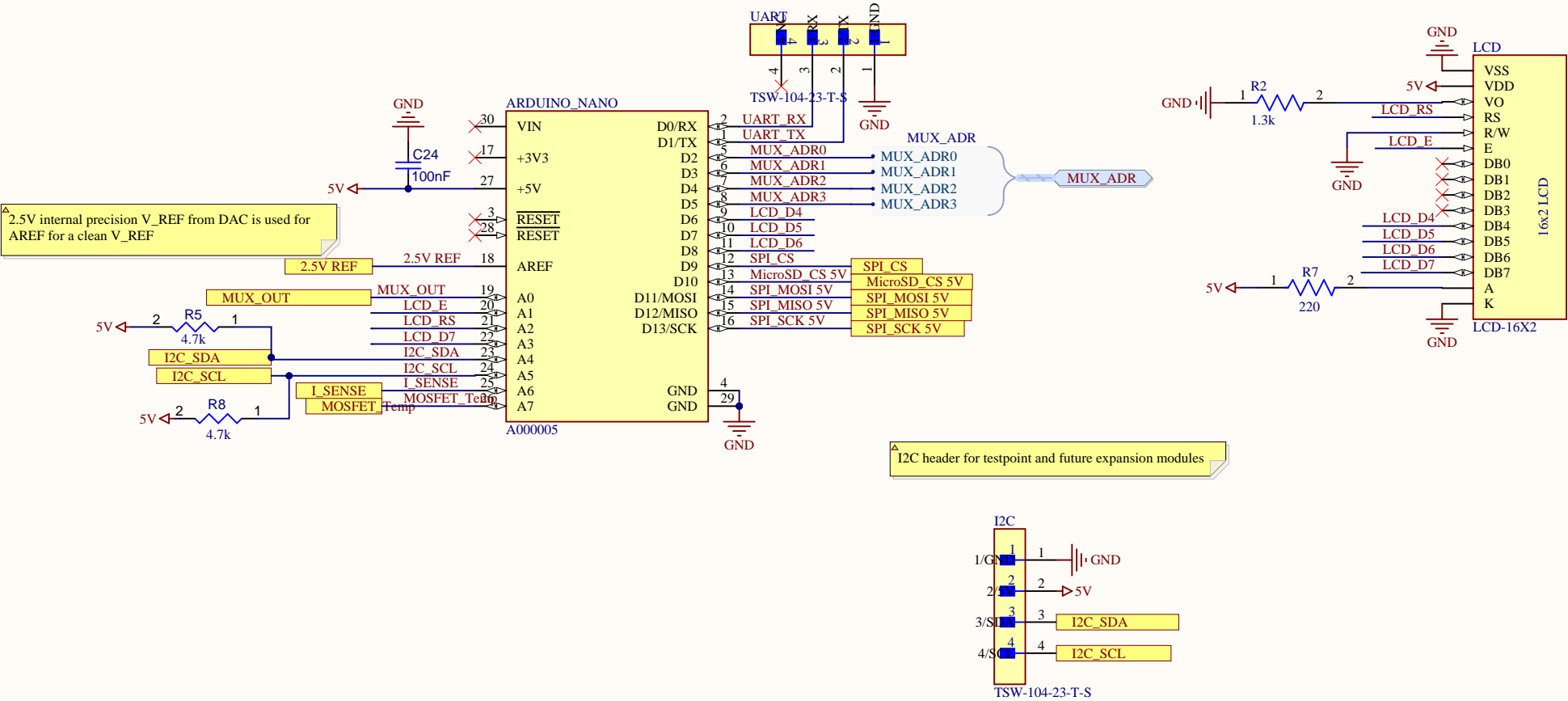
SEQUENTIAL CELL SWITCHING



APPROVALS		DATE	PROJECT		Altium	
ENG:	.		PROJECT REVISION:		DOCUMENT REVISION:	
DSN:	.		CHK:	.	DESIGN ITEM:	
REFERENCE DOCUMENTS			TITLE			
BOM:			*			
ASSY DWG:			SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:			B	?????	<SCH DWG NO>	
PCB DWG:			SCALE:	FILE NAME	Sequential Cell Switching.SchDoc	SHEET 5 OF 8

MCU (Nucleo / Nano)

LCD

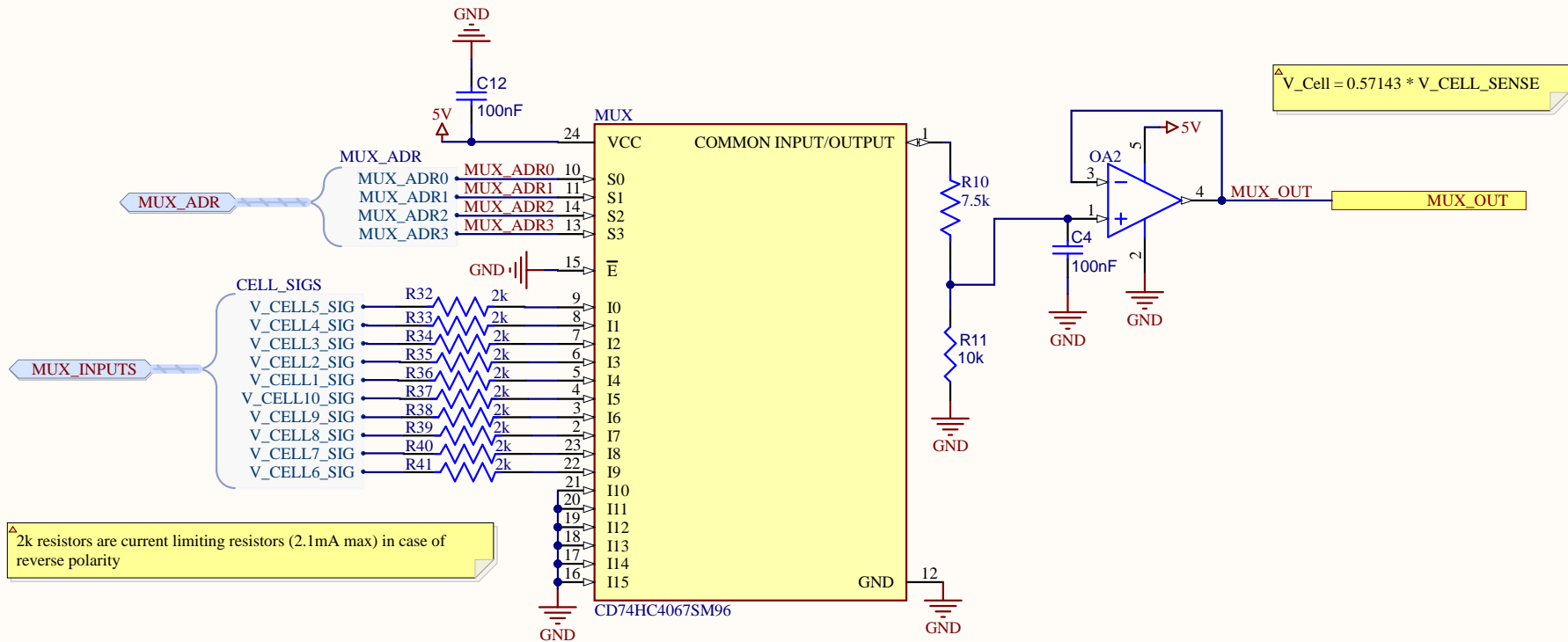


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CELL MONITORING



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