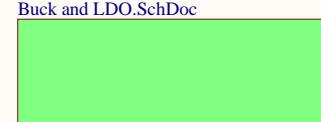


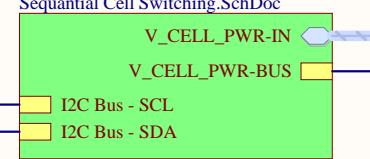
A

A

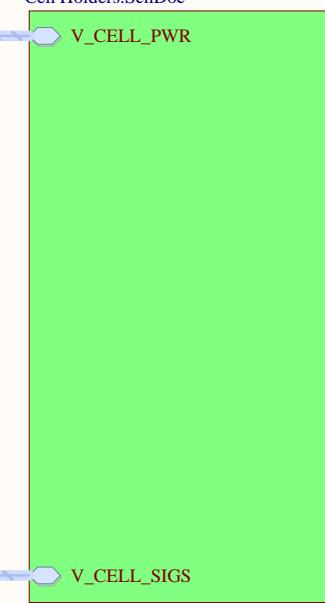
U_Buck and LDO
Buck and LDO.SchDoc



U_Sequential Cell Switching
Sequential Cell Switching.SchDoc



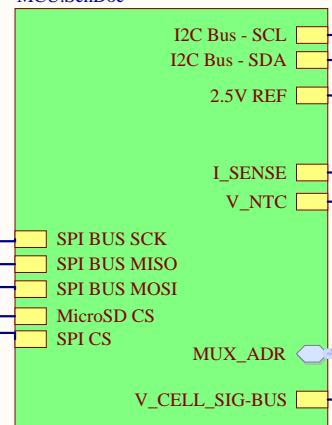
U_Cell Holders
Cell Holders.SchDoc



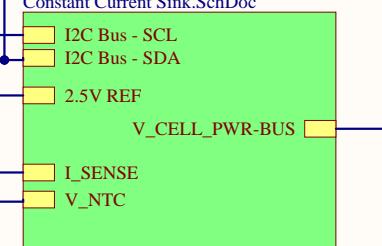
B

B

U MCU
MCU.SchDoc



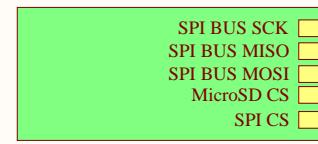
U_Constant Current Sink
Constant Current Sink.SchDoc



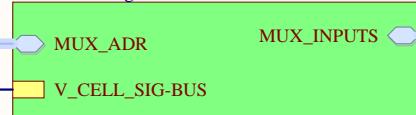
C

C

U_MicroSD
MicroSD.SchDoc



U_Cell Monitoring
Cell Monitoring.SchDoc



V_CELL_SIGS

D

D

Title

Size

Number

Revision

Date:

1-05-2026

Sheet of

File:

C:\Users\..\TopLevel.SchDoc

Drawn By:

5V Buck / 3.3V LDO

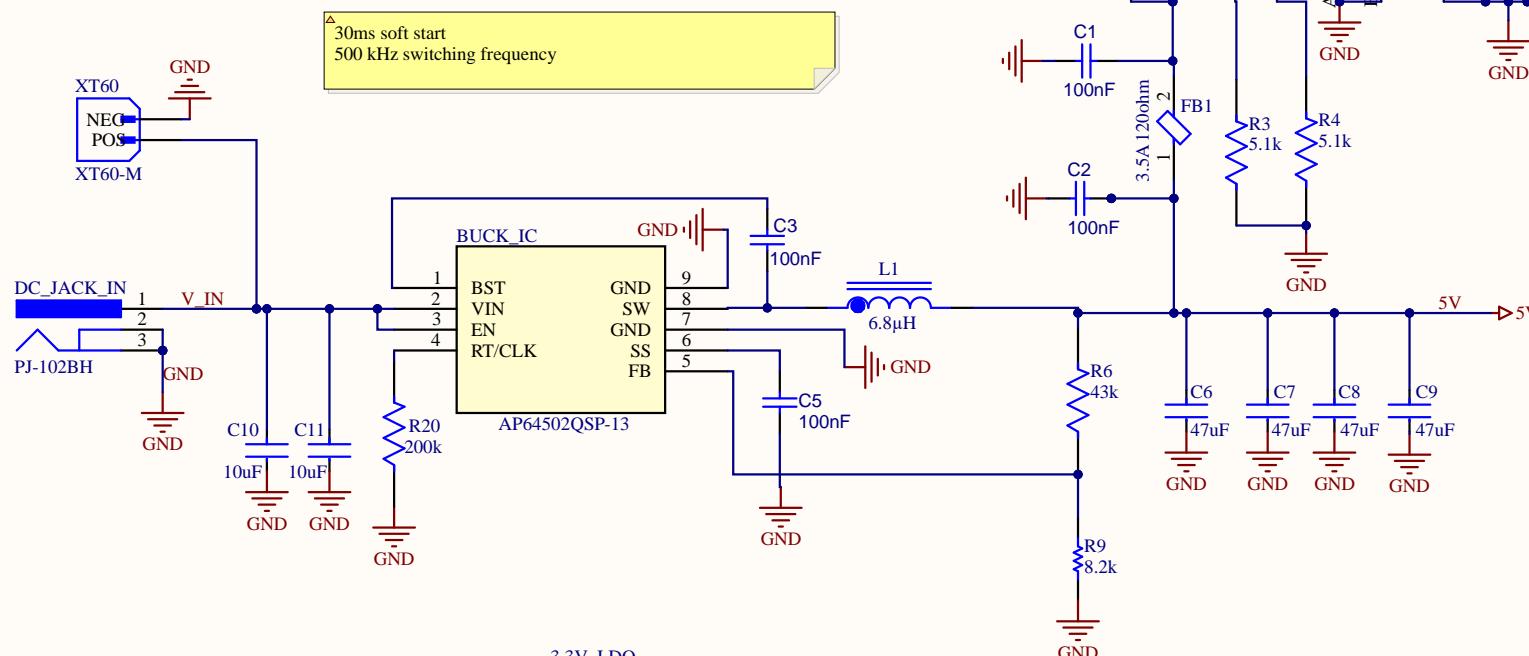
A

A

30ms soft start
500 kHz switching frequency

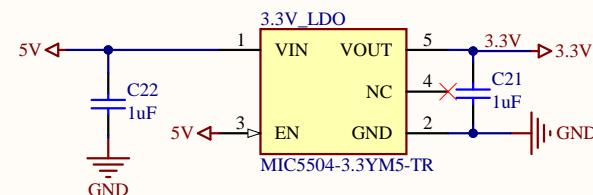
B

B



C

C



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D

Title

Size

Number

Revision

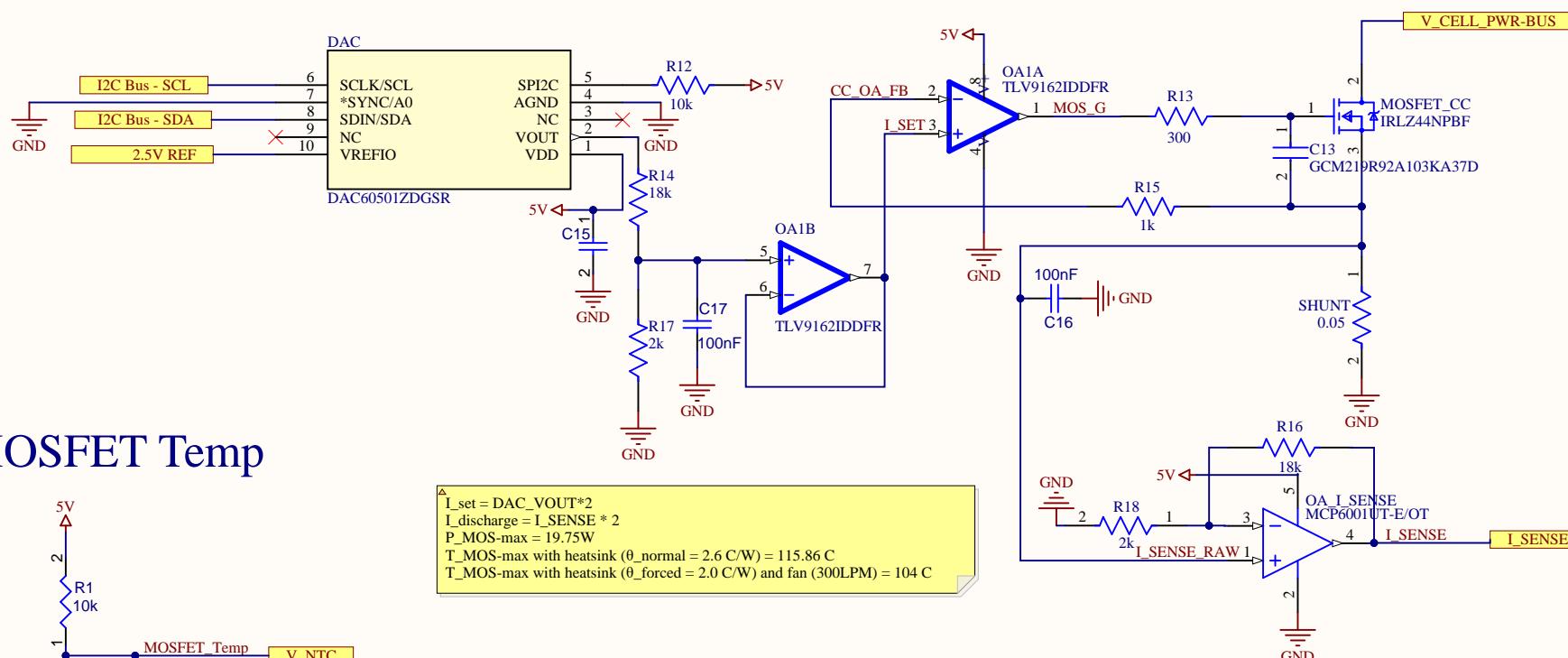
Date: 1-05-2026

Sheet of

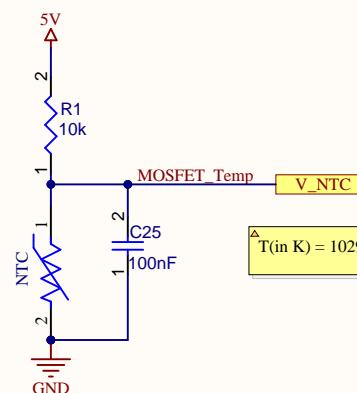
File: C:\Users\...\Buck and LDO.SchDoc

1

CONSTANT CURRENT SINK



MOSFET Temp



Title		
Size	Number	Revision
A4		
Date: 1-05-2026	Sheet of	
File: C:\Users\..\Constant Current Sink.SchDoc		Drawn By:

CELL HOLDERS

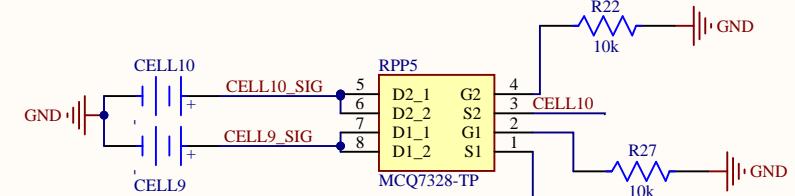
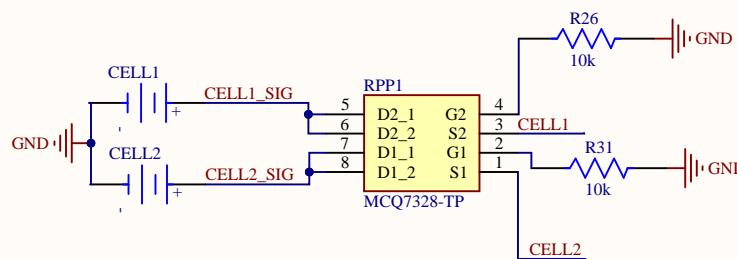
1

2

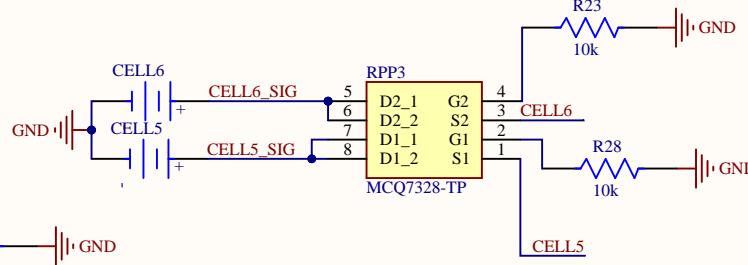
3

4

A

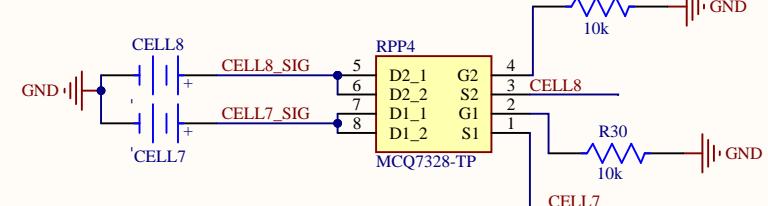
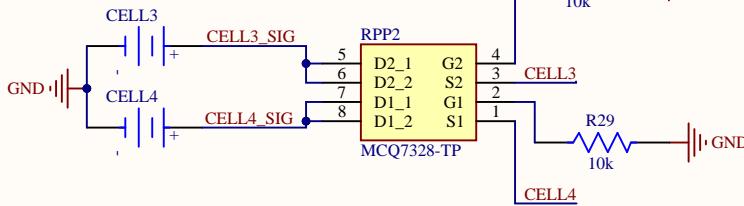


B

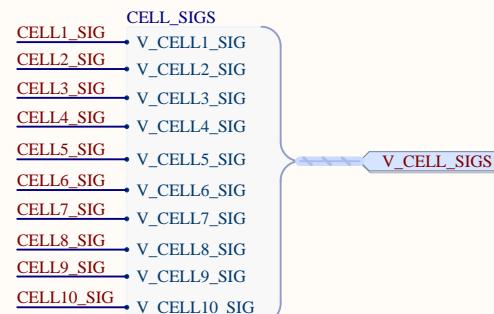
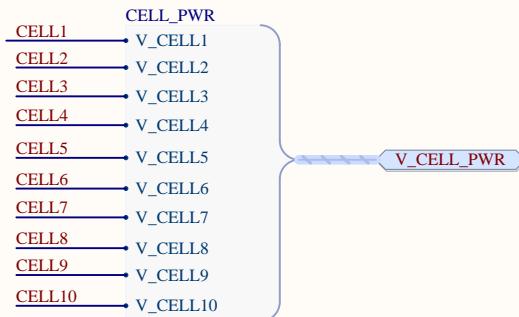


△ RPP1-5 are for reverse polarity protection. Approx voltage drop across D-S @ 5A is 140mv. SIG wires are added before RPP for kelvin sense

C



D



Title		
Size	Number	Revision
A4		
Date: 1-05-2026	Sheet of	
File: C:\Users\.\Cell Holders.SchDoc		Drawn By:

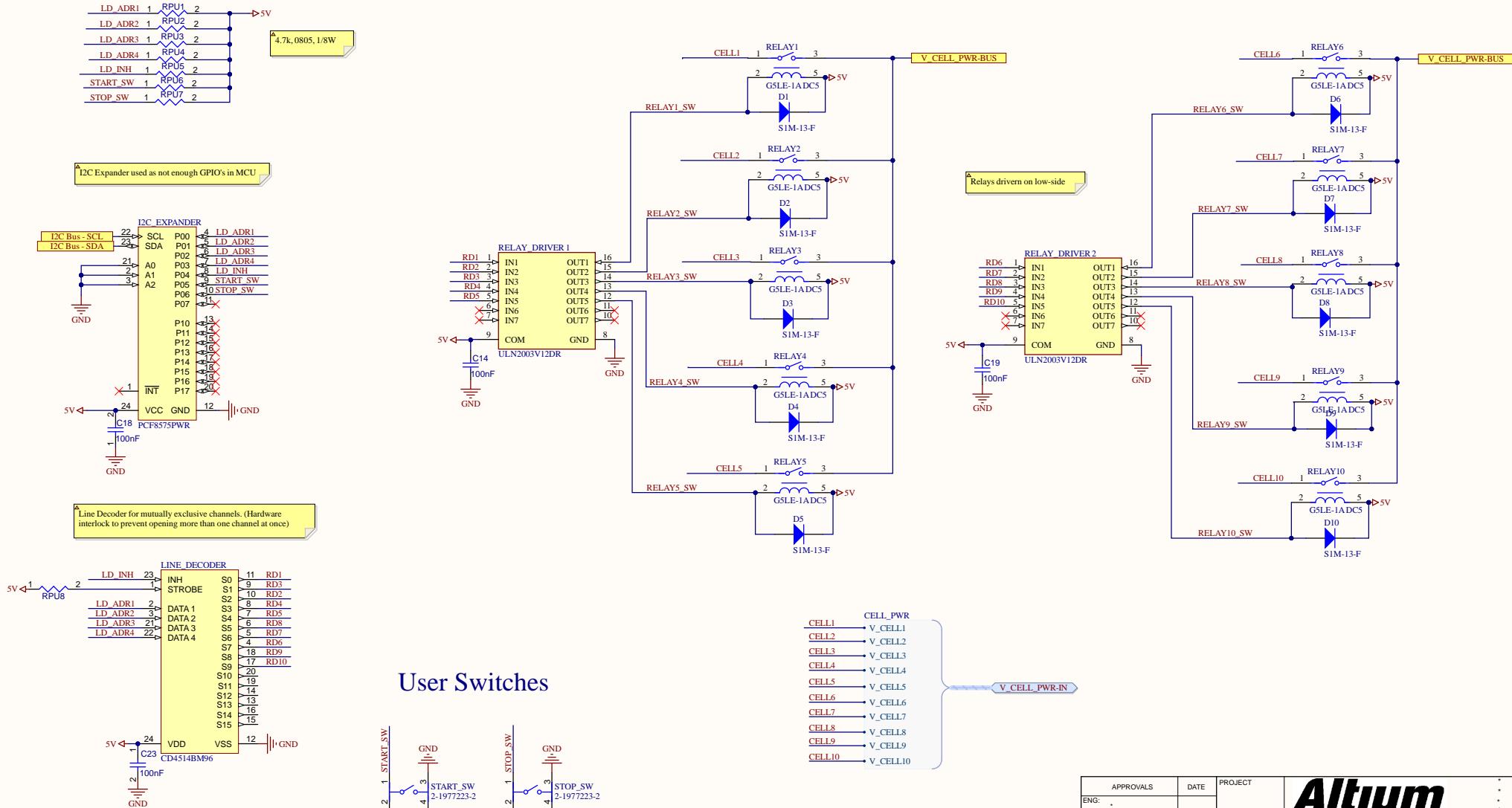
1

2

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4

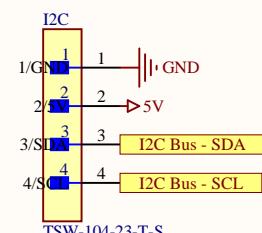
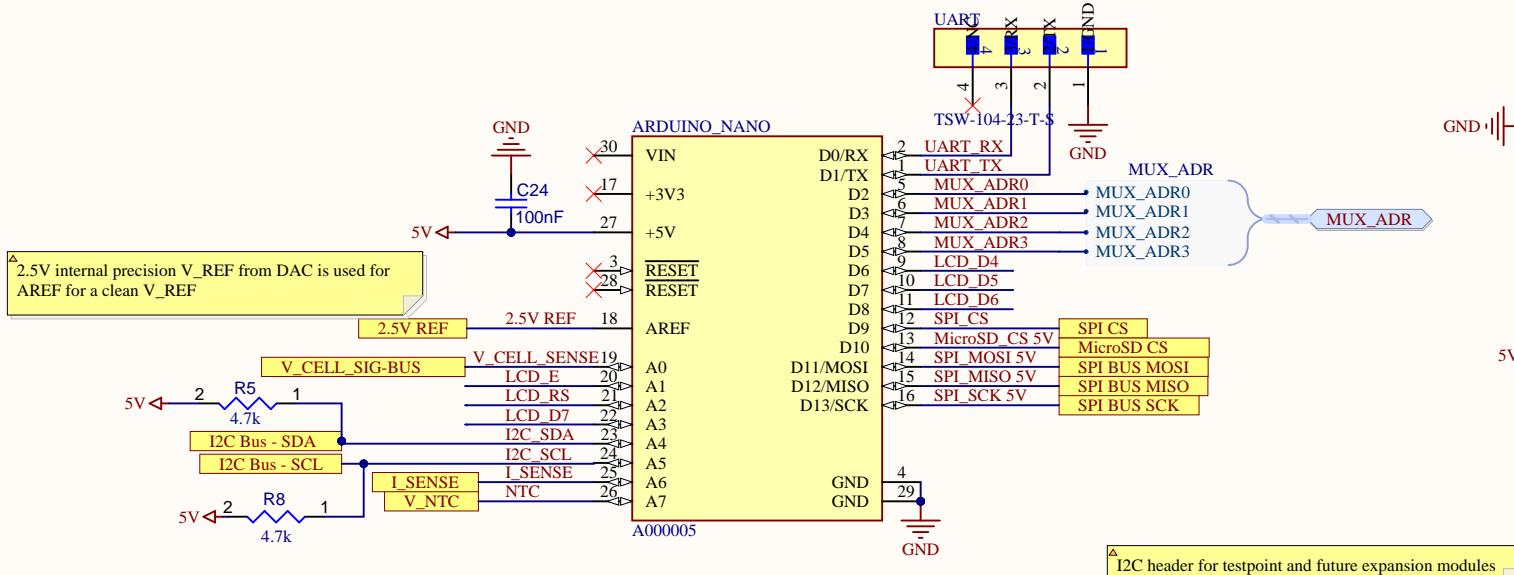
SEQUANTIAL CELL SWITCHING



APPROVALS	DATE	PROJECT	Altium		
ENG: *					
DSN: *					
CHK: *					
PROJECT REVISION: 66ea2eae9fedd1059cd9252d23a9a96ba9bb8c685e12648cef					
REFERENCE DOCUMENTS					
BOM: <BOM DOC NO>	SIZE: B		CAGE CODE: ?????	DWG NO: <SCH DWG NO>	
ASSY DWG: <ASSY DWG NO>	REV: *		FAB DWG: <FAB DWG NO>	PCB DWG: <PCB DWG NO>	
FILE NAME: Sequential Cell Switching.SchDoc	SCALE: 5 OF 8				

MCU (Nucleo / Nano)

LCD

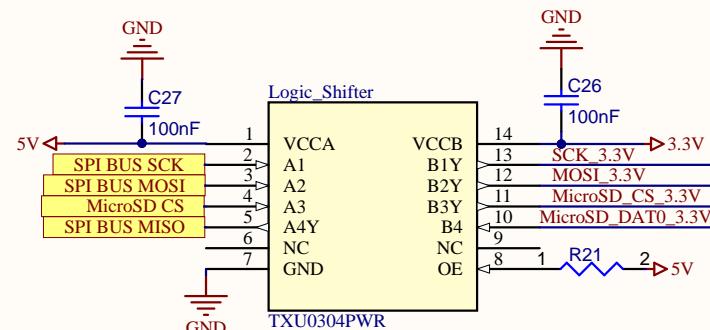


Title		
Size	Number	Revision
A4		
Date: 1-05-2026	Sheet of	
File: C:\Users\...\MCU.SchDoc		Drawn By:

MicroSD

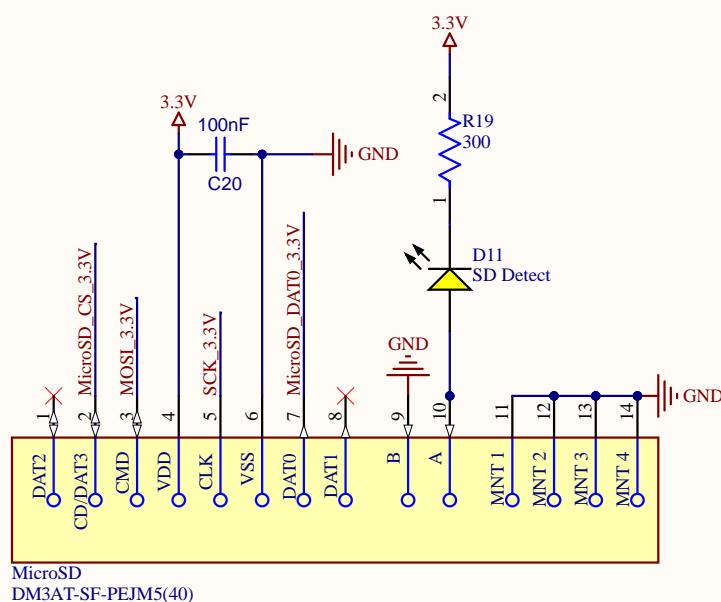
A

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B

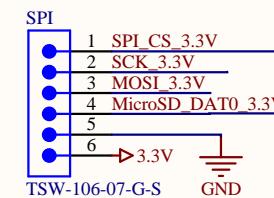
B



C

C

SPI header for testpoint and future expansion modules



D

D

Title

Size

Number

Revision

Date: 1-05-2026

Sheet of

File: C:\Users\...\MicroSD.SchDoc

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4

CELL MONITORING

B

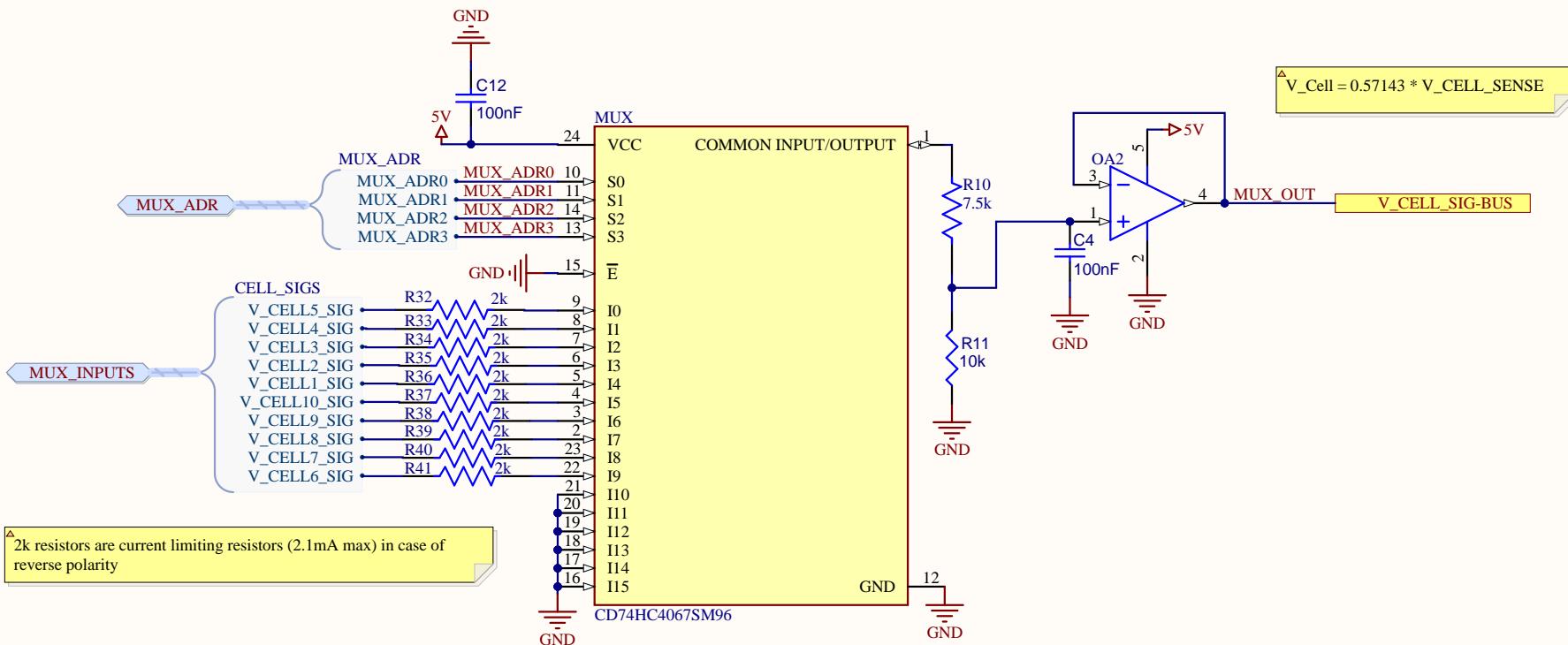
1

C

6

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1



Title		
Size A	Number	Revision
Date:	1-05-2026	Sheet of
File:	C:\Users\...\Cell Monitoring.SchDoc	Drawn By: