

# Guhan Iyer

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## Skills

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### Languages

C, C++, Java, Python, Scala, VHDL, Verilog, Assembly

### Tools & Technologies

Git, Processing, Arduino, FreeRTOS, CMake/Makefile, Django, NumPy, I<sup>2</sup>C, TCP/IP

## Experience

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### Research Assistant

UNIVERSITY OF WATERLOO

Waterloo, ON

SEPT. 2024 – DEC. 2024

- Developed a logic synthesis algorithm to optimize pre-synthesis **Verilog** circuits.

### Systems Software Engineering Intern

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Waterloo, ON

MAY 2024 – AUGUST 2024

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### Firmware Developer

UW ORBITAL

Waterloo, ON

JULY 2023 – PRESENT

- Developed a thermal monitoring system and essential functions for command & data handling.
- Leveraged **FreeRTOS** to develop an event handler for fatal over-temperature shutdowns.
- Improved system resiliency by integrating stack overflow handling and interrupt service logging.

## Projects

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### Maze Solving Firefighter Robot 🤖

BASIC, PIC MICROCONTROLLERS

- Built a robot to solve complex mazes in which it must locate and extinguish a lit candle.
- Developed an embedded system to manage sensor readings and allow for autonomous operation.
- Calibrated robot to be capable of **sub-five second** solves in standard competition mazes.

### Neural Network Cellular Automaton 🤖

JAVA, PROCESSING, GIT

- Created a cellular automaton which models the formation and operation of a biological neural circuit.
- Designed and documented a finite state machine and path-finding algorithm.
- Implemented ruleset with **Java**, visual output with **Processing**.

## Education

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### University of Waterloo

CANDIDATE FOR BASC. IN COMPUTER ENGINEERING

Waterloo, ON

SEPT. 2023 – PRESENT

- **Relevant Coursework:** Algorithms & Data Structures, Digital Computers