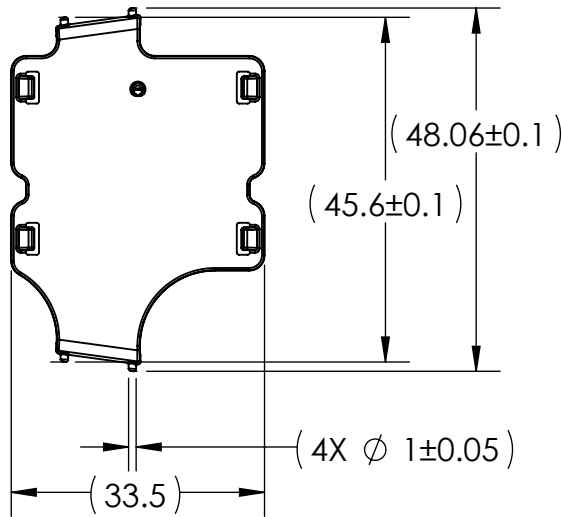
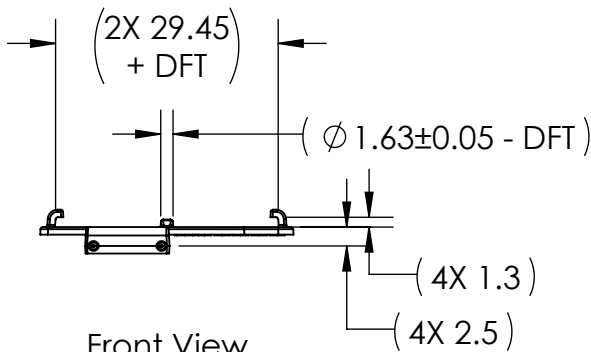


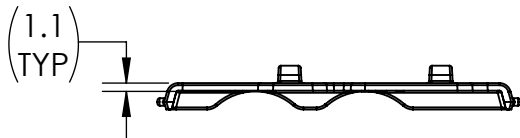
REVISIONS						
REV.	FAB CAD FILE	NATIVE DRAWING FILE	NATIVE CAD FILE	DESCRIPTION	DATE	APPROVED
1	VS0009-1.STEP	VS0009-1.SLDDRW	VS0009-1.SLDPRT	Initial Prototype Release.	8/8/2016	TB/DT
A	VS0009-A.STEP	VS0009-A.SLDDRW	VS0009-A.SLDPRT	Production release. Added PN and revision dot pattern.	9/16/2016	TB/DT



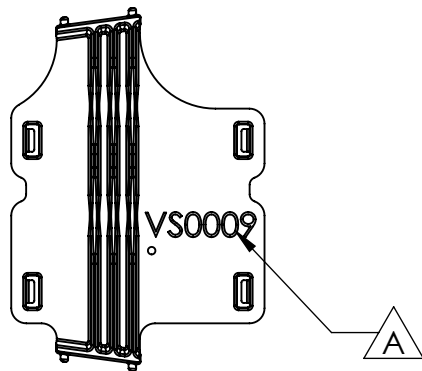
Top View



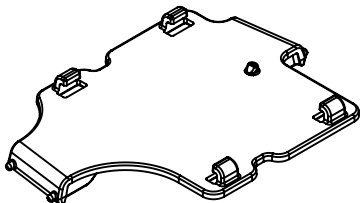
Front View



Right View



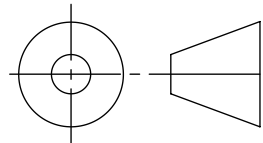

Bottom View
No Texture



Isometric View

Notes:

1. All dimensions are reference only. 3D CAD file is the master document.
2. Dimensions in Millimeters
3. All materials and finishes must be RoHS compliant.
4. Material: Makrolon 2407-550115
5. Color: PANTONE 11-0602 TCX Snow White
6. Mold Texture (unless otherwise noted): None.
7. Tolerances: DIN ISO 2768-M (unless otherwise noted)
Parting line mismatch must not exceed 0.13mm.
8. Mold design must be approved by VeriSolutions prior to mold fabrication.
9. Draft is maximum allowable.
10. Trim gate to flush or subflush.

 THIRD ANGLE PROJECTION	CUSTOMER VeriSolutions	DRAWN BY T. Burkhard	DATE 08/08/16	TITLE: VeriSense PCB Carrier			 DT ENGINEERING ASSOCIATES INC. 920 Reserve Dr. Suite 190 Roseville, CA95678 Tel. 916 474-5167 Fax 916 749-3793 www.dtengineer.com		
	PROGRAM NAME VeriSense	ENGINEER T. Burkhard	DATE 08/08/16						
	NEXT ASSEMBLY VeriSense	CHECKED D. Tostenson	DATE 08/08/16	DWG. NO. VS0009					
	TOLERANCES See Notes	APPROVED S. Justice	DATE 08/08/16	SCALE: 1:1	WEIGHT:	SHEET 1 OF 1			