Instrução	Opcode e/ou identificador único	Sinais de controle													
		RegDst	Jump	BranchNEQ	BranchEQ	MemToReg	AluOp(3)	AluOp(2)	AluOp(1)	AluOp(0)	MemWrite	AluSrc	RegWrite	PcSrc	ITController
ADD	000/0110011	1	0	0	0	0	0	0	0	1	0	0	1	1	0
SUB	001/0110011	1	0	0	0	0	0	0	1	0	0	0	1	1	0
ADDI	000/0010011	1	0	0	0	0	0	0	1	1	0	1	1	1	0
SLL	010/0110011	1	0	0	0	0	0	1	0	0	0	0	1	1	0
SRL	011/0000011	1	0	0	0	0	0	1	0	1	0	0	1	1	0
LW	010/0100011	1	0	0	0	1	0	1	1	0	0	1	1	1	0
SW	010/0000011	1	0	0	0	0	0	1	1	1	1	1	0	1	0
LB	000/0100011	1	0	0	0	1	1	0	0	0	0	1	1	1	0
SB	000/0100011	1	0	0	0	0	1	0	0	1	1	1	0	1	0
BEQ	011/0100011	1	0	0	0	0	1	0	1	0	0	0	0	1	0
BNE	001/0100011	1	0	0	0	0	1	0	1	1	0	0	0	1	0
SLT	100/0110011	1	0	0	0	0	1	1	0	0	0	0	1	1	0
SLTI	001/0010011	1	0	0	0	0	1	1	0	1	0	1	1	1	0
J	xxx/0000010	1	1	1	1	0	x	x	x	x	0	0	0	1	0
JAL	xxx/1101111	0	0	1	1	0	x	x	x	x	0	0	1	1	0
JARL	xxx/1100111	0	0	1	1	0	x	x	x	x	0	1	1	1	0
ECALL	xxx/1110011	0	0	0	0	0	x	x	x	x	0	0	0	0	1
NOP	010/0010011	1	0	0	0	0	x	x	x	x	0	0	0	1	0