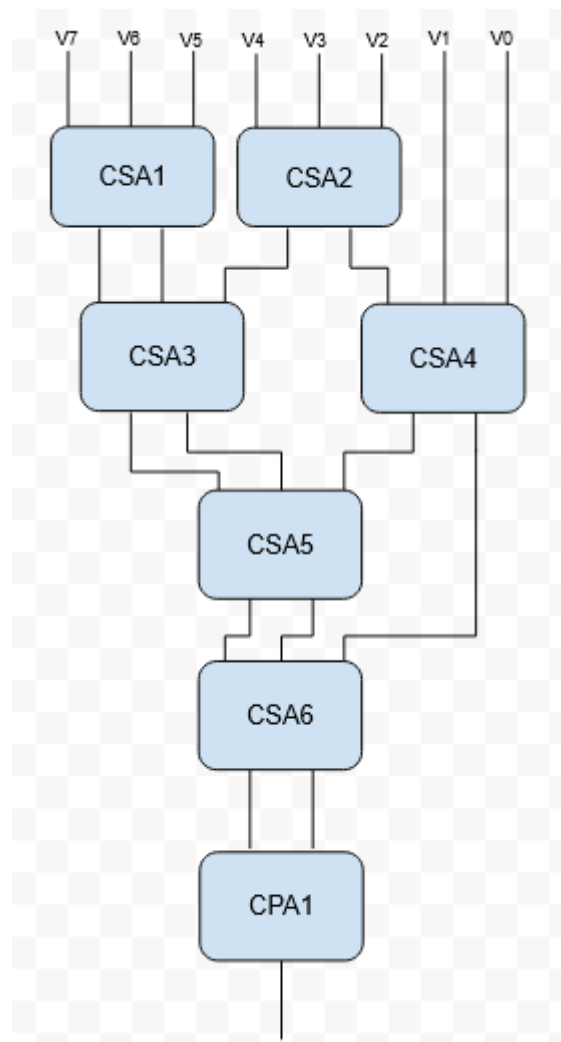# VHDL_Cap8

Guilherme Henrique Paggi Daros

19100811 - EEL7123 - 08235

A=0, B=0, C=0, D=0

A=1, B=0, C=0, D=0

A=0, B=1, C=0, D=0

A=0, B=0, C=1, D=0

A=0, B=0, C=0, D=1

A=15, B=15, C=15, D=15