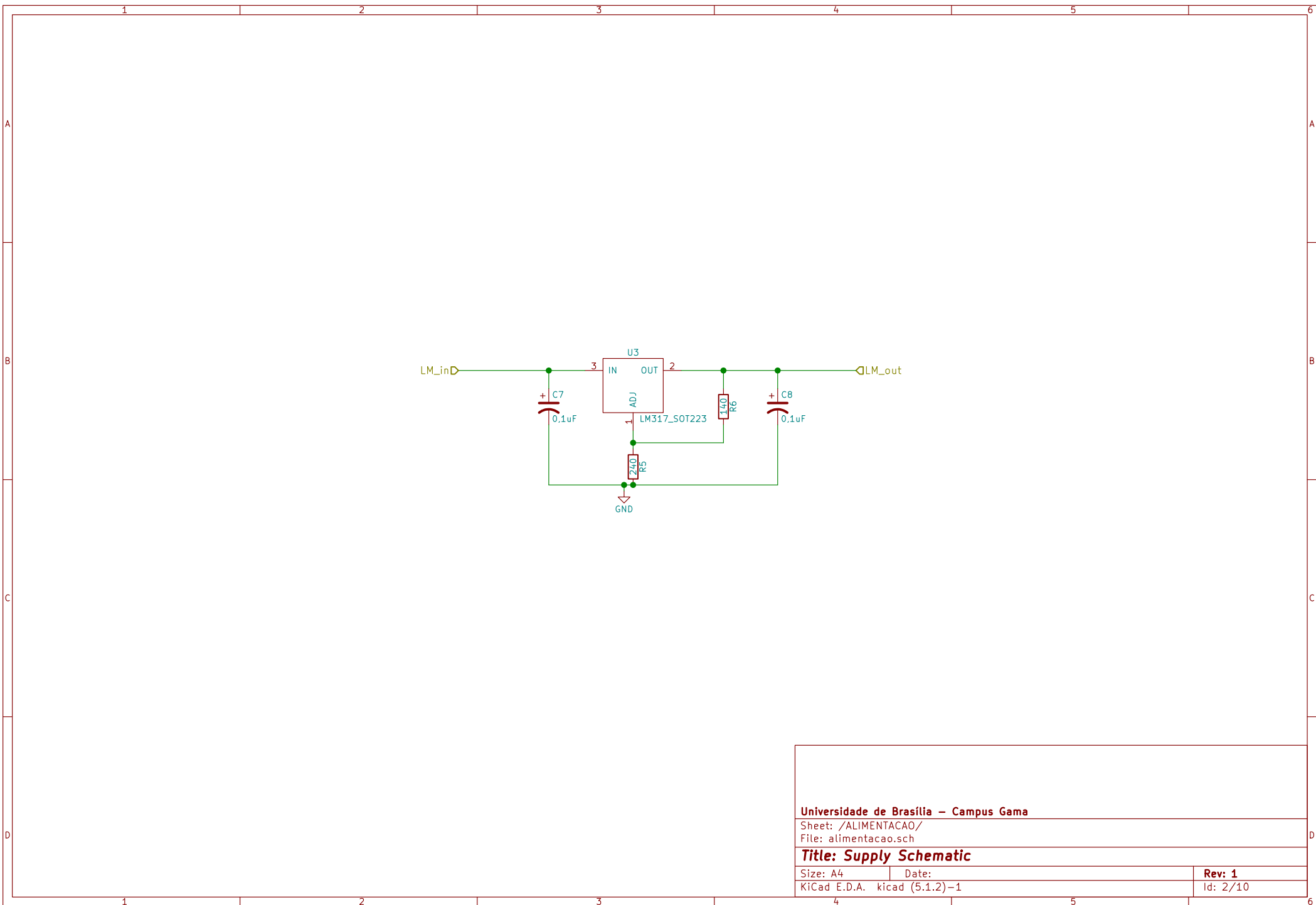
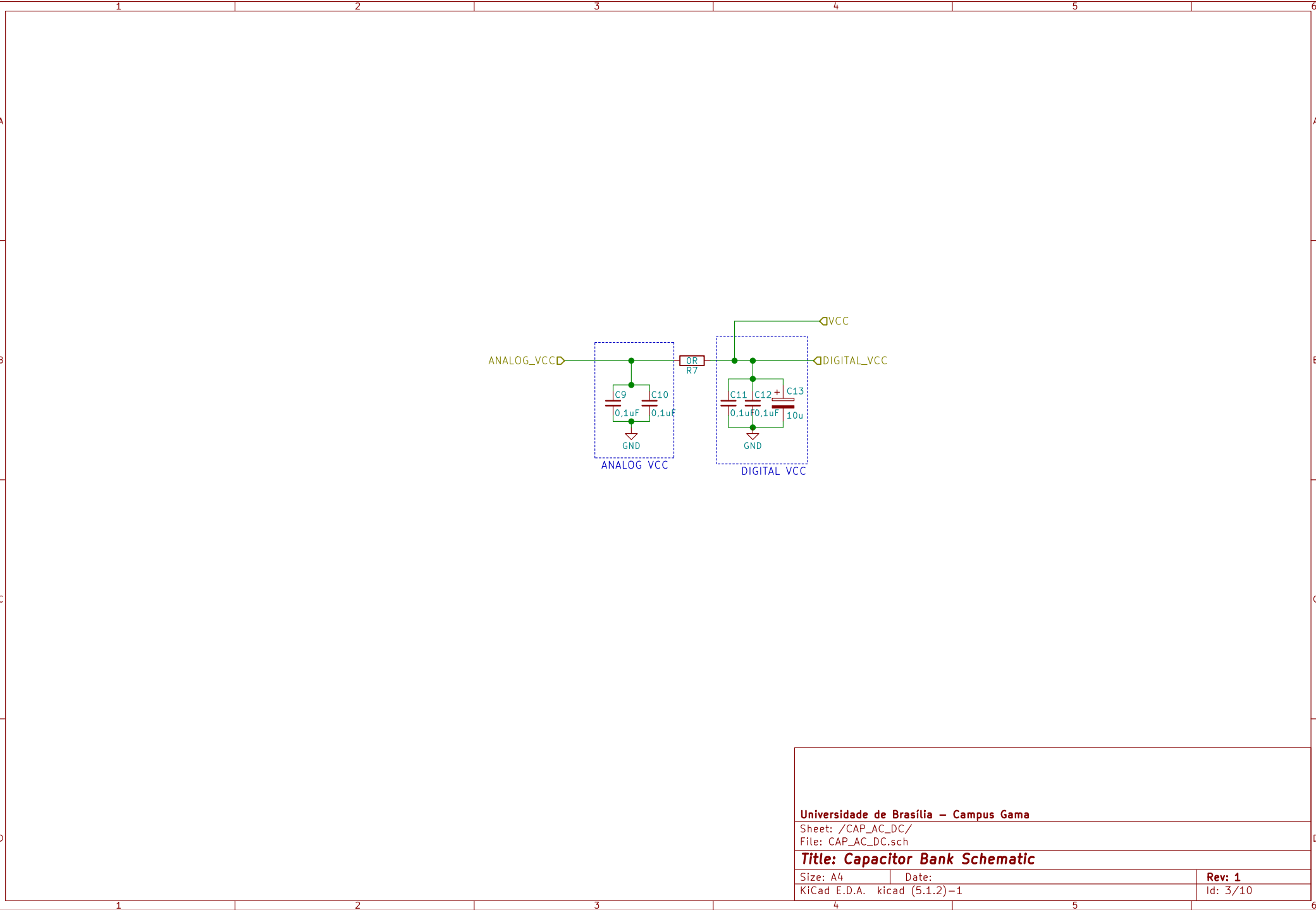


Id: 1/10





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Sheet: /CAP_AC_DC/

File: CAP_AC_DC.sch

Title: Capacitor Bank Schematic

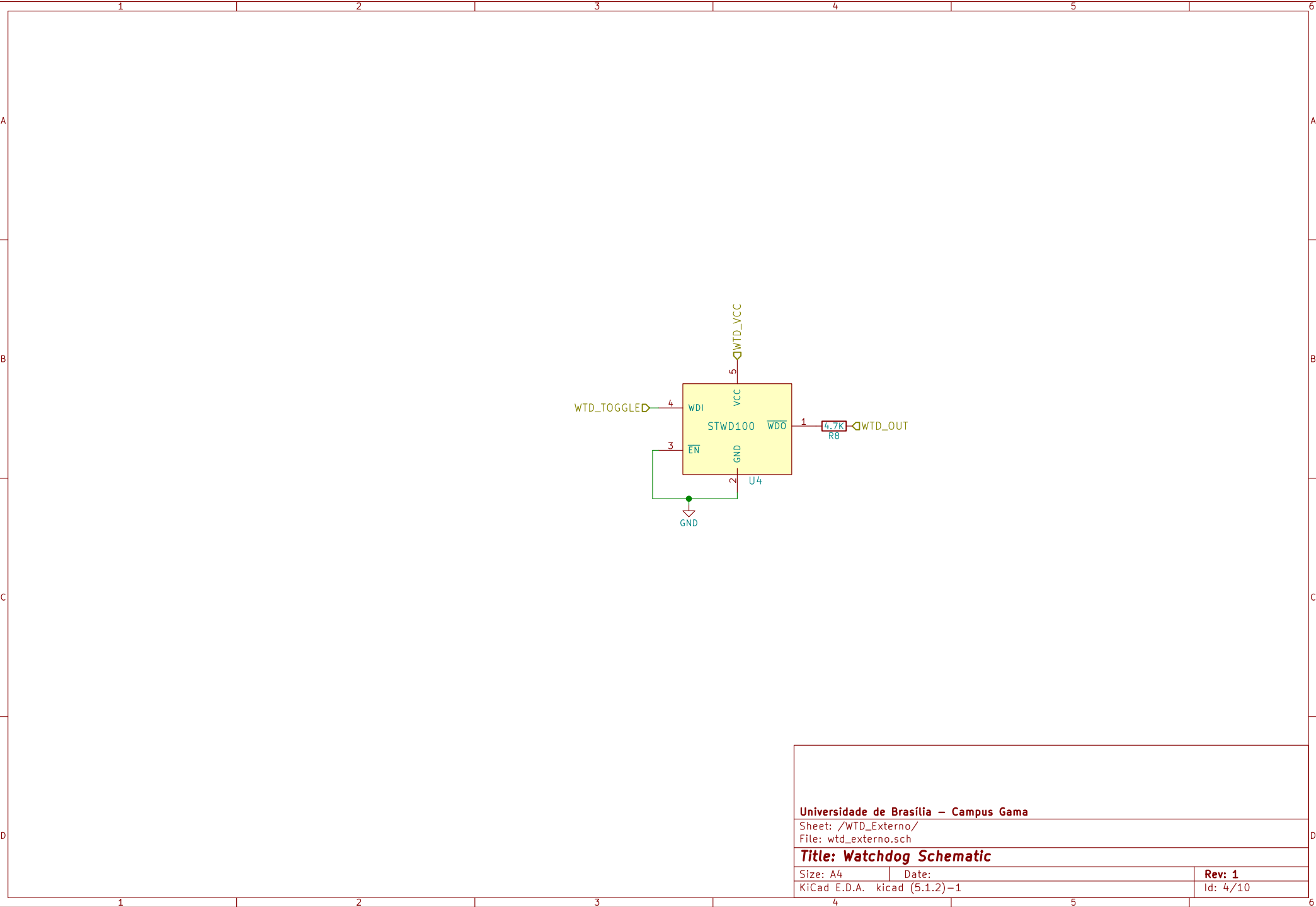
Size: A4

Date:

KiCad E.D.A. kicad (5.1.2)–1

Rev: 1

Id: 3/10

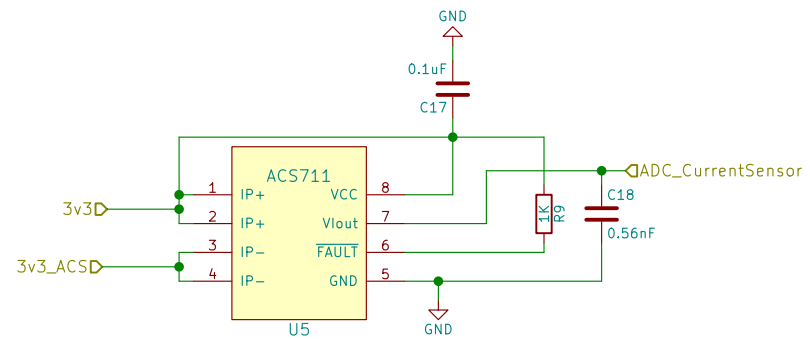


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Sheet: /WTD_Externo/
File: wtd_externo.sch

Title: Watchdog Schematic

Size: A4	Date:	Rev: 1
KiCad E.D.A. kicad (5.1.2)–1		Id: 4/10



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Sheet: /CurrentSensor/

File: CurrentSensor.sch

Title: Current Sensor Schematic

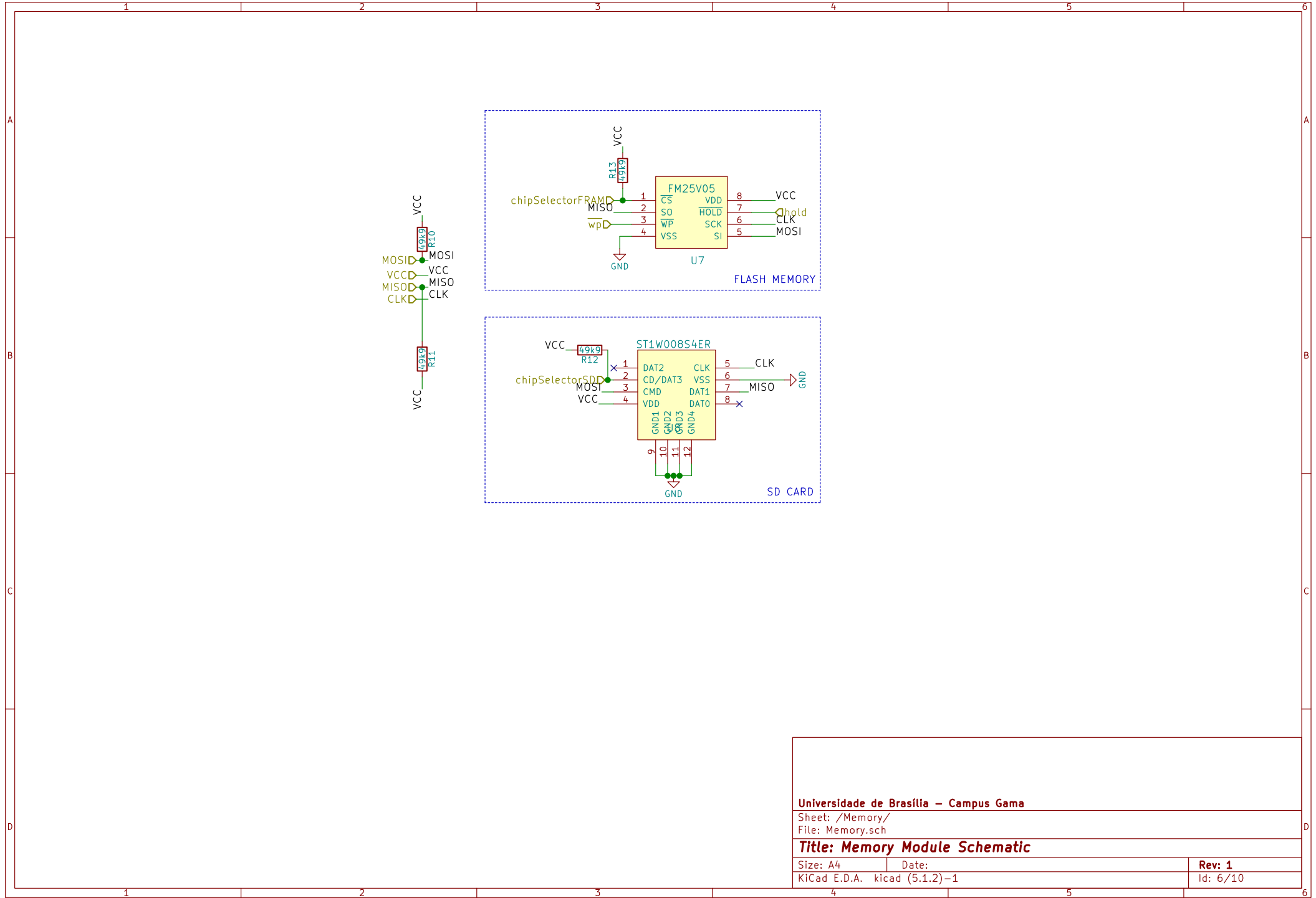
Size: A4

Date:

KiCad E.D.A. kicad (5.1.2)–1

Rev: 1

Id: 5/10



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Sheet: /Memory/

File: Memory.sch

Title: Memory Module Schematic

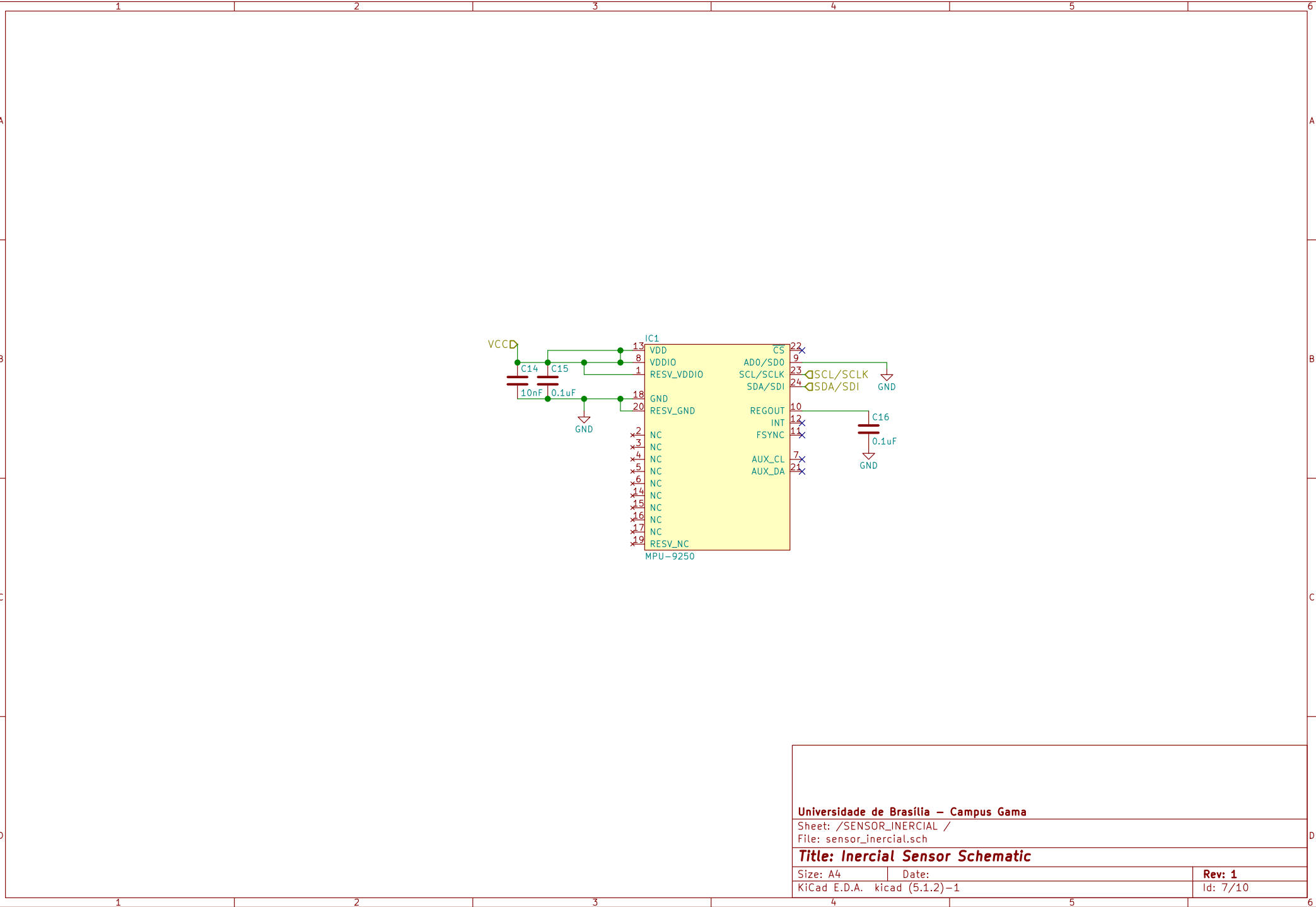
Size: A4

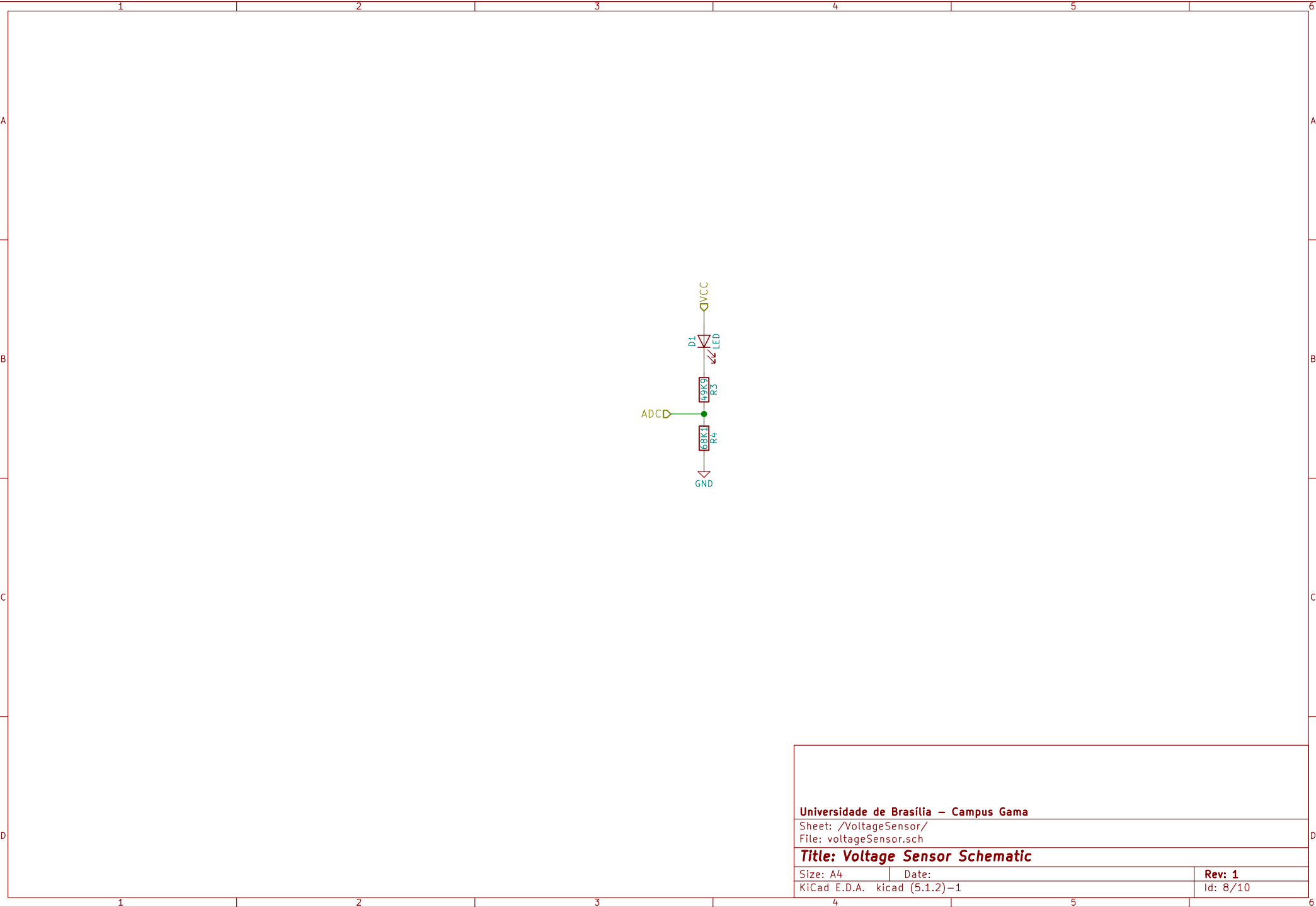
Date:

KiCad E.D.A. kicad (5.1.2)–1

Rev: 1

Id: 6/10





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Sheet: /VoltageSensor/

File: voltageSensor.sch

Title: Voltage Sensor Schematic

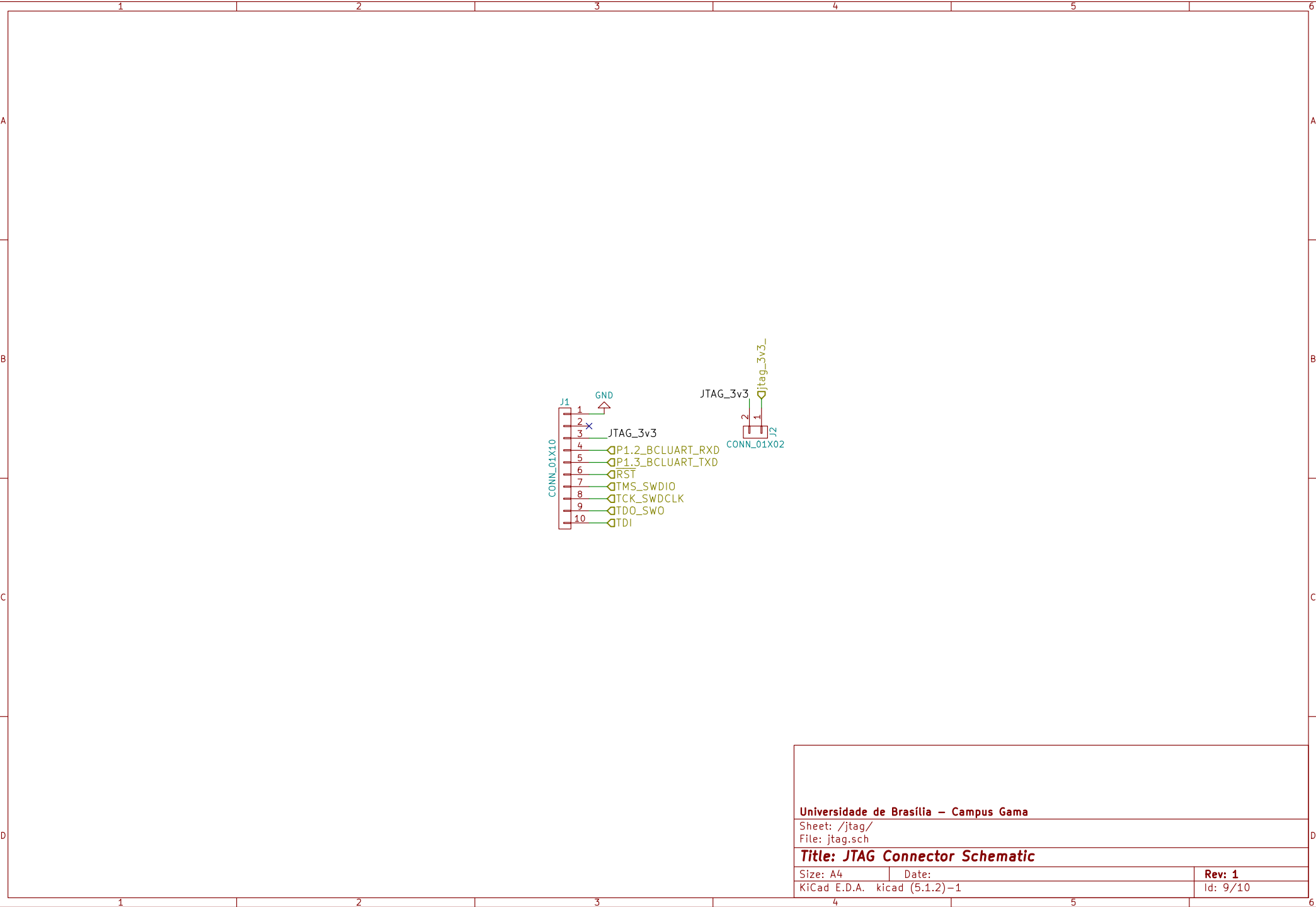
Size: A4

Date:

Rev: 1

KiCad E.D.A. kicad (5.1.2)–1

Id: 8/10



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Sheet: /jtag/

File: jtag.sch

Title: JTAG Connector Schematic

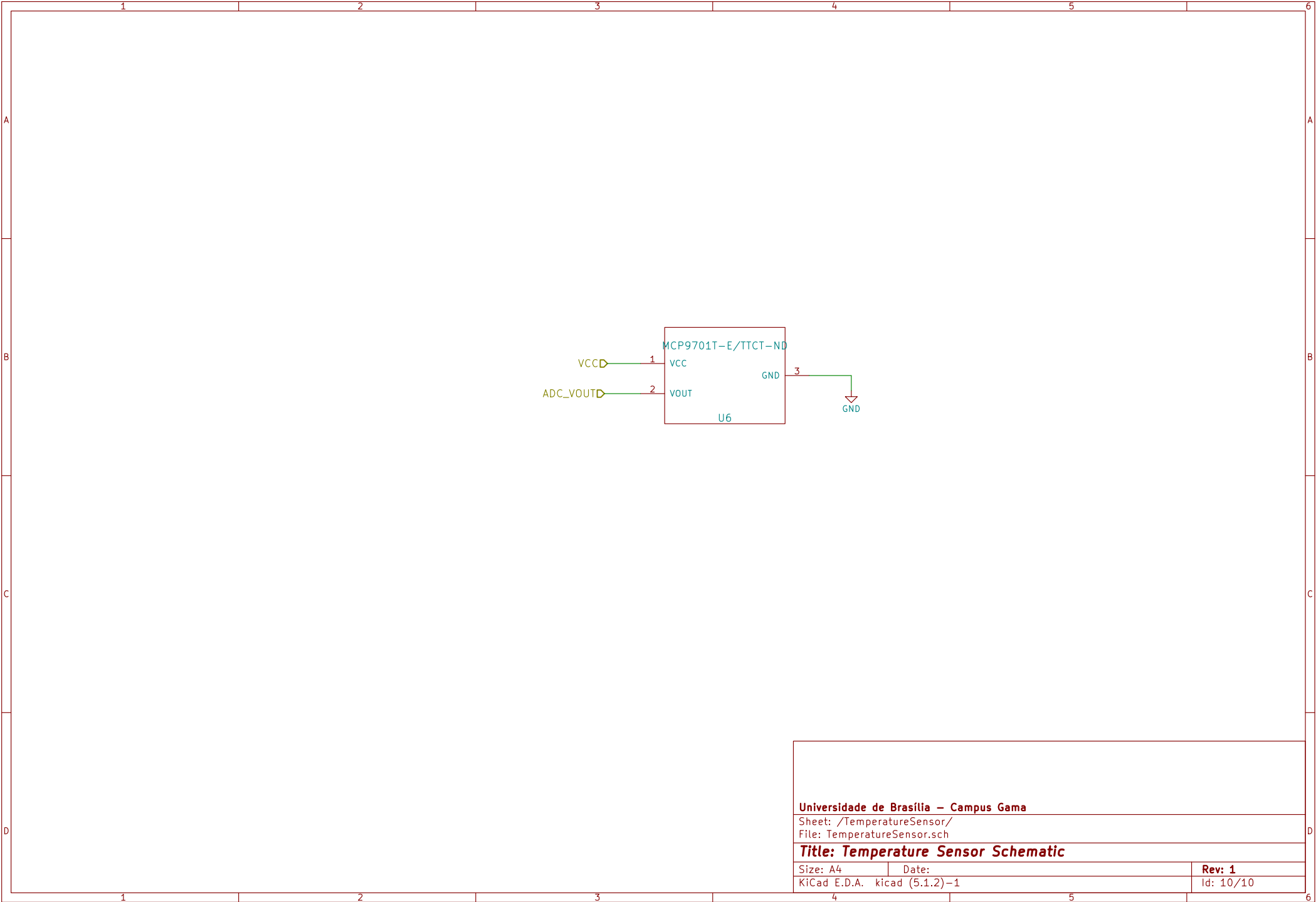
Size: A4

Date:

Rev: 1

KiCad E.D.A. kicad (5.1.2)-1

Id: 9/10



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Sheet: /TemperatureSensor/

File: TemperatureSensor.sch

Title: Temperature Sensor Schematic

Size: A4

Date:

Rev: 1

KiCad E.D.A. kicad (5.1.2)-1

Id: 10/10