## **GUILLERMO MONTECINOS**

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## **WORK**

2016 – present <b>Analist Engineer</b> , Enel Power Chile		
2016 – 2016	Project Engineer, Systep Engineering and Consultancy Services	
2014 – 2016	Instructor of "Laboratory of Electronics" course, Universidad Diego Portales, Chile	
2012 – 2013	Instructor of "Electrical Circuits" course, Pontificia Universidad Católica de Chile	

## **EDUCATION**

2016 – 2016	Musical Composition course, Projazz Institute of Music, Chile
2013 – 2015	Master of Science in Electrical Engineering, Pontificia Universidad Católica de Chile
2008 – 2014	Electrical Engineer, Professional Degree and B.S., Pontificia Universidad Católica de Chile

## **COMPUTATIONAL SKILLS**

Basic Javascript, Max MSP/Jitter, Processing

Intermediate C, Java, Visual Basic, Ableton Live