



EE3704 Embedded System

Chapter 6.5

Presented by

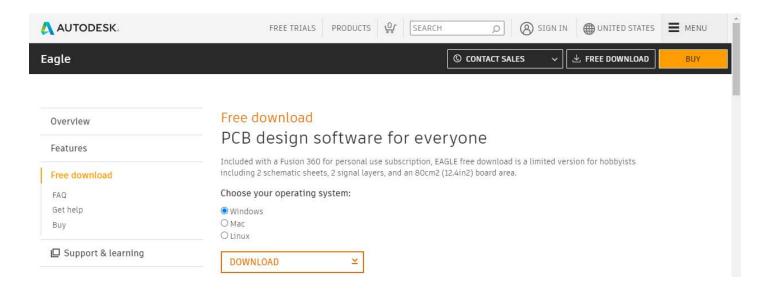
Asst. Prof. Dr. Narong Aphiratsakun

Chapter 6.5 : PCB Design



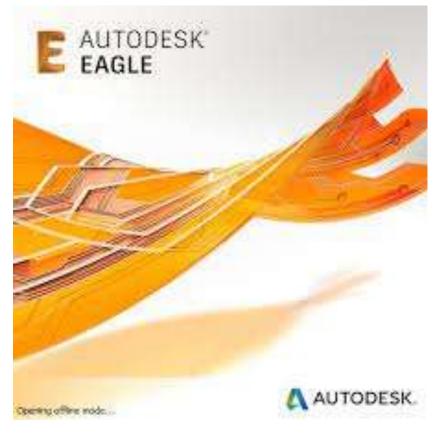


Chapter 6.5 : AutoDesk

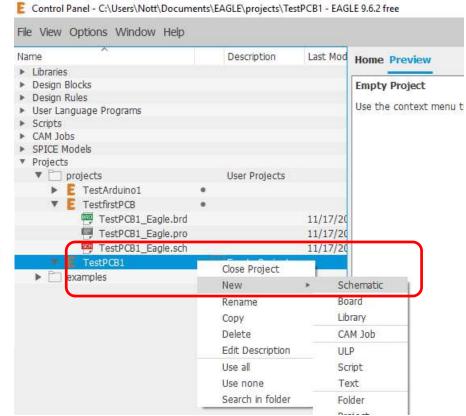


https://www.autodesk.com/products/eagle/free-download?plc=F360&term=1-YEAR&support=ADVANCED&quantity=1

Chapter 6.5 : AutoDesk Eagle



Chapter 6.5: Adding project



Create New Schematic

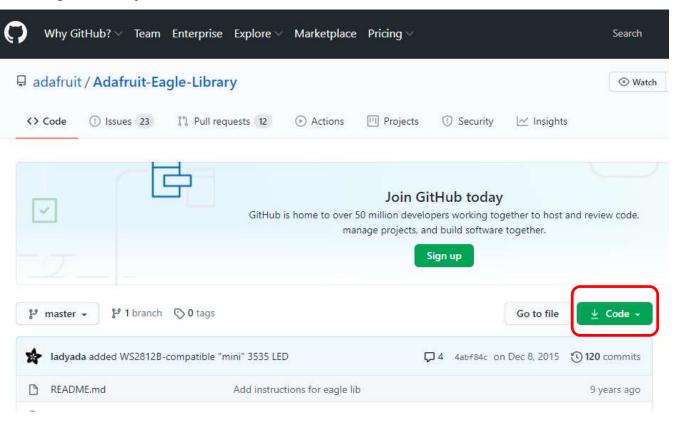


Chapter 6.5: Downloading library

https://github.com/adafruit/Adafruit-Eagle-Library

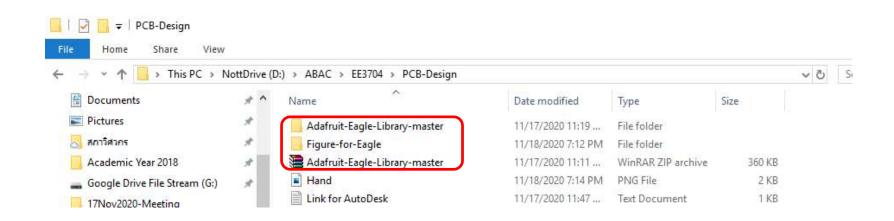


Download Adafruit Eagle library

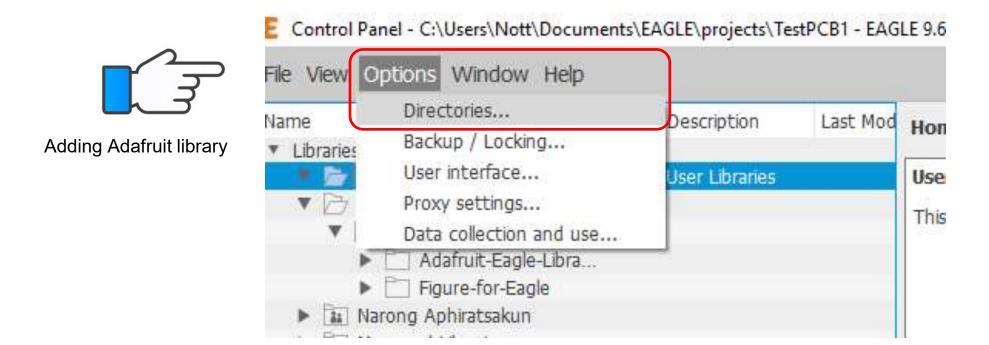


Chapter 6.5: Adding library

Extracted in your directive folder



Chapter 6.5 : Adding library

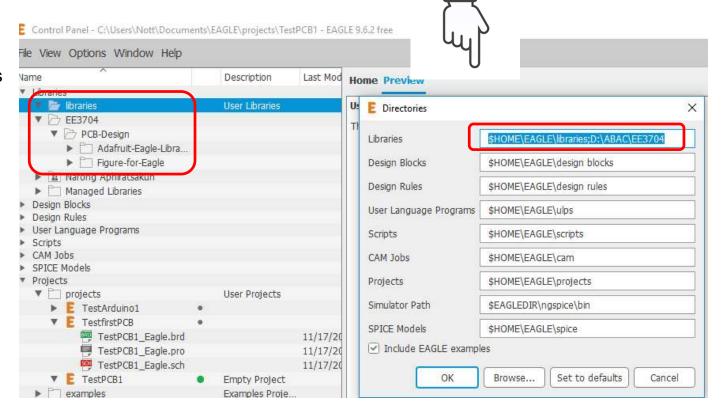


Chapter 6.5 : Adding library

Select the directive folder

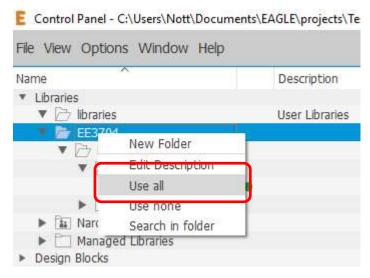






Chapter 6.5 : Adding library



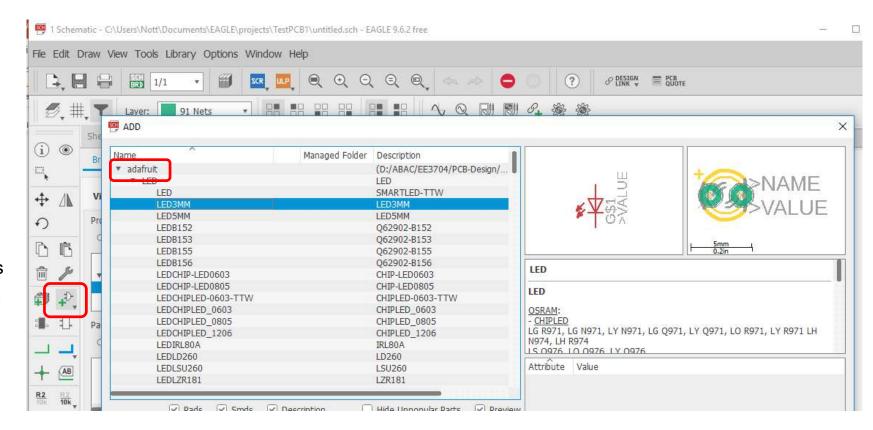




Adafruit library added

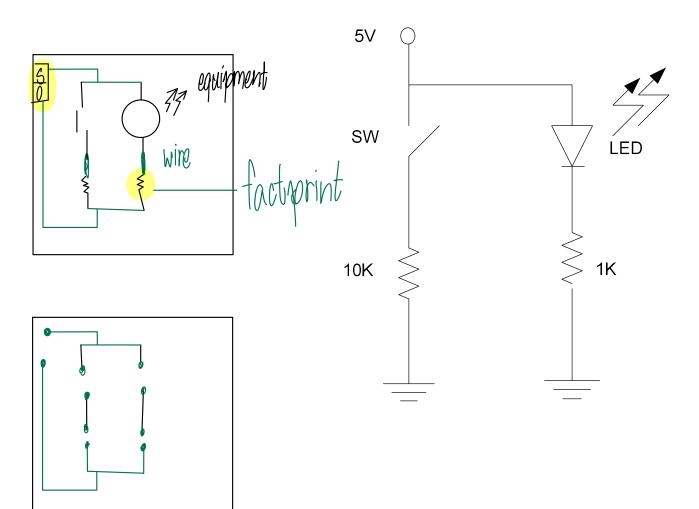


Chapter 6.5: Adding parts after library added

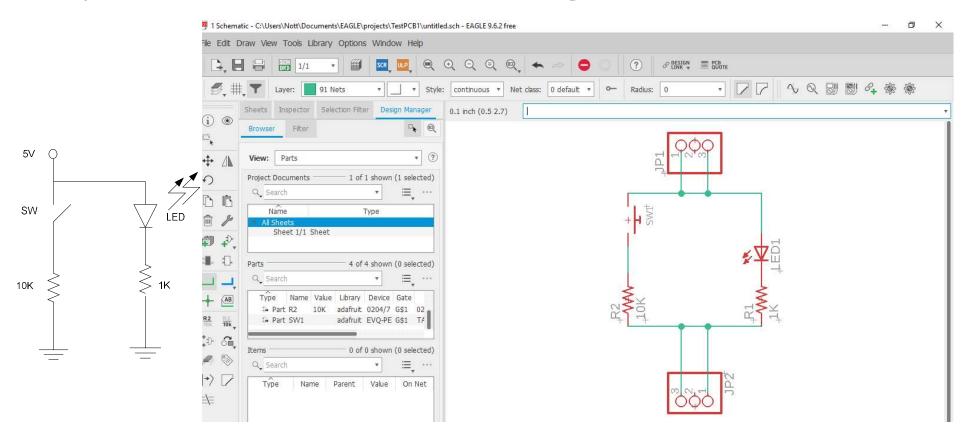




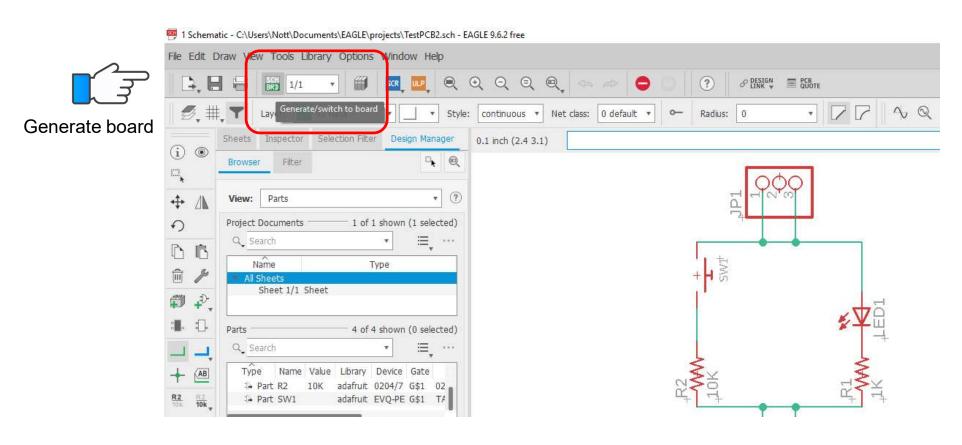
Chapter 6.5: Example design



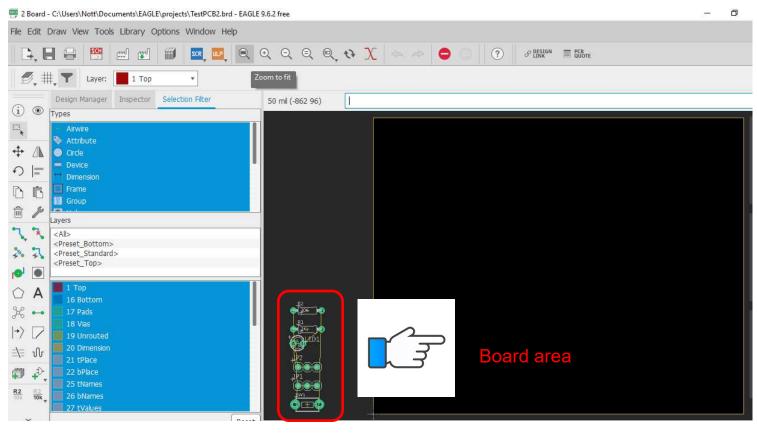
Chapter 6.5: Draw the schematic diagram



Chapter 6.5 : Generate to PCB

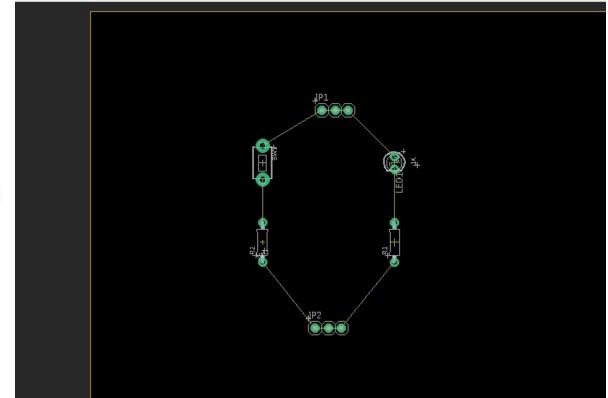


Chapter 6.5: Arrange the PCB layout



Chapter 6.5 : Arrange the PCB layout

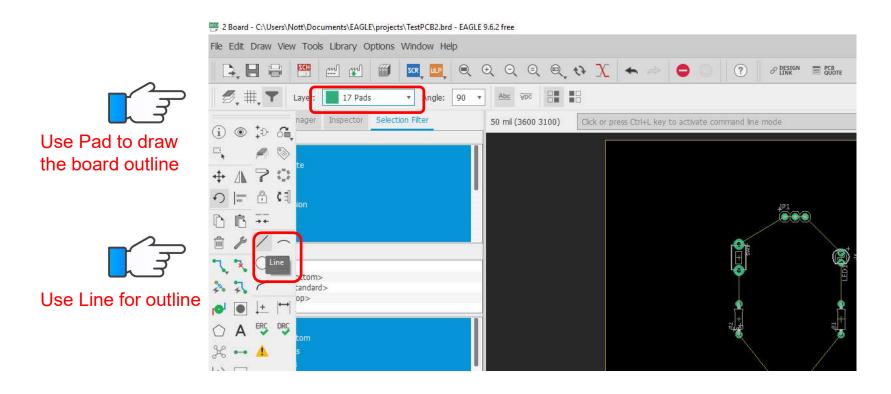
50) Click or press Ctrl+L key to activate command line mode



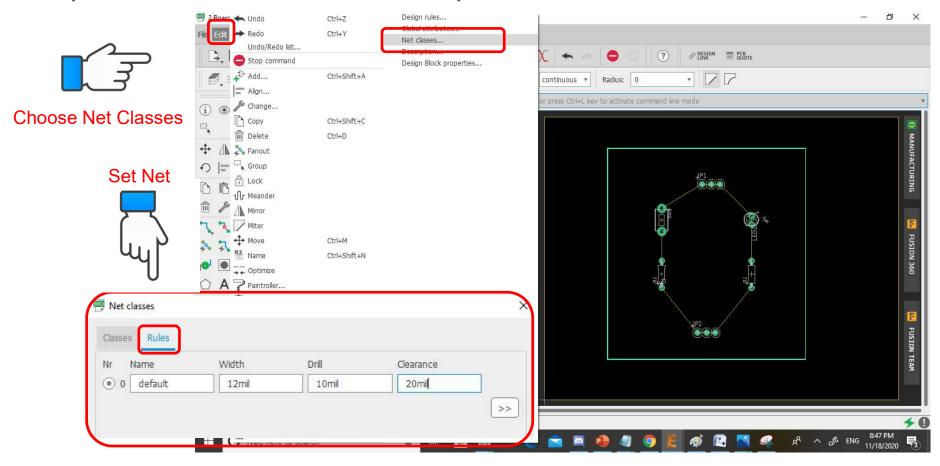


Arrange layout in the area

Chapter 6.5: Draw the board outline

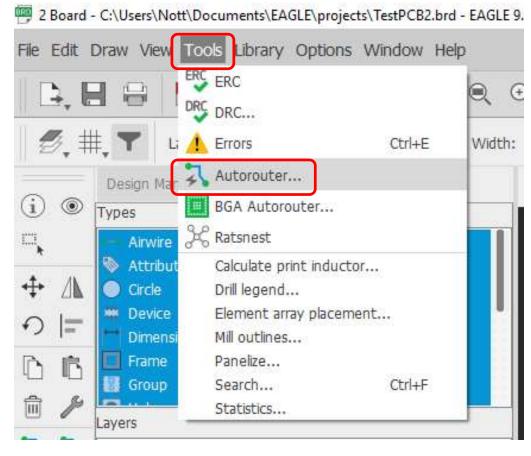


Chapter 6.5: Net Classes set up

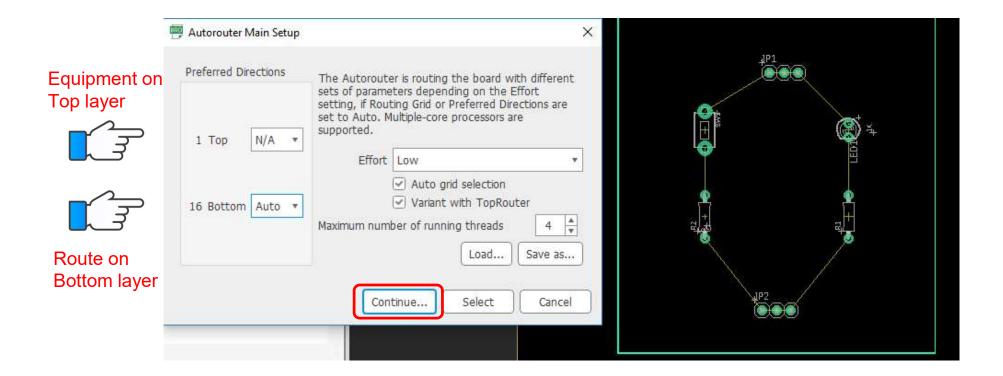


Chapter 6.5: AutoRoute





Chapter 6.5: Route at Bottom layer

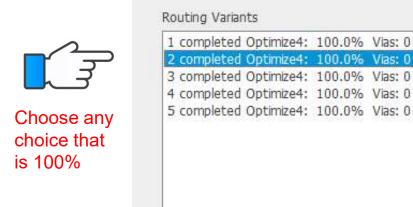


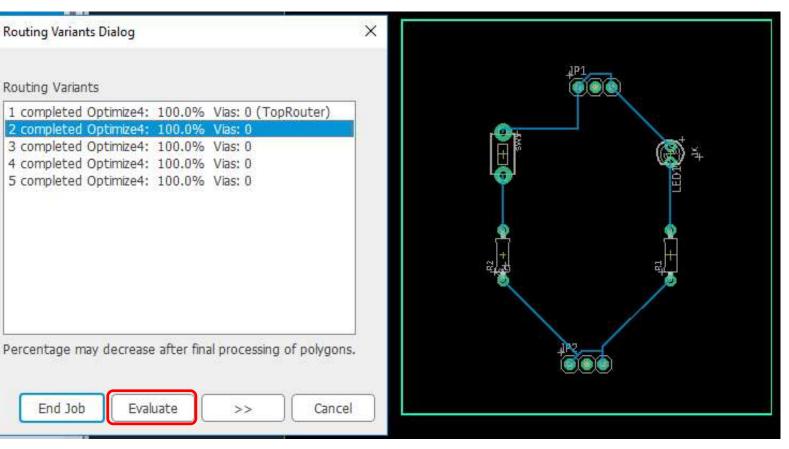
Chapter 6.5: Choose the design

Routing Variants Dialog

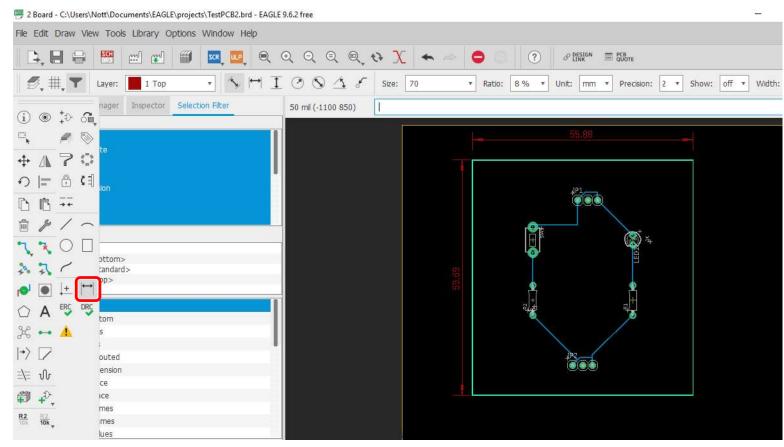
End Job

Evaluate



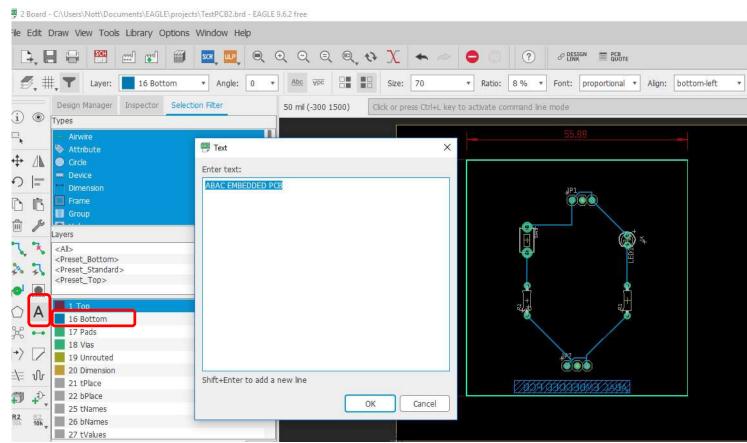


Chapter 6.5: Board dimension





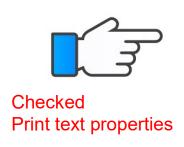
Chapter 6.5: Text on PCB

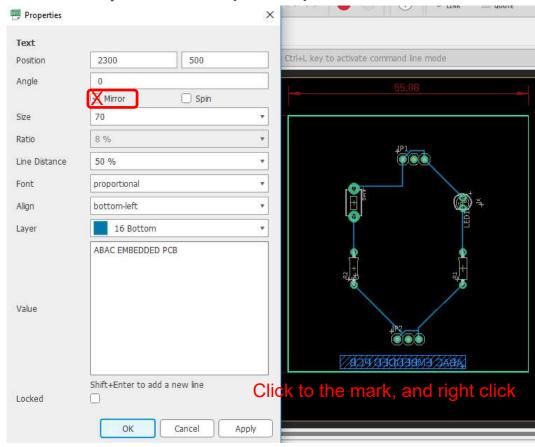


Add text to the board bottom layer



Chapter 6.5: Print for process (Text)

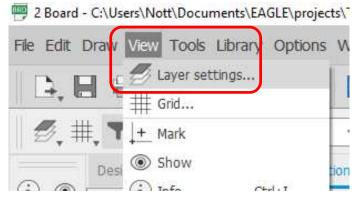


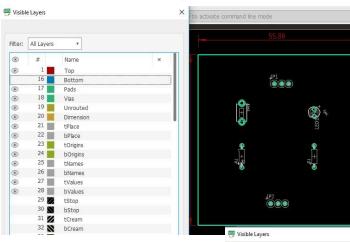


Chapter 6.5 : View from Layers Top layer

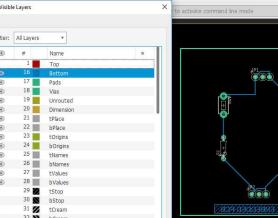
To see how board look like on both layers





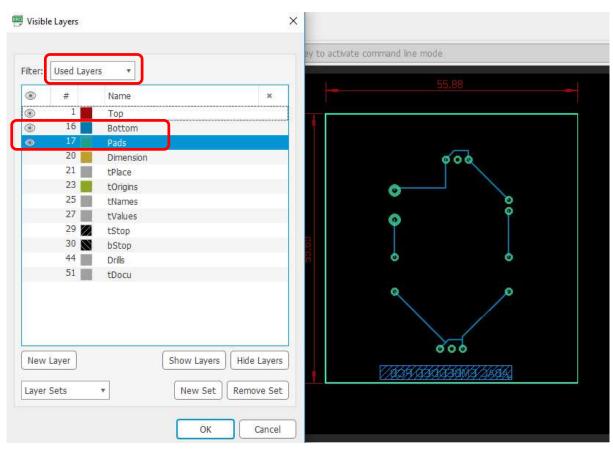


Bottom layer

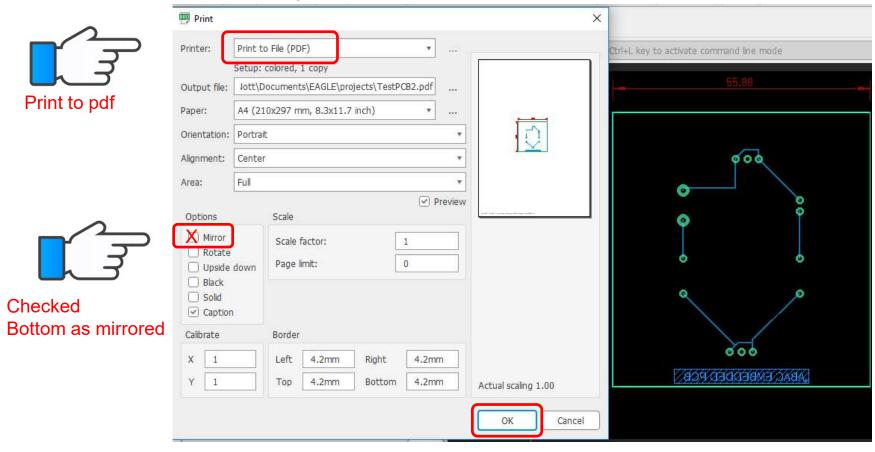


Chapter 6.5: Print for process (Layers)

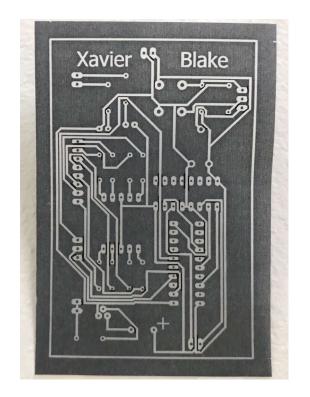




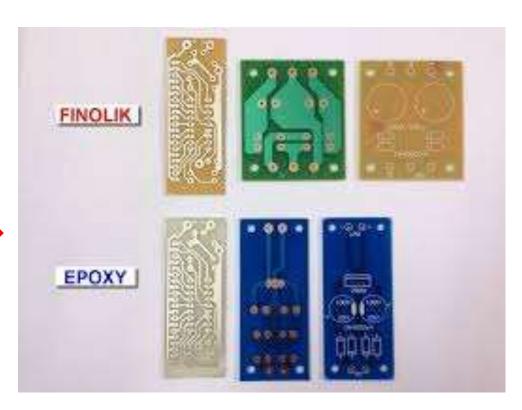
Chanter 6 5 · Print for process (Post-Process)



Chapter 6.5 : PCB Design







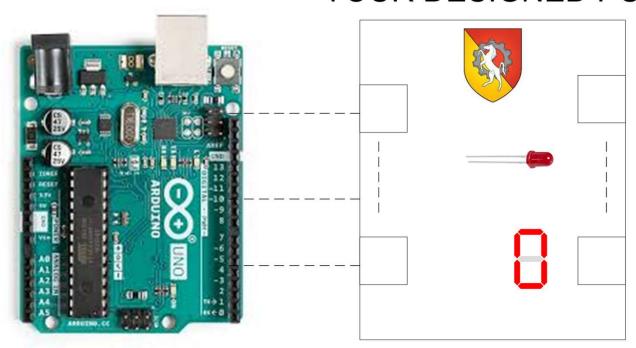
Chapter 6.5: PCB Shop (can be send via Email)

http://www.seegate15.com/Product1.html

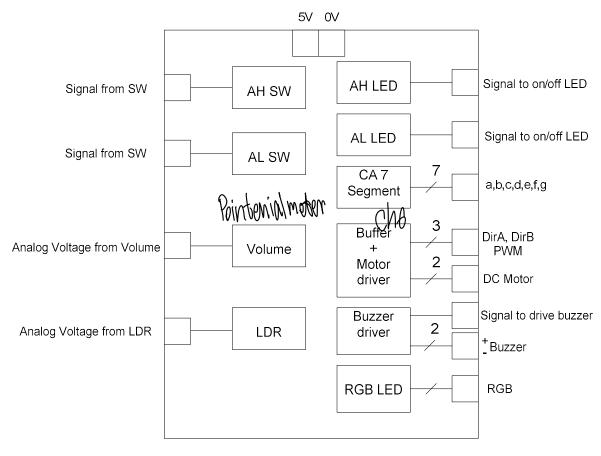


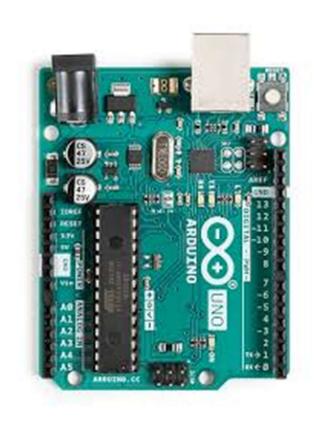
ร้าน ชีเกท เชอร์กิต Email : seegatel5@hotmail.com 127 ถ.อัษญางค์ แขวงวังบุรพาภิรมณ์ เขตพระนคร กรุงเทพฯ 10200 โทร 02-2215461,02-6238692 แฟ็กซ์ 02-6239102

YOUR DESIGNED PCB



5W lok LEI 220



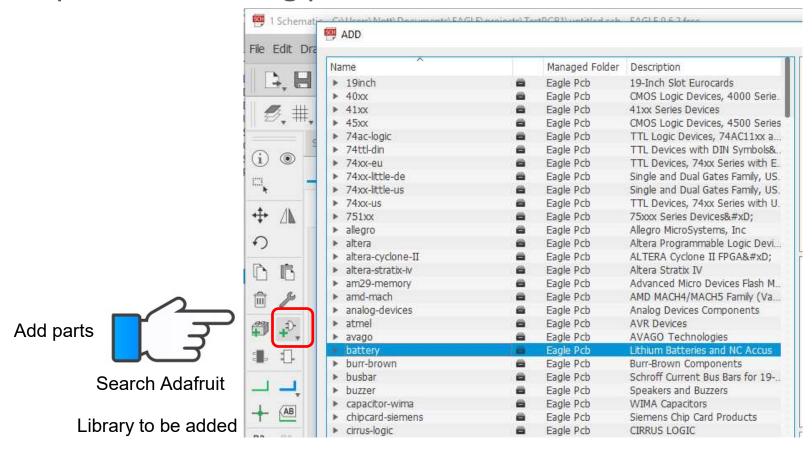






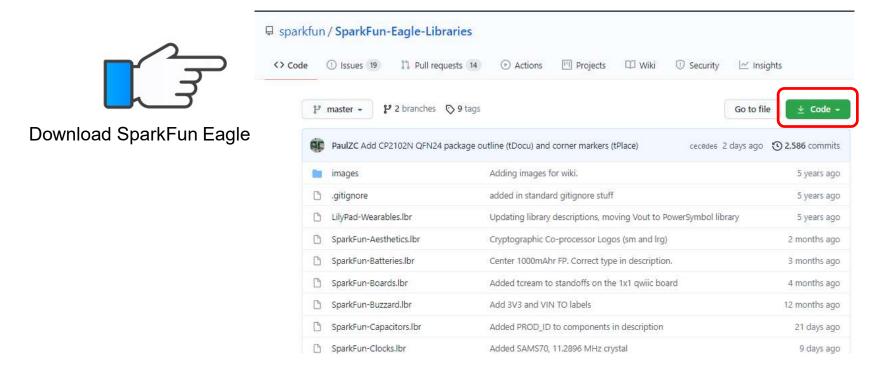


Chapter 6.5: Adding parts



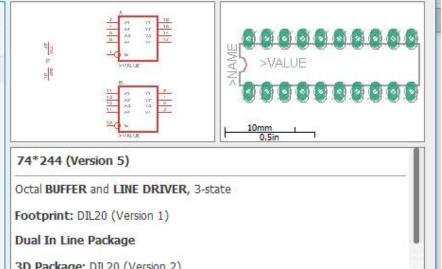
Chapter 6.5: Downloading library

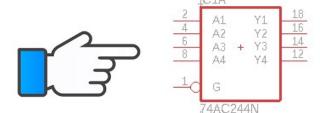
https://learn.sparkfun.com/tutorials/how-to-install-and-setup-eagle/using-the-sparkfun-libraries



Chapter 6.5: Hidden Pins

Name ^	Managed Folder	Description	
▼ 74*244	Province of Control	Octal BUFFER and LINE DF	# 64 17 36 # 77 18 36 # 77 36 # 73 37
74AC244DW	-	SO20W	1 AC YT NO AC
74AC244FK		LCC20	
74AC244N		DIL20	
74ACT244DW	A	S020W	
74ACT244FK		LCC20	
74ACT244N	A	DIL20	
74HC244DW	A	S020W	
74HC244FK		LCC20	74*244 (Version 5)
74HC244N	A	DIL20	
74HCT244DW	A	S020W	Octal BUFFER and LINE DRIVER, 3-state
74HCT244FK		LCC20	
74HCT244N	P	DIL20	Footprint: DIL20 (Version 1)
74LS244DW	A	S020W	
74LS244FK		LCC20	Dual In Line Package
74LS244N	4	DIL20	3D Package: DII 20 (Version 2)



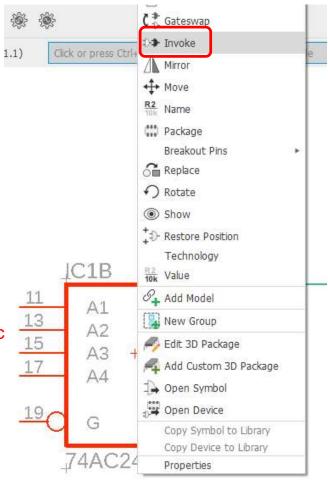


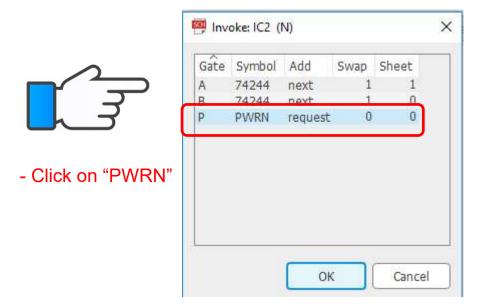
By default, there are no Vcc and Gnd pins shown up

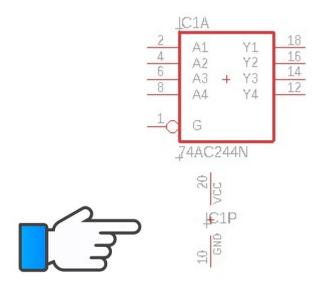


- Right Click on the IC schematic

- Click on "Invoke"







- Vcc and Gnd Pins are visible