

Arm #1 market share in Infrastructure!



Top-of-Rack Switches



Cellular Base Stations



Gateways

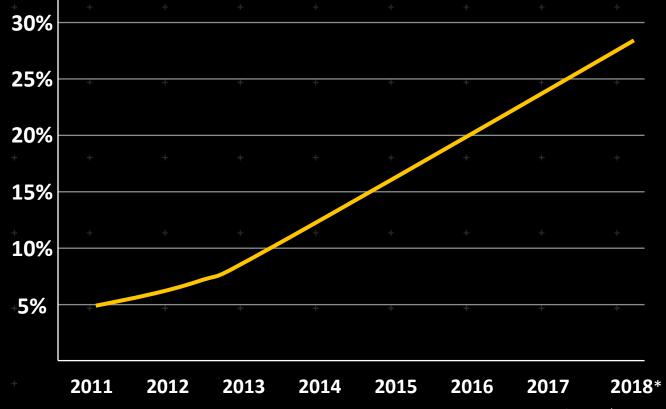


WAN Routers



Servers

Infrastructure Processor Unit Market Share



Source: IDC and Arm *thru 6/2018



What is Arm Neoverse?

What?



- A new vision the cloud to edge infrastructure foundation for a word of 1T intelligent devices.
- A new brand dedicated to infrastructure, a new brand as Arm Cortex.
- A new roadmap of processors dedicated to infrastructure and achieving 30 percent annual system performance gains
- Broad ecosystem support from microarchitecture to hardware, software, tools, and services

Why?



 To enable a new cloud to edge infrastructure designed to support the demands of a trillion intelligent devices

The Cloud to Edge Infrastructure Foundation for a World of 1T Intelligent Devices

High Performance, Secure IP and Architectures

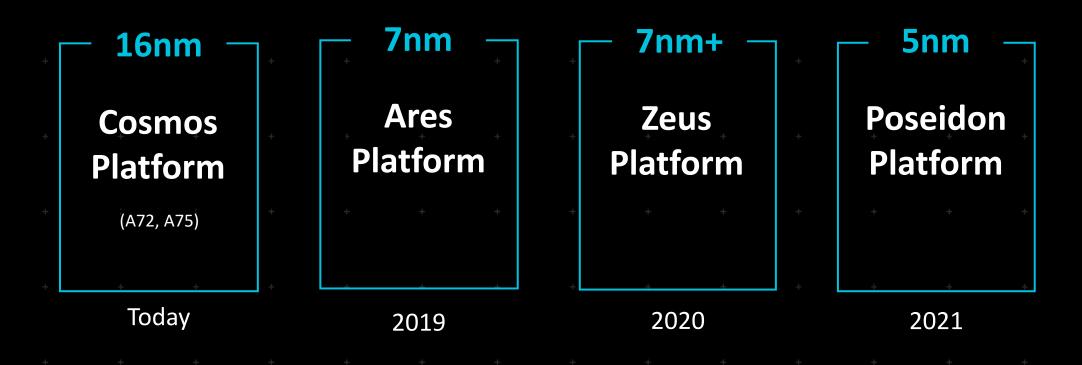
Diverse Solutions and Ecosystem

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Arm Neoverse Infrastructure IP roadmap leverages world-class process technology



~30% per Gen Faster Performance & New Features

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Broad SoC system design options within Arm Ecosystem

High performance CPUs **Arm Architectural design Arm IP** Data plane CPUs **CMN Fabric** Custom Arm High performance CPU Other IP **Custom Fabric & IP Accelerators** ML, on-die FPGA Networking, security, encryption Video, Custom Memory DDR, HBM, Flash, Storage Class memory 10 PCIe, CCIX, 100G+ ethernet **Foundry** TSMC 7FF, Samsung 7LPP, UMC **Common Software Platform and Ecosystem** Arm Architecture v8.x-A

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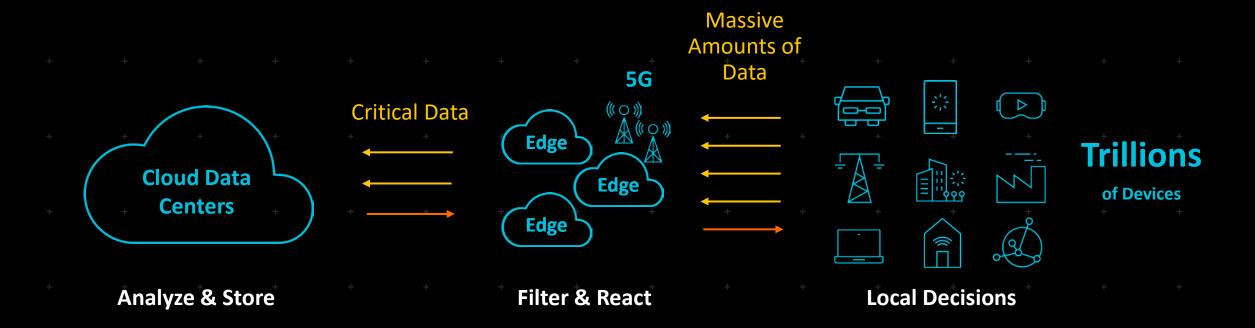
Diverse Solutions and Ecosystem



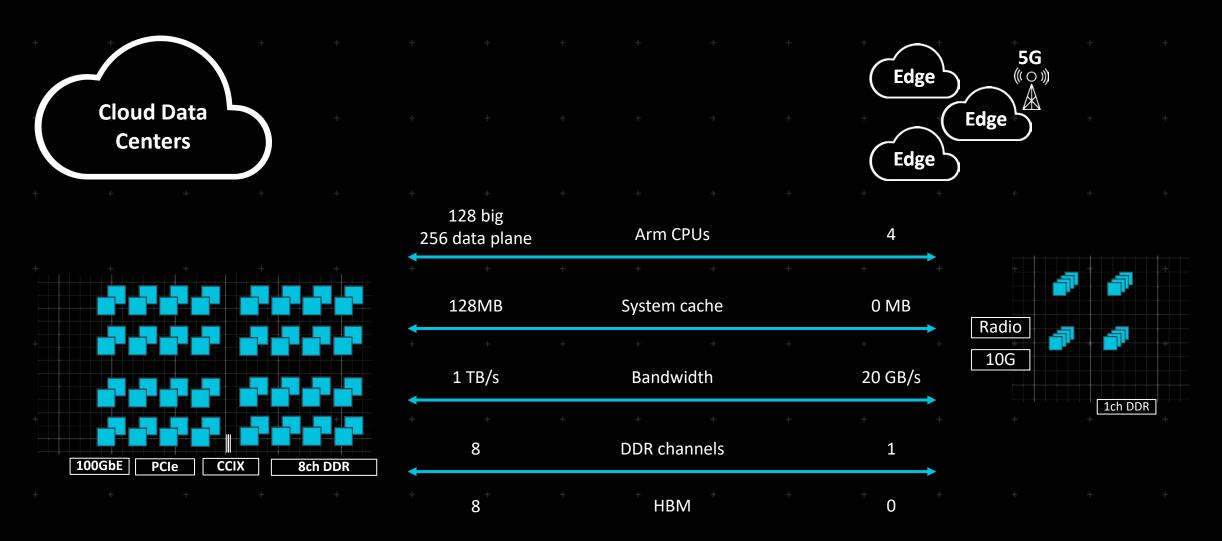
Media content distribution and consumption drive today's Internet infrastructure design



Data consumption is driving future designs



Scalable system solutions from cloud to edge



Edge compute solutions

Arm enables custom acceleration

Perf/W enables more compute at the edge

Scalable system

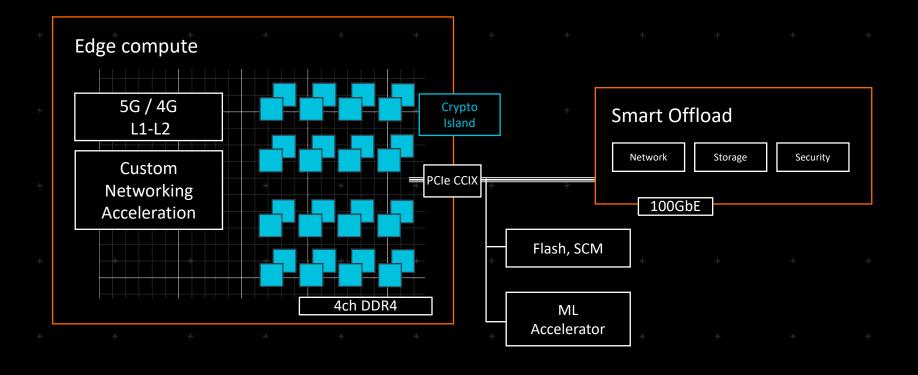
4x4 mesh

32 CPU

32MB system cache

Workloads

Virtual network functions, flexible orchestration



Cloud compute solutions

Arm enables more performance per socket 48, 64, 96 Arm cores per socket

Scalable system

8x8 1TB/s mesh

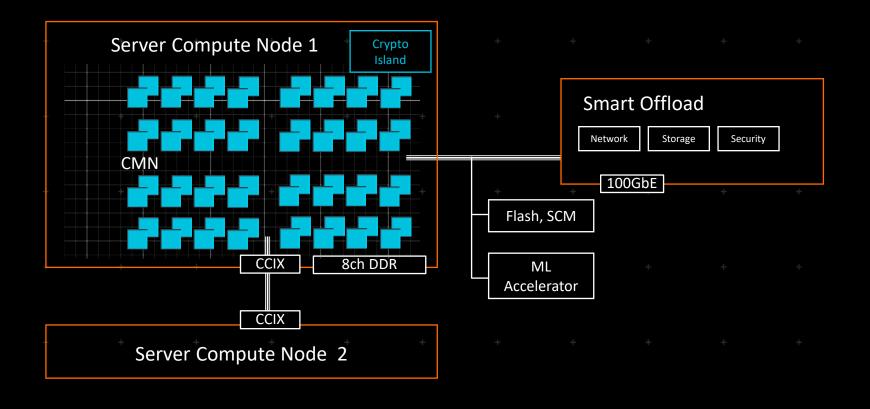
48, 64, 96 CPU

64MB system cache

Multi-socket

Workloads

Java, NGINX, Docker, Kubernetes, DB, Hadoop, Ceph, etc.



Networking, storage, & security accelerated processing

Scalable system

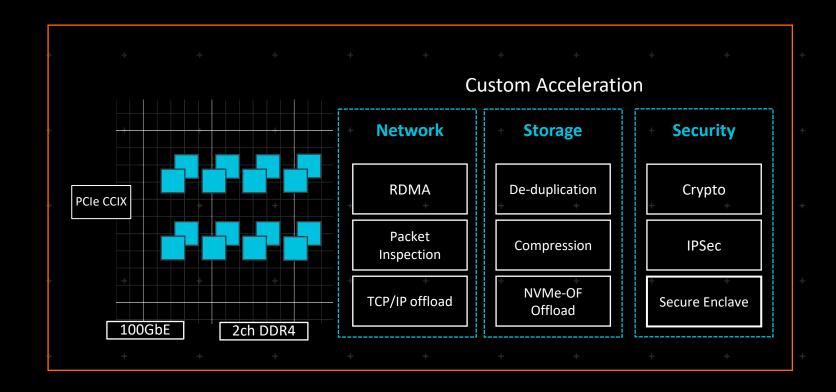
2x4 to 4x4 mesh

16~24 CPU

16+ MB system cache

Workloads

OVS, IPSec, RoCE, NFV, software defined network/storage



*Endorsed by a robust ecosystem

Silicon +	Cloud,	Platforms	Operators
tsinc BROADCOM	annapurnalabs an amagon company	Hewlett Packard CISCO ERICSSON	SoftBank Ovodafone
AMPERE HISILICON Mellanox SAMSUNG	Alibaba.com	⁺NOKIA * ZTE¢∺ Inventec	orange Sprint
Qualcom FUITSU MEDIATEK FLORAL	Bai de 百度	Uiと创新科 GIGABYTE PEGATRON	
★ XILINX* NXP achronix HuaXinTong 华市通	packet	Telco Systems A BATH COMPANY	
← BLUWIRELESS Cādence SYNOPSYS* Mentor*	+ + + +		
	+ + + +		

arm NEOVERSE

Endorsed by a broad and growing open networking and server ecosystem

Operating Systems	& Virtualization	Language & Libraries	Dev Tools & Resources	Open Source Projects Server & Networking
redhat SUSE ORACLE CANONICAL	docker wwware project openstack kubernetes ceph	OpenJDK =GO ♣ python*	shippable (Codefresh Works on arm	Linaro LIFNETWORKING CLOUD NATIVE COMPUTING FOUNDATION
	+ + + +	+ + + +	+ + +	+ + +

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