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Guoyi LOU

Education	Shanghai Jiao Tong University (SJTU), Shanghai, P.R.China	Sep 2013 - Aug 2017 (Expected)
	University of Michigan - Shanghai Jiao Tong University Joint Institute (UM-SJTU JI) Bachelor of Engineering in Electrical & Computer Engineering Overall GPA: 3.36/4.0 , Major Ranking: 33/102	
	The University of Hong Kong, Hong Kong SAR, China	Jan 2016 - May 2016
	Undergraduate exchange program, Department of Computer Science	
Work Experience	NSG Intern, Intel, Shanghai, P.R.China	Oct. 2016 - Aug 2017
	<ul style="list-style-type: none">Worked at <i>Non-volatile Solution Group</i> division.Studied and applied <i>Groovy</i> scripts for hard drive tests automation.Translated technical documents from Chinese to English.	
	ERS Intern, Deloitte, Shanghai, P.R.China	Jan. 2015 - Mar 2015
	<ul style="list-style-type: none">Served at <i>Enterprise Risk Services</i> department.Documented medical sales representatives' work for medicines companies.Performed payment reviews for travel agencies.	
Team Projects	Capstone Design, UM-SJTU JI, SJTU, Shanghai, P.R.China	Sep 2016 - Dec 2016
	Intel, UM-SJTU JI cooperating project	
	<ul style="list-style-type: none">Deployed a <i>Continuous Integration</i> server for automated testing using <i>Jenkins</i>.Designed data structure for test results to be stored in <i>Mysql</i> database.Wrote <i>Bash</i> scripts to push test results to users by Email.	
	Python Chatroom Application, ComSc, HKU, Hong Kong SAR, P.R.China	Jan 2016 - May 2016
	Course: Computer Networks (CSIS0234) project	
	<ul style="list-style-type: none">Implemented client-server chatroom application based on socket programming in <i>Python</i> that runs on <i>MacOSX</i>.	
	CPU Simulation, UM-SJTU JI, SJTU, Shanghai, P.R.China	Sep 2015 - Dec 2015
	Course: Intro to Computer Organization (VE370) project	
	<ul style="list-style-type: none">Implemented single cycle & pipelined MIPS architecture CPU using <i>Verilog HDL</i>.Solved all text book hazard problems using forwarding and hazard prediction techniques.Simulated <i>Verilog HDL</i> implementations on FPGA board.	
	Robotic Racecar, SJTU, Shanghai, P.R.China	Sept 2013 - Dec 2013
	Introduction to Engineering Project	
	<ul style="list-style-type: none">Designed the baseboard for the robotic car using <i>AutoCAD</i>.Perfected and calibrated the parameters for the sensors in <i>Arduino</i> programmes.Ranked <i>2nd</i> place in the final competition.	
Teaching Experience	Teaching Assistant, UM-SJTU JI, SJTU, Shanghai, P.R.China	Sep 2015 - Dec 2016
	<ul style="list-style-type: none">Worked as a teaching assistant in three courses: Academic Writing II (VY200), Technical Communications (VE300) and Introduction to Political Theories (VR101).Graded essays & homework and proctored exams.Held office hours in English.	
Selected Honors	Fung Scholarship, Victor and William Fung Foundation (1% in SJTU)	Dec 2015
	Academic Excellence Scholarship (10% in SJTU)	Oct 2015
	Dean's List, UM-SJTU JI (GPA > 3.5)	Fall Freshman, Fall & Summer Junior
English Tests	GRE, 325/340 + 3.0/6.0	Feb 2016
	TOEFL, 110/120	Sept 2016
Computer Skills	Operating Systems: Linux Ubuntu, MacOSX, Minix3 Computer Languages: C, C++, Python, Matlab, MIPS Assembly, Verilog HDL, L ^A T _E X	
Athletics	Third Place Freshmen Basketball League, SJTU, Shanghai, P.R.China	Nov 2013