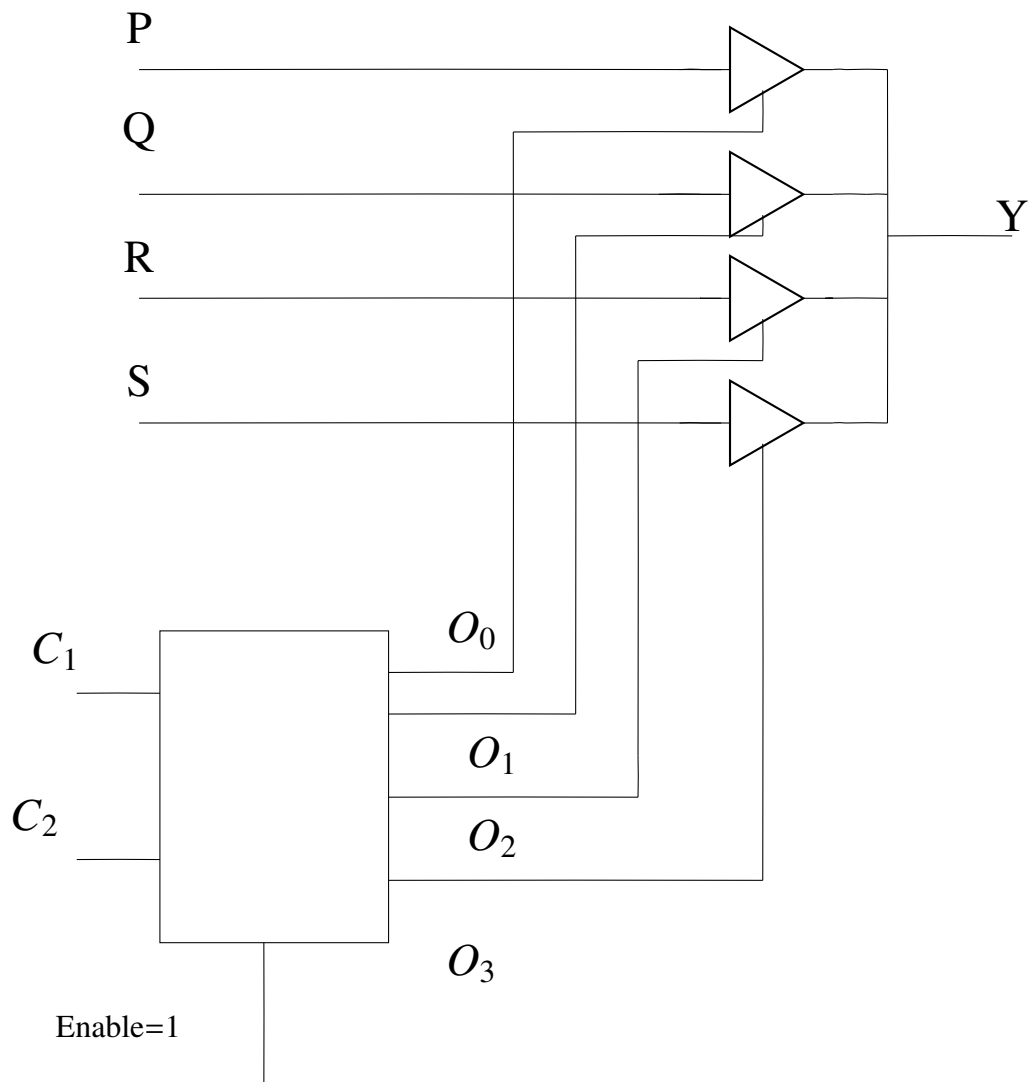


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The functionality implemented by the circuit below is



- A. 2-to-1 multiplexer
- B. 4-to-1 multiplexer
- C. 7-to-1 multiplexer
- D. 6-to-1 multiplexer