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Theory & Applications (FICTA)

16 - 17 September , 2016

Organised by
School of Computer Engineering

Bhubaneswar, Odisha, India

DAY-I Friday, 16 th September, 2016	
10:00 am to 10:45 am	Keynote Lecture-I
10:45 am to 11:30 am	Keynote Lecture-II
11:30 am to 12:00 pm	Tea Break
12 00 pm to 12:45 pm	Keynote Lecture-III
01:00 pm to 02:00 pm	Lunch Break
02:15 pm to 05:30 pm	Parallel Paper Presentation Tracks
05:30 pm to 06:30 pm	Inauguration Ceremony
06:30 pm to 07:30 pm	Cultural Evening
07:30 pm	Banquet Dinner
DAY-II	
Saturday, 17 th September, 2016	
09:30 am to 11:15 am	Parallel Paper Presentation Tracks
11:15 am to 11:30 am	Tea Break
11:30 am to 01:00 pm	Parallel Paper Presentation Tracks
01: 00 pm to 02:00 pm	Lunch Break
02:30 pm onwards	Social Networking and Local City Tour

V. Important (Please Note)

- 1. The Authors presentation slots are OPEN. Authors may choose their slots on FIRST COME FIRST SERVE basis for Day-I or Day-II at the time of registration only. (Please do not mail/call for prior reservation of presentation slots)
- 2. Kindly be informed that Certificates for all slots of Day-I will be distributed during Inauguration by the Chief Guest. No requests will be entertained to give the certificates before inauguration ceremony. Hence, it is being pre-informed that kindly plan your travel accordingly. However, certificates of all slots of Day-II will be distributed in the session (presentation venue) itself.
- 3. Many Hotels (economy and Luxury types) are available in Bhubaneswar. Online booking can be done by authors. We are trying to contact few Guest Houses /Hotels near the Venue. We will keep you updated for the same as and when it is available with us.