# GLOBALFOUNDRIES

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Senior Engineer Tapeout Engineer/ Software Development Life cycle

**Company** Name: **Nature Of Business:** 

Service Semiconductor Foundry

Tentative Job Location:

Designation

Company:

Bangalore

### **Summary of Role:**

GLOBALFOUNDRIES is looking for a highly motivated software engineer to work in the GSO team located in Bangalore, India. This is an excellent opportunity for a software engineer to start his or her professional career with a world leading Semiconductor Technology Company.

The Global Shuttle MPW team enables customers around the globe to prototype their chip designs on Multi Project Wafers (MPWs) and gives all required support to them to make their silicon prototype successful and eventually turn it into a product manufactured at GLOBALFOUNDRIES. MPW runs are offered by GLOBALFOUNDRIES for a large variety of technologies, from leading edge FinFet to State-of-the-Art BiCMOS, RF and Silicon Photonics, manufactured in Fabs located in Singapore, US and Germany.

The Global Shuttle MPW team belongs to GLOBALFOUNDRIES' Pre-and Post Fab Operations and is a global team with offices in the U.S., Singapore, India and Germany.

This role requires a close working relationship with the engineers that use these processes to deliver qualified designs.

# Essential Responsibilities: \_\_

- Modify existing and develop new software infrastructureImprove existing and develop new end-to-end workflows for Global Shuttle MPW program management, MPW Project execution and assembly
- Interact with the team and reach out to other teams such as design teams, mask teams, and data mask data prep teams to obtain information on functional requirements and priorities for new features of GSO's workflows
- Maintain detailed application documentation and revision control Contribute to multiple projects and expert in test strategies

# **Description:**

#### **Skills and Abilities:**

- Be comfortable working through all stages of the software development life-cycle from requirements, development, test and deployment using Agile disciplines
- Work in a dynamic collaborative environment requiring strong teaming skills with programmers, engineers, managers, and production employees
- Strong ability to learn and explore new technologies and who are able to demonstrate good analysis and problem solving skills are preferred

Ability to develop critical functionalities consistently and in a timely manner

## **Required Qualifications:**

- Master's Degree in Software Engineering, Computer Science, Electrical Engineering or equivalent with multiple years of professional experience.
- UNIX based script development and WEB programming skills.
- Expert in one of TcL, Python, XML, Javascript, basic knowledge in the other languages.
- Strong interpersonal skills and ability to work effectively in cross-functional teams and with different cultures across different sites.
- Language Fluency English (Written & Verbal).Good verbal and written communication skills

# **Preferred Qualifications:**

- Semiconductor experience in a foundry environment, preferably in an integration/process or tapeout execution role.
- Detail-oriented with first-time right quality output

#### Program AE BSBE CE CHE CSE EE ES ME MSE PHY CHM MTH ECO DES IME CGS HSS EEM MSP NET PSE Stats BT No No No No No No -- No No -- No -- --BS No No No No MT No No No No Yes Yes No No No No No No No No DoubleMajor No No No No No No -- No No No No No No dual No No No No Yes Yes No No No No No No No **Eligibilty:** dualB No No No No Yes Yes -- No dualC No No Yes Yes No Mdes No MBA No No No Phd No No No No Yes Yes No No No No No No No Nο No Nο Nο Nο No Nο MSc No Nο No No MSR No No No No Yes Yes -- No No No No

Cost to Company:

For Master's- CTC: 1288772 INR

**Package** Following is the break up for Master's: **Details:** Base Pay- 1100000, AIP- 110000, Gratuity- 22540, PF- 56232 Bond: **False CPI CutOff:** 0.0Medical Requirments Medical Insurance is provided by the company Resume True **Shortlist:** Resume N/A **Shortlist** Criteria: **Aptitude** False Test: Group False **Discussion: Technical False Test:** Technical True Interview: **Technical** N/A Interview **Duration:** Number of **Techincal** 3 Interview **Rounds:** HR True Interview: HR Interview N/A

Duration: Additional Information: