

Company :
Company Name :
Nature Of Business :
Designation :
Tentative Job Location :

TVS Motor Company Ltd.
TVS Motor Company Ltd.

Design Engineer - Power Electronics and Drives

Hosur

Job Description

Coding with VHDL/Verilog and graphical tools for FPGA platforms.

1. Study and understand requirements from design inputs

2. Comparative evaluation of various fpga modules and selection based on that.

3. Implementation of peripherals such as ADC, customized pwm etc on fpga

4. Communication between fpga and microprocessor

5. C code development for microprocessor to communicate with FPGA.

6. Implementation of closed loop control of various complexity.

Description :

| Program | AE | BS | BE | CE | CHE | CSE | EE | ES | ME | MSE | PHY | CHM | MTH | ECO | DES | IME | CGS | HSS | EEM | MSP | NET | PSE | Stats |
|-------------|-----|-----|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-------|
| BT | No | No | | No | No | No | No | -- | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| BS | -- | -- | | -- | -- | -- | -- | No | -- | -- | No | No | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| MT | Yes | Yes | | Yes | Yes | Yes | Yes | Yes | Yes | Yes | -- | -- | -- | -- | -- | Yes | -- | -- | Yes | Yes | Yes | Yes | -- |
| DoubleMajor | No | No | | No | No | No | No | -- | No | No | No | No | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| dual | Yes | Yes | | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| dualB | Yes | Yes | | Yes | Yes | Yes | Yes | -- | Yes | Yes | Yes | Yes | Yes | Yes | -- | Yes | -- | -- | Yes | -- | Yes | Yes | -- |
| dualC | Yes | Yes | | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | Yes | -- | -- | -- | -- | -- | -- | -- |
| Mdes | -- | -- | | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | No | -- | -- | -- | -- | -- | -- | -- | -- |
| MBA | -- | -- | | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | No | -- | -- | -- | -- | -- | -- | -- |
| Phd | No | No | | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No |
| MSc | -- | -- | | -- | -- | -- | -- | -- | -- | -- | Yes | Yes | Yes | -- | -- | -- | -- | -- | -- | -- | -- | -- | Yes |
| MSR | Yes | Yes | | Yes | Yes | Yes | Yes | -- | Yes | Yes | -- | -- | -- | -- | -- | -- | Yes | -- | -- | -- | -- | Yes | -- |

Cost to Company :
Package Details :
Bond :
CPI CutOff :
Medical Requirments :
Resume Shortlist :
Resume Shortlist Criteria:
Aptitude Test:
Group Discussion:
Technical Test:
Technical Test Duration:
Technical Interview:
Technical Interview Duration:
Number of Techincal Interview Rounds:
HR Interview:
HR Interview Duration:

9 LPA
9 LPA
False
0.0

True

N/A

False

False

True

90min

True

45min

2

True

30min

**Additional
Information:**

Minimum 60% thru out their academics (from - till date) No standing arrears (till date)