Company :	**Silicon Labs**
Company Name :	**Silicon Labs**
Nature Of Business :	**Semiconductors**
Designation :	**Design Engineer**
Tentative Job Location :	Hyderabad

Description :

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Design Engineer & Software Engineer

We are Silicon Labs. We are the leading provider of silicon, software and solutions for a smarter, more connected world.

We hire the most innovative talent in the world through professional and university channels and then solve the industry's toughest problems, providing our customers with significant advantages in performance, energy savings, connectivity and design simplicity. Silicon Labs' engineering teams create solutions for customers in diverse markets including Internet of Things, internet infrastructure, industrial automation, consumer and automotive. We take pride in our products and in our people, and that's one of the many reasons we continue to be awarded *Most Respected Public Semiconductor Company*. Learn more at www.silabs.com/careers

At Silicon Labs, new college grads have the opportunity to dive into a wide variety of projects across disciplines and make a real impact in an environment that encourages learning and growth. We've developed a reputation as a great place to work - come launch your career with us!

B.Tech EE, including ECE, EIE and related streams

M.Tech Microelectronics, VLSI, RF Design, and related streams

Areas of work:

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- **Digital chip design and verification, chip engineering including front-end and back-end activities**
- **Processor architecture and design**
- **Signal processing and wireless modem design of next gen Wi-Fi, BT, 5G**
- **Analog and mixed signal chip design, including circuit design and layout**
- **CMOS RF transceiver and power amplifier development up to 6 GHz**
- **High performance RF systems design and validation**
- **IoT applications development and engineering**
- **Embedded software and IoT wireless protocol development**

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B.Tech or M.Tech CS

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- **Embedded software architecture**
- **Networking, IoT, wireless protocol embedded software development**
- **Device drivers, RTOS porting and applications**
- **SQA and automation**
- **IoT analytics**
- **UX design**
- **IoT applications development and engineering**

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Any EE or CS branch

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- **Product marketing, product management and GTM initiatives of high technology products**
- **Programme management of high technology product development**

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SoC/Chip Design Engineer/Verification Engineer

This position requires knowledge on SoC architectures, multi core architectures, memory architectures, Caches. Apart from this, knowledge of 3 or more of the below is mandatory:

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- **Strong fundamentals in digital electronics (digital logic design /Switching theory and logic design)**
- **Fundamentals knowledge of digital circuit design with exposure to design using hardware description languages such as HDL and Verilog**
- **Functional simulations**
- **Logic synthesis**
- **Knowledge on Verilog and System Verilog**
- **Knowledge on Verification Methodologies**
- **Knowledge on peripheral interfaces like SPI, I2S, I2C, UART**
- **Knowledge on Verification Methodologies (UVM)**
- **Testbench development**
- **Knowledge on scripting including TCL/Shell/Perl/Python**

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Good to have: Protocols Knowledge of Wi-Fi, Bluetooth, Zigbee

Baseband/Modem Design - M.Tech (or dual degree) / MS / PhD

Background and skills in the following areas at the level of M. Tech or above is mandatory

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- **Communication theory**
- **Signal processing**
- **Wireless communications concepts**
- **Digital design**
- **Matlab coding**
- **Knowledge or experience in the following is a plus**
- **OFDM**
- **Verilog implementation**

Analog/RF design

- **Knowledge of Analog, mixed signal or RF design**
- **Course in field of MOS/CMOS/device physics/analog design/PLLs/power management block and fundamentals**
- **Good know how of IC/Analog design tools**
- **Projects in field of Analog mixed signal / RF design is plus**
- **Candidate will be participating in all phases of product development including product definition, architecture, layout, analog circuit design and lab evaluations and debug.**

Eligibilty :

Program	AE	BSBE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
BT	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	--	--	--	--	--	--
BS	--	--	--	--	--	--	No	--	--	No	No	No	No	--	--	--	--	--	--	--	--	--
MT	No	No	No	No	Yes	Yes	No	No	No	--	--	--	--	--	No	--	--	No	No	No	Yes	--
DoubleMajor	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--
dual	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--
dualB	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	No	--	--	No	--	No	Yes	--
dualC	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	--	--	--	--	--	--	--
Mdes	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
Phd	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	No	No	No	No	No	Yes	No
MSc	--	--	--	--	--	--	--	--	--	No	No	No	--	--	--	--	--	--	--	--	--	No
MSR	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	No	--	--	--	--	Yes	--

Cost to Company :	1640000
Package Details :	1250000 Gross Fixed Cash 390000 Variable pay
Bond :	False
CPI CutOff :	0.0
Medical Requirments :	
Resume Shortlist :	True
Resume Shortlist Criteria:	N/A
Aptitude Test:	False
Group Discussion:	False
Technical Test:	True
Technical Test Duration:	N/A
Technical Interview:	True
Technical Interview Duration:	N/A
Number of Techincal Interview Rounds:	2
HR Interview:	False
Additional Information:	