

Company : Intel
Company Name : Intel
Nature Of Business : Core
Designation : Hardware Engineer
Tentative Job Location : Bangalore/ Hyderabad

Hardware Engineer (SoC / IP) JD:
We have roles in Digital and Analog domain
ï. Oversees definition, design, verification, and documentation for SoC (System on a Chip) development.
ï. Determines architecture design, logic design, and system simulation.
ï. Defines module interfaces/formats for simulation.
ï. Performs Logic design for integration of cell libraries, functional units and sub-systems into SoC full chip designs. Register Transfer Level coding, and simulation for SoCs.
ï. Contributes to the development of multidimensional designs involving the layout of complex integrated circuits.
ï. Performs all aspects of the SoC design flow from high-level design to synthesis, place and route, timing and power to create a design database that is ready for manufacturing.
ï. Analyzes equipment to establish operation infrastructure, conducts experimental tests, and evaluates results.
ï. May also review vendor capability to support development.
ï. He/She should be willing to ramp-up quickly on required areas like RTL, Verification/Validation, Structural Design / Physical Design, Analog, Firmware, Embedded etc.

Program	AE	BSBE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
BT	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	--	--	--	--	--	--
BS	--	--	--	--	--	--	No	--	--	No	No	No	No	--	--	--	--	--	--	--	--	--
MT	No	No	No	No	Yes	Yes	No	No	No	--	--	--	--	--	No	--	--	No	No	No	Yes	--
DoubleMajor	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--
dual	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--
dualB	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	No	--	--	No	--	No	No	--
dualC	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	--	--	--	--	--	--	--
Mdes	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--
Phd	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No
MSc	--	--	--	--	--	--	--	--	--	No	No	No	--	--	--	--	--	--	--	--	--	No
MSR	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	No	--	--	--	--	Yes	--

IITs		
CG Hirig 2020	Btech	Mtech
Base	8.55	10.91
Base + Bonus + Retirals + SPP	10.05	12.91
Special Allowance	0.22	0.22
HB (in 2 installments)	3.5	4.5
Relocation (tax protected)	1	1
Other Benefits (Insurance premiums +Transportation)	1.22	1.22
Total CTC	15.99	19.85
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Bond : False
CPI CutOff : 0.0
Medical Requirments :
Resume

Shortlist :	True
Resume Shortlist Criteria:	N/A
Aptitude Test:	False
Group Discussion:	False
Technical Test:	True
Technical Test Duration:	90 mins
Technical Interview:	True
Technical Interview Duration:	45
Number of Techincal Interview Rounds:	2
HR Interview:	True
HR Interview Duration:	30
Additional Information:	