Company :	**KLA Tencor Software India Pvt Ltd**
Company Name :	**KLA Tencor Software India Pvt Ltd**
Nature Of Business :	**Product Engineering**
Designation :	**Algorithm Engineer (AI/ML)**
Tentative Job Location :	Chennai

Algorithm Development Engineering – AI/ML Group

KLA-Tencor - Chennai, India

KLA Overview:

Calling the adventurers ready to join a company that's pushing the limits of nanotechnology to keep the digital revolution rolling. At KLA, we're making technology advancements that are bigger—and tinier—than the world has ever seen.

Who are we? We research, develop, and manufacture the world's most advanced inspection and measurement equipment for the semiconductor and nanoelectronics industries. We enable the digital age by pushing the boundaries of technology, creating tools capable of finding defects smaller than a wavelength of visible light. We create smarter processes so that technology leaders can manufacture high-performance chips—the kind in that phone in your pocket, the tablet on your desk and nearly every electronic device you own—faster and better. We're passionate about creating solutions that drive progress and help people do what wouldn't be possible without us. The future is calling. Will you answer?

Key Responsibilities:

KLA is seeking applications for an Algorithm engineer in EBEAM division for developing next generation technologies in yield management for Semi-Conductor industry. This position involves developing advanced Image Processing and Machine Vision algorithms for the wafer Inspection, Review and Metrology products. We are one of the pioneering companies in the Semi-Conductor industry to use Deep Learning algorithms for defect detection and classification. In addition, we are actively exploring using Generative models (GAN) for SEM image simulation and image quality enhancement.

Description :

- This position involves developing advanced image processing algorithms for the wafer inspection and review.
- Designs and develops cutting edge image processing and machine learning algorithms.
- Implement system level solutions to push the limits of sensitivity on semiconductor inspection & review systems.
- Designs and implementations are always constrained to operate with the boundaries of robustness and stability.
- Real time requirements pose additional challenges and works extensively on providing solutions to live customer problems in the field.
- This position is unique as it interfaces with several system level groups - Optics, mechanics, sensors, and applications among others.
- The opportunities are boundless in the technical areas as we are constantly pushing the envelope of leading-edge technologies in algorithms and image computers.
- System level solutions for these complex automated inspection platforms. We are constantly innovating and releasing products every few months

Preferred Qualifications

- New college graduates in any of the following degree: Ph.D., Dual Degree, Masters, or Bachelors. (Candidates from EE, CS and Mathematics background would be preferred)
- The ideal candidate will have a working knowledge of Machine Vision.
- Knowledge of Deep Learning with working experience in Tensor Flow, Theano, PyTorch etc.
- Ability to formulate problems into mathematical equations.
- Proficiency in computational methods, C++, and object-oriented design.
- Team player with good written and verbal communication skills.

Eligibilty :

Program	AE	BSBE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
BT	No	No	No	No	Yes	Yes	--	Yes	No	--	--	--	--	--	--	--	--	--	--	--	--	--
BS	--	--	--	--	--	--	No	--	--	Yes	No	Yes	No	--	--	--	--	--	--	--	--	--
MT	No	No	No	No	Yes	Yes	No	Yes	No	--	--	--	--	--	No	--	--	No	No	No	No	--
DoubleMajor	No	No	No	No	Yes	Yes	--	Yes	No	Yes	No	Yes	No	--	--	--	--	--	--	--	--	--
dual	No	No	No	No	Yes	Yes	No	Yes	No	Yes	No	Yes	No	--	--	--	--	--	--	--	--	--
dualB	No	No	No	No	Yes	Yes	--	Yes	No	Yes	No	Yes	No	--	No	--	--	No	--	No	No	--
dualC	No	No	No	No	Yes	Yes	No	Yes	No	Yes	No	Yes	No	No	No	--	--	--	--	--	--	--
Mdes	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
MBA	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--	--
Phd	No	No	No	No	Yes	Yes	No	Yes	No	Yes	No	Yes	No	No	No	No	No	No	No	No	No	No
MSc	--	--	--	--	--	--	--	--	--	Yes	No	Yes	--	--	--	--	--	--	--	--	--	No
MSR	No	No	No	No	Yes	Yes	--	Yes	No	--	--	--	--	--	--	No	--	--	--	--	No	--

Bachelors: INR 2124542 + Hiring Bonus INR 200000

Cost to Company :

Master's/DD: INR 2341728 + Hiring Bonus INR 200000

PhD: INR 3075284 + Hiring Bonus INR 200000

Package Details :

Heads	Btech	Masters/DD	PhD
Basic	1700000	1800000	2200000
Bonus	170000	180000	330000
LTI	122916	218833	368583
Other Benefits	131626	142895	176701
Total CTC	2124542	2341728	3075284
One time Hiring Bonus	200000	200000	200000

LTI is for 3 years paid in 3 installments (only 1st year is included in annual CTC above)

Bachelors: INR 368750

Masters: INR 656500

Bachelors: INR 1105750

Bond :	**False**
CPI CutOff :	**0.0**
Medical Requirments :	**No**
Resume Shortlist :	**False**
Aptitude Test:	**False**
Group Discussion:	**False**
Technical Test:	**True**
Technical Test Duration:	**60 Mins**
Technical Interview:	**True**
Technical Interview Duration:	**120 Mins**
Number of Techincal Interview Rounds:	**2**
HR Interview:	**True**
HR Interview Duration:	**30 Mins**
Additional Information:	