

Company : Taiwan Semiconductor Manufacturing Company
Company Name : Taiwan Semiconductor Manufacturing Company
Nature Of Business : Semiconductor
Designation : Chip Design Engineer
Tentative Job Location : Hsinchu City, Taiwan

Responsibilities
 1. Block level and top level Digital Design implementation from RTL to Physical Design, including Synthesis, DFT, DV, floorplan, CTS, P&R, Performance/Power/Area Optimization, Timing Closure, EM/IR and PV fixes, design signoff
 2. design and sign-off methodology development and EDA flow enhancement
 3. R&D, innovation and path-finding for most advanced process challenges
 4. Quality infrastructure development and project/program management
 5. Self-motivated, strong ownership and commitment, quick learner, and good teamwork

Description :

Requirements
 1.Familiar with process fabrication, CAD, algorithm, EDA tool, APR, timing, IR/EM, low power, physical verification, device modeling , VLSI chip implementation or 3DIC
 2. Innovative and good problem solving skills
 3 Willing to learn all aspects of chip design and come up with new solutions
 4. Familiar with AI/ML technology is a plus
 5. Familiar with Script languages (shell, python, perl, TCL)

| | Program | AE | BS | BE | CE | CHE | CSE | EE | ES | ME | MSE | PHY | CHM | MTH | ECO | DES | IME | CGS | HSS | EEM | MSP | NET | PSE | Stats |
|--------------------------|----------------------|----------------------|----|----|----|-----|-----|-----|----|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-------|
| Eligibility : | BT | No | No | | No | No | No | No | -- | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| | BS | -- | -- | | -- | -- | -- | -- | No | -- | -- | No | No | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| | MT | No | No | | No | No | Yes | Yes | No | No | Yes | -- | -- | -- | -- | -- | No | -- | -- | No | Yes | No | Yes | -- |
| | DoubleMajor | No | No | | No | No | No | No | -- | No | No | No | No | No | No | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| | dual | No | No | | No | No | Yes | Yes | No | No | Yes | No | No | Yes | No | -- | -- | -- | -- | -- | -- | -- | -- | -- |
| | dualB | No | No | | No | No | Yes | Yes | -- | No | Yes | No | No | Yes | No | -- | No | -- | -- | No | -- | No | Yes | -- |
| | dualC | No | No | | No | No | Yes | Yes | No | No | Yes | No | No | Yes | No | No | No | -- | -- | -- | -- | -- | -- | -- |
| | Mdes | -- | -- | | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | No | -- | -- | -- | -- | -- | -- | -- | -- |
| | MBA | -- | -- | | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | -- | No | -- | -- | -- | -- | -- | -- | -- |
| | Phd | No | No | | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No | No |
| | MSc | -- | -- | | -- | -- | -- | -- | -- | -- | -- | No | No | Yes | -- | -- | -- | -- | -- | -- | -- | -- | -- | Yes |
| | MSR | No | No | | No | No | Yes | Yes | -- | No | Yes | -- | -- | -- | -- | -- | -- | No | -- | -- | -- | -- | Yes | -- |
| Cost to Company : | Item (Annual) | Master Degree | | | | | | | | | | | | | | | | | | | | | | |
| | GROSS | \$ 49,813 | | | | | | | | | | | | | | | | | | | | | | |
| | CTC | \$ 58,913 | | | | | | | | | | | | | | | | | | | | | | |

Currency: USD
 1. Including 12 months base pay, 2 months Year-end bonus, 18 months Profit Sharing, Design Incentive Bonus, 2 months relocation bonus, 4-6 months sign on bonus.
 2. Base Salary, Year-end bonus, Design Incentive Bonus are fixed and recurring. Profit Sharing is minimum amount for an average performance (i.e. top 50%) and recurring. Relocation and sign on bonus are one shot paid after onboard.

Package Details :

Bond : True
CPI CutOff : 0.0
Bond Details : 24 months
Medical Requirments :
Resume Shortlist : True
Resume Shortlist Criteria: N/A
Aptitude Test: False
Group Discussion: False
Technical Test: False
Technical Interview: True
Technical Interview Duration: 60 mins
Number of

**Technical
Interview
Rounds:
HR
Interview:
HR
Interview
Duration:
Additional
Information:**

1

True

60 mins