Company :	**Silicon Labs**
Company Name :	**Silicon Labs**
Nature Of Business :	**Semiconductors**
Designation :	**Validation Engineer**
Tentative Job Location :	Hyderabad

We are Silicon Labs. We are the leading provider of silicon, software and solutions for a smarter, more connected world.
We hire the most innovative talent in the world through professional and university channels and then solve the industry's toughest problems, providing our customers with significant advantages in performance, energy savings, connectivity and design simplicity. Silicon Labs' engineering teams create solutions for customers in diverse markets including Internet of Things, internet infrastructure, industrial automation, consumer and automotive. We take pride in our products and in our people, and that's one of the many reasons we continue to be awarded Most Respected Public Semiconductor Company. Learn more at www.silabs.com/careers
At Silicon Labs, new college grads have the opportunity to dive into a wide variety of projects across disciplines and make a real impact in an environment that encourages learning and growth. We've developed
a reputation as a great place to work - come launch your career with us!
What We are Looking For?

Description :

Silicon Labs is seeking validation engineers for a new IoT MCU Validation team in Hyderabad, India. The ideal candidate is an enthusiastic engineer with higher education in electrical engineering or computer engineering or similar. The candidate should be passionate for technology and enjoy technical challenges ranging from software & firmware to hardware on both printed circuit board and microchip (integrated circuits) level. Test instrumentation, measurement techniques and tests automation are key areas of this position. It is also desirable with good understanding of low power MCU architecture, both digital and analog aspects.
Key responsibilities:
ï,· Test case implementation and automation
ï,· Test data management, analysis and presentation
ï,· Participate in debug sessions and provide measurement data for root cause analysis
ï,· FPGA prototyping and pre-silicon validation
ï,· Technical documentation authoring
ï,· Participate in cross functional technical reviews
ï,· Infrastructure development for our automated silicon characterization platform which consist of hardware, device/instruments drivers, software toolchains, frameworks, libraries and databases
ï,· Implement and develop work processes to scale efficiently and ensure high product quality
ï,· Close cooperation with existing IoT MCU Validation teams in other locations, with IC design engineers and SW/FW developers

Eligibilty :

Program	AE	BS	BE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
BT	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	--	--	--	--	--	--	--
BS	--	--	--	--	--	--	No	--	--	No	No	No	No	--	--	--	--	--	--	--	--	--	--
MT	No	No	No	No	Yes	Yes	No	No	No	--	--	--	--	--	No	--	--	No	No	No	--	Yes	--
DoubleMajor	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--	--
dual	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--	--
dualB	No	No	No	No	Yes	Yes	--	No	No	No	No	No	No	--	No	--	--	No	--	No	--	Yes	--
dualC	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	--	--	--	--	--	--	--	--
Mdes	--	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--	--
Phd	No	No	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	No	No	No	No	No	No	Yes	No
MSc	--	--	--	--	--	--	--	--	--	--	No	No	No	--	--	--	--	--	--	--	--	--	No
MSR	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	No	--	--	--	--	Yes	--

Cost to Company :	**1640000**
Package Details :	**1250000 Gross Fixed Pay** **390000 Variable Pay**
Bond :	**False**
CPI CutOff :	**0.0**
Medical Requirments :	
Resume Shortlist :	**True**
Resume Shortlist Criteria:	**N/A**
Aptitude Test:	**False**
Group Discussion:	**False**
Technical Test:	**True**

Technical Test Duration:	N/A
Technical Interview:	True
Technical Interview Duration:	N/A
Number of Techincal Interview Rounds:	2
HR Interview:	False
Additional Information:	