

Company :
Company Name :
Nature Of Business :
Designation :
Tentative Job Location :

GLOBALFOUNDRIES
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Service Semiconductor Foundry
Principal Product Development Engineer/ Design Engineer:
Bangalore

Summary of Role:
Work with the IP Development & Design team on the RTL design, F.E. verification & timing of IPs & test chips in various technology nodes.
Essential Responsibilities:
Use industry-standard design tools to perform RTL design, F.E. model development, verification and timing rules generation for IPs such as Standard Cell Logic libraries, IOs, and Efuse Macros & Memories.

Description :

- Requires a Masters OR PhD degree in Electronics
- Be able to collaborate with program and technical leads on multiple concurrent projects.
- Should have excellent problem-solving skills, written & oral communication, teaming & interpersonal skills
- Perform all activities in a safe and responsible manner and support all Environmental, Health, Safety & Security requirements and programs

Required Qualifications: __
Preferred Qualifications:

- Knowledge of the end-to-end IP design cycles.
- Should have good understanding of VLSI Design and CMOS technology

knowledge in RF technologies (Bulk, CMOS & SOI) process is desired.																								
Eligibility :	Program	AE	BS	BE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
	BT	No	No		No	No	No	No	--	No	No	--	--	--	--	--	--	--	--	--	--	--	--	--
	BS	--	--		--	--	--	--	No	--	--	No	No	No	No	--	--	--	--	--	--	--	--	--
	MT	No	No		No	No	No	Yes	No	No	Yes	--	--	--	--	--	No	--	--	No	No	No	No	--
	DoubleMajor	No	No		No	No	No	Yes	--	No	No	No	No	No	No	--	--	--	--	--	--	--	--	--
	dual	No	No		No	No	No	Yes	No	No	Yes	Yes	No	No	No	--	--	--	--	--	--	--	--	--
	dualB	No	No		No	No	No	Yes	--	No	Yes	Yes	No	No	No	--	No	--	--	No	--	No	No	--
	dualC	No	No		No	No	No	Yes	No	No	Yes	Yes	No	No	No	No	No	--	--	--	--	--	--	--
	Mdes	--	--		--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
	MBA	--	--		--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--
	Phd	No	No		No	No	No	Yes	No	No	Yes	Yes	No	No	No	No	No	No	No	No	No	No	No	No
	MSc	--	--		--	--	--	--	--	--	--	Yes	No	No	--	--	--	--	--	--	--	--	--	No
	MSR	No	No		No	No	No	Yes	--	No	Yes	--	--	--	--	--	--	No	--	--	--	--	No	--

Cost to Company :
Package Details :
Bond :
CPI CutOff :
Medical Requirments :
Resume Shortlist :
Resume Shortlist Criteria:
Aptitude Test:
Group Discussion:
Technical Test:
Technical Interview:
Technical

For PhDs- CTC: 2167480 INR
For Master's- CTC: 1288772 INR
Following is the break up for PhDs:
Base Pay- 1850000, AIP- 185000, Gratuity- 37908, PF- 94572
Following is the break up for Master's:
Base Pay- 1100000, AIP- 110000, Gratuity- 22540, PF- 56232
False
0.0
Medical Insurance is provided by the company
True
N/A
False
False
False
True

Interview Duration:	N/A
Number of Techincal Interview Rounds:	3
HR Interview:	True
HR Interview Duration:	N/A
Additional Information:	