Company :	**SLK SOFTWARE SERVICE PVT LTD**
Company Name :	**SLK SOFTWARE SERVICE PVT LTD**
Nature Of Business :	**IT Product and Serivices**
Designation :	**Project Engineer - AI Technologies**
Tentative Job Location :	Bangalore

Project Engineer Positions in AI Technologies

V-Labs is a member of SLK Group of companies focused on Artificial Intelligence (AI), Internet of Things (IoT) Platforms and Solutions. SLK Software is a leading software and services company with over 3500 employees in North America/Europe/India, over $150 Million Revenue and with over 25 clients in the Banking, Financial, Insurance and Manufacturing industries. SLK is focused on Software Development, Technology Services and Automation Solutions and in the process of diversifying its portfolio in the emerging technology areas like Artificial Intelligence, IoT and related areas. V-Labs was a set up in 2018, focused on driving innovation in AI and IoT based Integrated Hardware and Software Platforms and Solutions. V-Labs brings together Video, Image, Signal Processing, NLP and Sensor technologies for real-time, scalable, and robust solutions and applications in BFSI, Security, Surveillance, and authentication areas. The current 40+ member team consists of over 20 Masters and PhDs from top universities including MIT (USA), IISc, IITs and universities abroad. With initial round of successful technology development and client deployments, V-Labs is looking to expand in the following areas,

i. Computer Vision and Image Processing

ii. Natural Language Processing and Voice Analytics

Description :

Experience in computer vision, image processing, NLP or embedded systems and implementation/ deployment of AI projects is a must requirement. Candidate should be willing to travel to client locations for survey, deploy and test the solutions as required. Candidate must be familiar with programming languages such as Python, C/C++ and machine learning frameworks. Ability to learn and ramp up quickly on emerging technologies is an added plus. Salaries will be competitive with best-in-class in the industry.

Qualifications: Requires Masters in Electrical Engineering/Computer Science/Computational Physics/Embedded Systems/Computer Vision/NLP and related areas from premier universities in India and abroad.

Interested candidates may send their resume, summary of work experience and list of references to *shreemathi.h@slkgroup.com*

Location: Bangalore, India

Profile of Co-Founder Dr. Murali Kota is the Co-founder and Board Member of V-Labs and an Adjunct Professor of Electrical and Computer Engineering at University of Southern California, Los Angeles. Murali got his Masters and PhD in Quantum Computing and information Processing from Massachusetts Institute of Technology. Till recently, Murali was a Distinguished Member of Technical Staff for Advanced Technology Development at GlobalFoundries and a Chief Technologist of IBM India SRDC. He is a recipient of several awards, including the Best of IBM Award, two IBM Outstanding Technical Achievement Awards, INSA and NASI Young Scientist Award, INAE Young Engineer Award and MRSI Medal. Murali has over 75 journal/ conference papers and 35 issued/filed US patents.

https://www.linkedin.com/in/murali-kota-39290b4 https://viterbi.usc.edu/directory/faculty/Kota/Murali

Eligibilty :

Program	AE	BSBE	CE	CHE	CSE	EE	ES	ME	MSE	PHY	CHM	MTH	ECO	DES	IME	CGS	HSS	EEM	MSP	NET	PSE	Stats
BT	Yes	Yes	Yes	Yes	Yes	Yes	--	Yes	Yes	--	--	--	--	--	--	--	--	--	--	--	--	--
BS	--	--	--	--	--	--	No	--	--	Yes	Yes	Yes	No	--	--	--	--	--	--	--	--	--
MT	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	--	--	--	--	--	No	--	--	No	No	No	Yes	--
DoubleMajor	Yes	Yes	Yes	Yes	Yes	Yes	--	Yes	Yes	Yes	Yes	Yes	No	--	--	--	--	--	--	--	--	--
dual	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	No	--	--	--	--	--	--	--	--	--
dualB	Yes	Yes	Yes	Yes	Yes	Yes	--	Yes	Yes	Yes	Yes	Yes	No	--	No	--	--	No	--	No	Yes	--
dualC	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	No	Yes	No	--	--	--	--	--	--	--
Mdes	--	--	--	--	--	--	--	--	--	--	--	--	--	Yes	--	--	--	--	--	--	--	--
MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--	--
Phd	Yes	Yes	Yes	Yes	Yes	Yes	No	Yes	Yes	Yes	Yes	Yes	No	Yes	No	Yes	No	No	No	No	Yes	No
MSc	--	--	--	--	--	--	--	--	--	Yes	Yes	Yes	--	--	--	--	--	--	--	--	--	No
MSR	Yes	Yes	Yes	Yes	Yes	Yes	--	Yes	Yes	--	--	--	--	--	Yes	--	--	--	--	--	Yes	--

Cost to Company :

Qualification	Role	CTC
M Tech	Project Engineer	10 LPA
	Tech role	15 LPA
PhD	Research Engineer	18 LPA

Package Details :

Qualification	Role	CTC
M Tech	Project Engineer	10 LPA
	Tech role	15 LPA
PhD	Research Engineer	18 LPA

Bond :   **False**

CPI CutOff :	**0.0**
Medical Requirments :	
Resume Shortlist :	**True**
Resume Shortlist Criteria:	**N/A**
Aptitude Test:	**False**
Group Discussion:	**False**
Technical Test:	**False**
Technical Interview:	**True**
Technical Interview Duration:	**N/A**
Number of Techincal Interview Rounds:	**0**
HR Interview:	**True**
HR Interview Duration:	**N/A**
Additional Information:	