

**Company :** SLK SOFTWARE SERVICE PVT LTD  
**Company Name :** SLK SOFTWARE SERVICE PVT LTD  
**Nature Of Business :** IT Product and Services  
**Designation :** Research and Development Positions in AI Technologies  
**Tentative Job Location :** Bangalore

**Research and Development Positions in AI Technologies**

V-Labs is a member of SLK Group of companies focused on Artificial Intelligence (AI), Internet of Things (IoT) Platforms and Solutions. SLK Software is a leading software and services company with over 3500 employees in North America/Europe/India, over \$150 Million Revenue and with over 25 clients the Banking, Financial, Insurance and Manufacturing industries. SLK is focused on Software Development, Technology Services and Automation Solutions and in the process of diversifying its portfolio in the emerging technology areas like Artificial Intelligence, IoT and related areas. V-Labs was a set up in 2018, focused on driving innovation in AI and IoT based Integrated Hardware and Software Platforms and Solutions. V-Labs brings together Video, Image, Signal Processing, NLP and Sensor technologies for real-time, scalable and robust solutions and applications in BFSI, Security, Surveillance and authentication areas. The current 40+ member team consists of over 20 Masters and PhDs from top universities including MIT (USA), IISc, IITs and universities abroad. With initial round of successful technology development and client deployments, V-Labs is looking to expand in the following areas,

*i. Computer Vision and Image Processing*

*ii. Natural Language Processing and Voice Analytics*

**Description :** Hands-on experience in using state-of-the-art Deep Learning Frameworks and their Associated Mathematical Principles, Modeling and Optimization Techniques, and Programming is a must. Strong background in Computer Vision, Image Processing Algorithms, Sensor and Speech based Signal Processing and Natural Language Processing is required. Programming experience in Python/C++/Java and familiarity with libraries like Tensorflow, Torch, Caffe, Keras, and OpenCV is essential. Ability to learn and ramp up quickly on emerging technologies is an added plus. Salaries will be competitive with best-in-class in the industry.

**Qualifications:** Requires Masters and PhDs in Electrical Engineering/Computer Science/Computational Physics/Embedded Systems/Computer Vision/NLP and related areas from premier universities in India and abroad.

Interested candidates may send their resume, summary of work experience and list of references to [shreemathi.h@slkgroup.com](mailto:shreemathi.h@slkgroup.com) Location: Bangalore, India

Profile of Co-Founder Dr. Murali Kota is the Co-founder and Board Member of V-Labs and an Adjunct Professor of Electrical and Computer Engineering at University of Southern California, Los Angeles. Murali got his Masters and PhD in Quantum Computing and information Processing from Massachusetts Institute of Technology. Till recently, Murali was a Distinguished Member of Technical Staff for Advanced Technology Development at GlobalFoundries and a Chief Technologist of IBM India SRDC. He is a recipient of several awards, including the Best of IBM Award, two IBM Outstanding Technical Achievement Awards, INSA and NASI Young Scientist Award, INAE Young Engineer Award and MRSI Medal. Murali has over 75 journal/ conference papers and 35 issued/filed US patents.

<https://www.linkedin.com/in/murali-kota-39290b4> <https://viterbi.usc.edu/directory/faculty/Kota/Mural>

	Program	AE BSBE CE CHE CSE EE ES ME MSE PHY CHM MTH ECO DES IME CGS HSS EEM MSP NET PSE Stats																			
		No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	--	--	--	--
<b>Eligibility :</b>	BT	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	--	--	--	--	--
	BS	--	--	--	--	--	--	No	--	--	Yes	No	Yes	No	--	--	--	--	--	--	--
	MT	No	No	No	No	Yes	Yes	No	No	No	--	--	--	--	No	--	--	No	Yes	No	Yes
	DoubleMajor	No	No	No	No	Yes	Yes	--	No	No	Yes	No	Yes	No	--	--	--	--	--	--	--
	dual	No	No	No	No	No	No	No	No	No	No	No	No	--	--	--	--	--	--	--	--
	dualB	No	No	No	No	No	No	--	No	No	No	No	No	--	No	--	--	No	--	No	No
	dualC	No	No	No	No	No	No	No	No	No	No	No	No	No	No	--	--	--	--	--	--
	Mdes	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--	--
	MBA	--	--	--	--	--	--	--	--	--	--	--	--	--	No	--	--	--	--	--	--
	Phd	No	No	No	No	Yes	Yes	No	No	Yes	Yes	No	Yes	No	Yes	No	Yes	Yes	No	Yes	Yes
<b>Cost to Company :</b>	MSc	--	--	--	--	--	--	--	--	--	Yes	No	Yes	--	--	--	--	--	--	--	Yes
	MSR	No	No	No	No	Yes	Yes	--	No	No	--	--	--	--	--	--	Yes	--	--	--	Yes
<b>Package Details :</b>	<b>Qualification</b>	<b>Role</b>				<b>CTC</b>															
	M Tech	Project Engineer				10 LPA															
		Tech role				15 LPA															
	PhD	Research Engineer				18 LPA															
<b>Bond :</b>	<b>Qualification</b>	<b>Role</b>				<b>CTC</b>															
	M Tech	Project Engineer				10 LPA															
		Tech role				15 LPA															
	PhD	Research Engineer				18 LPA															

**Bond :** False  
**CPI CutOff :** 0.0

Medical  
Requirments  
:  
Resume  
Shortlist :  
Resume  
Shortlist  
Criteria:  
Aptitude  
Test:  
Group  
Discussion:  
Technical  
Test:  
Technical  
Test  
Duration:  
Technical  
Interview:  
HR  
Interview:  
HR  
Interview  
Duration:  
Additional  
Information:

True  
  
N/A  
  
False  
  
False  
  
True  
  
N/A  
  
False  
  
True  
  
N/A