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INCETTRUCCTOONES

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FIFTOMOSEDIUFIEE: Your meet to subbuilit your schilden condince alt Charlescope ((Nttps://granitescomeccom/) Alternesseet the following guide from Chabbescope for sultanifiting Homework. Yould reseekt touget bankte FRD Familia manktov the eccent thoughout his misson exectly.

Recall that a DNA is composed of 4 different nucleotides, G, C, A, and T (or U), grouped as 2 pairs (G with C, and A with T). The complementary pairing facilitates transcription of DNA into RNA. Three nucleotides in sequence are recognized as distinct amino acids. For instance, GGG correspond to glycine.

- (a) Suppose that we chose a 2-bit binary representation for the nucleotides. We assign G to be nb[1:0] = {nb[1], nb[0]} = {0,0} = 2'b00 (note these different ways we represent this 2-bit signal) and T is represented as nb[1:0]=2'b01. What should one choose for C and A to facilitate representing the nucleotides after RNA transcription (which produces a "complement" strand).
- (b) Special 3 nucleotide sequences are known as a "stop codon" that indicate the end of a gene. They are TAA, TAG, and TGA. The 3 nucleotide sequence when using the encoding in (a) is represented as cb[5:0] where for TAG, bits cb[5:4] are for T, cb[3:2] are for A, and cb[1:0] are for G. Write the logic equation for indicating the detection of a stop codon, S, as a function of cb[5:0] in fully-disjunctive normal form.
- (c) Apply factoring and other Boolean properties to reduce the result in (b) into an expression using the least number of literals.
- (d) Instead of binary representation, let us use one-hot representation for the nucleotides, whereby each nucleotide is expressed with 4 bits, coh[3:0] = 4'b0001 for G, 4'b0010 for C, 4'b0100 for A, and 4'b1000 for T. The codon is represented as a 12-bit combination of 3 nucleotides, coh[11:0]. Write a minimal sum-of-products, logic equation for the same indicator for a stop codon, S.
- (e) Reduce the expression in part (d) and to one using the least number of literals.
- (f) One-hot encoding is not difficult to "complement" between the nucleotide pairing, G-to-C, C-to-G, A-to-T, and T-to-A. Write the logical equations for cnoh[3:0], the "complemented" nucleotides, from the inputs noh[3:0].

Answer the question for all parts in the space below.

A = (L[5], B = (L[4]), C = (L[3], D - (L[3], E = (L[4]), F = (L[4])) $(!A \cdot B \cdot C \cdot !D \cdot E \cdot F) + (!A \cdot B \cdot C \cdot !D \cdot !E \cdot !F) + (!A \cdot B \cdot !C \cdot !D \cdot E \cdot !F)$ $= (!A \cdot B \cdot !D) \cdot (C \cdot E \cdot F + C \cdot !E \cdot !F + !C \cdot E \cdot F)$ $= (!C \cdot L[5] \land (L[4]) \land (L[6]) \land (L$

d) TAA: 1000 0100 0100 TAG: 1000 0100 0001

TGA: 1000 0001 0100

A= (oh[1], B= coh(10) (= (oh[9], D= coh[8], E= coh[7], F= coh[6], b=coh[5], H= coh(4], I= coh(3], T= coh(3), K= coh(7), L= coh(6)

VEVINUIHVIEV IZVIKVE) A(VIBVICVIDVIE VIEVIPVICVIDVIE (VVIBVICVIDVIE) (VVIBVICVIDVIE)

=> (coh(11) 1 coh(6) 1 coh(2)) V(coh(11) 1 coh(6) 1 coh(0)) V

(coh[1] 1 coh[2])

(=(A·F·J)+(A·F·L)+(A·H·J))

 $= \frac{1}{A(F\cdot J + F\cdot L + H\cdot J)}$

= A.F(J+L) + (A.H.J)

= A(F(J+L)+(H.J))

A= noh[3], C=noh[2], C=noh[1], D=noh[0]: inputs E=choh[3], F=choh[2], G=choh[1], H=choh[0]: outputs

input: G=choh[3], G=choh[3], G=choh[1], G=choh[1

6:4160010 C:4160000 A:416000 T:416000

 $(noh[3]=(!noh[3] \land noh[2])$ $(noh[2]=(noh[3] \land !noh[2])$ $(noh[1]=(!noh[2] \land noh[0])$ $(noh[0]=(!noh[2] \land noh[1])$ (3210) (3210) (3210) (3210)

(0000) T (0000) A (1000) T (0010) A (1000) D (0000) D (0000) D (0000) D

Consider the Boolean function below.

$$y = \neg((a \lor \neg b) \land \neg c) \land \neg((a \land d) \lor (\neg b \land d)) \land e$$

- (a) Use Boolean properties to rewrite the above function as a minimal product of sums.
- (b) Use factoring and DeMorgan's theorem to rewrite and simplify the expression above into one with the least number of literals.
- (c) Design and draw an implementation of your result in (b) using only 2-input NOR gates. You can assume that true and complemented versions of each literal are available as inputs (a and ~a can both be used as inputs without needing an INV).
- (d) Using the design in (c) as the starting point, implement the function using only 2-input NAND gates (again, true and complemented inputs are available).

Answer the question for all parts in the space below.

a)
$$y = 7((aV7b) \wedge 7c) \wedge 7((aNd) \vee (7b Nd)) \wedge e$$

$$= (7(aV7b) \vee c) \wedge ((7aV7d) \wedge (bV7d)) \wedge e$$

$$= ((7a \wedge b) \vee c) \wedge (7aV7d) \wedge (b \vee 7d) \wedge e$$

$$= (7a \vee c) \wedge (b \vee c) \wedge (7aV7d) \wedge (b \vee 7d) \wedge e$$

$$= (7a \vee (c \wedge 7d)) \wedge (b \vee (c \wedge 7d)) \wedge e \quad \text{by distributhe prop}$$

$$= ((c \wedge 7d) \vee (7a \wedge b)) \wedge e$$

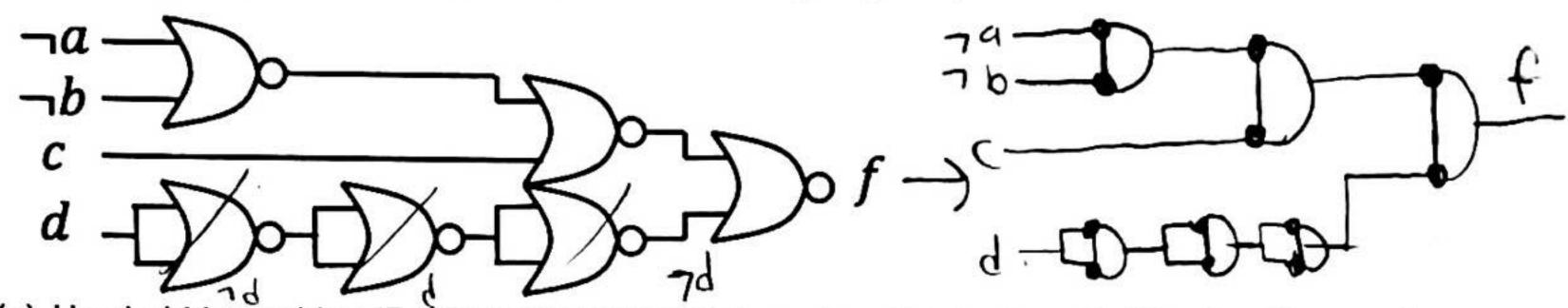
$$= ((c \wedge 7d) \vee (7a \wedge b)) \wedge e$$

to by looking at logicy we see that

NOR gate

Problem #3

The following logical function, f, is implemented using only 2-input NORs



- (a) Use bubble-pushing (DeMorgan's theorem) to re-draw this logic so that the function can be easily expressed.
- (b) Write the expression, based on (a), for the function, f.

Answer the question for all parts in the space below. $A = \neg \left(\neg \left((\neg a \lor \neg b) \lor c\right) \lor \neg \left((\neg (A \lor b) \lor A) \lor A\right) \lor A\right)$ $\neg \left((A \land b) \land \neg C\right)$

= 7 (7(7aV1b)V C)V7(7(2V2)Vd)Vd))

7 ((a1b) V c)

=7(((¬aV¬b) \ ¬() \ ¬(¬(¬(dVd)Vd)) \ \ ¬(¬(¬(dVd)Vd))

9 N (PLV P N) N 9

= 7 (Favzb) 17() V 7d))

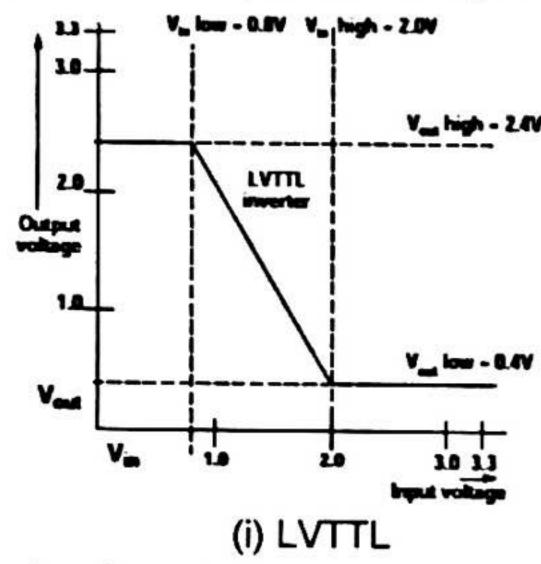
= ((a1b) vc) 1 d) = ((a.b)+c) · d

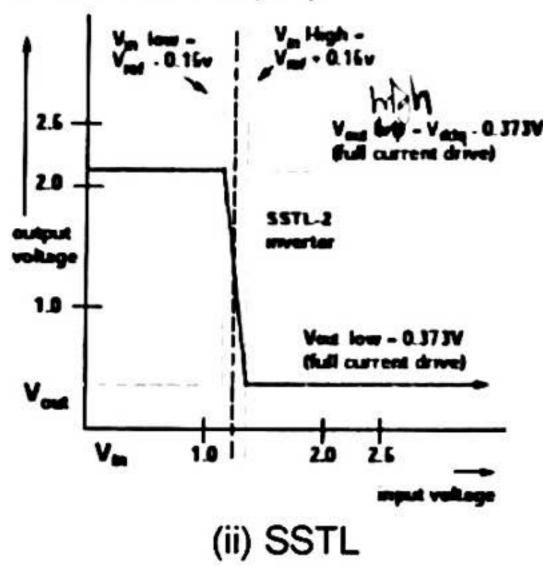
 $\frac{a}{b}$ $\frac{abtc}{d}$ $\frac{f}{d}$

b) work shown above

f = ((anb)vc) nd

Two different types of logic are shown below with different voltage transfer characteristics: (i) low-voltage TTL (transistor-transistor logic), and (ii) series-stub termination logic. Both are used in memory DIMMs that you commonly find in compute servers and laptops.





- (a) Determine the noise margin for LVTTL. The supply voltage is 3.3V.
- (b) Determine the noise margin for SSTL. Vref is set to 1.25V. The supply voltage (Vddq) is 2.5V.
- (c) Discuss the difference between these two different logic types. Despite needing a lower supply voltage, SSTL is better. What accounts for the noise margin differences? What are the advantages of using SSTL versus LVTTL?

Answer the question for all parts in the space below.

$$V_{IL} = .8V$$
, $V_{IH} = 2.0V$, $V_{OH} = 2.4V$, $V_{OL} = 0.4V$
 $V_{NH} = V_{OH} - V_{IH}(20) = 2.4V - 2.0V = 0.4V$
 $V_{NL} = .8V$, $V_{IH} = 2.0V$, $V_{OH} = 2.4V$, $V_{OL} = 0.4V$

VIL= 1.25V - 0.15V VIH= 1.25V + 0.15V VOH = 2.5V - 0.373V

c) SSTL allows for a higher noise margin. The difference in noise margin is most likely due to the input high and low values being much closer than the LVTTL counterpart. TTL significal that transitors perform both the logic function and the amplifying function. SSTL employs a reduced voltage swing on the inputs by specifying a reference voltage, which allows for a greater noise margin to be

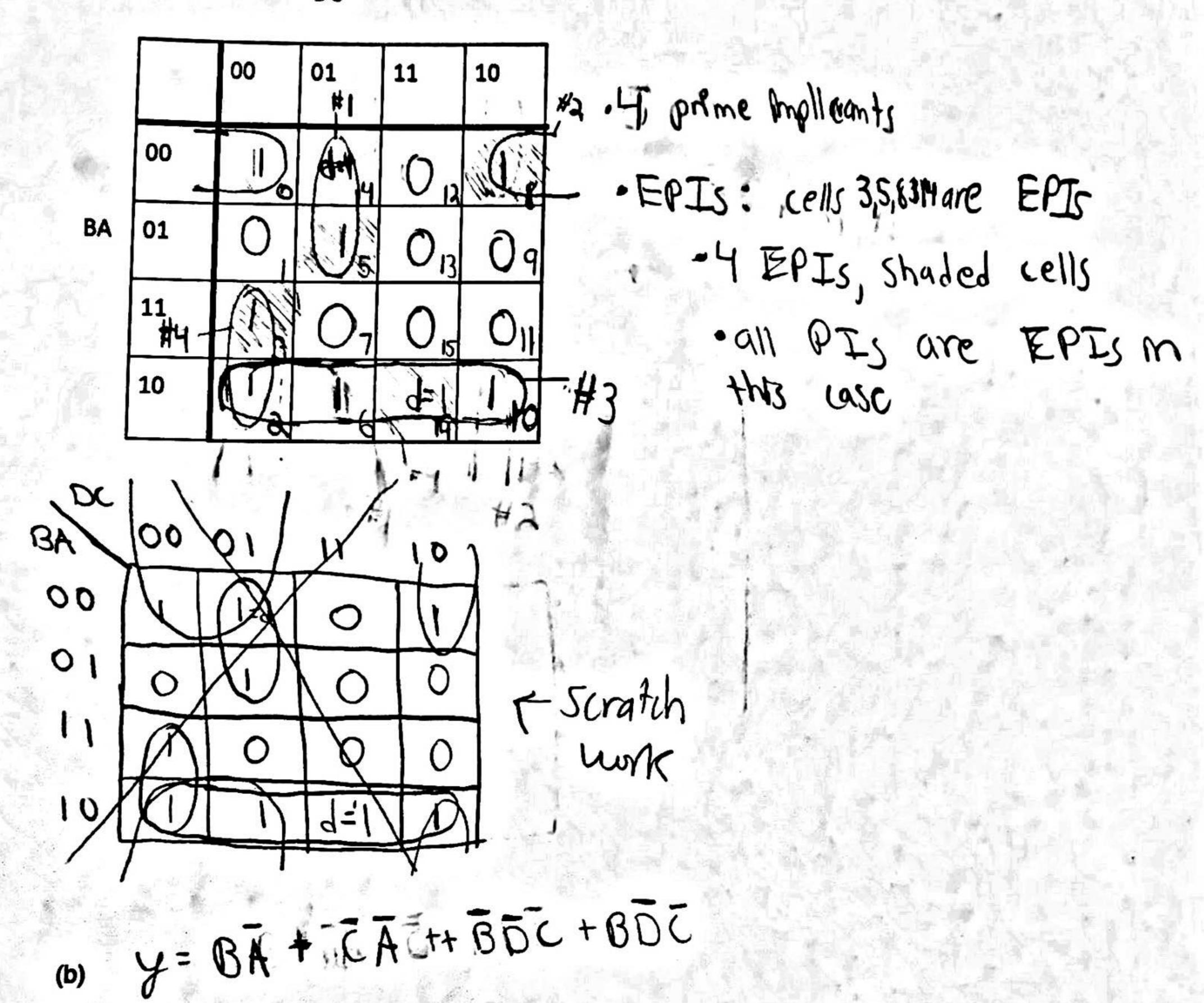
to the nureoid toleration of noise. The SSTL-2 inverter has a much steeper. sluge when compared to the LVTTL, inverter. A shurper, slope Usually indicates that the transfer curve is more ideal, membry that noisy signals are determined faster. Furthermore, the graphs depict that the LVTTL has a larger ground between what is deemed as a "I" and "Q", while the SSTL Ps set up to have a small "questionable" state zone and is predominably 0 or 1.

D	С	В	Α	Y	Y'
0	0	0	0	1	O
0	0	0	1	0	
0	0	1	0	1	0
0	0	1	1	1	O
0	1	0	0	Ъ	d
0	1	0	1	1	0
0	1	1	0	1	0
0	1	1	1	0	1
1	0	0	0	1	O
1	0	0	1	0	
1	0	1	0	1	0
1	0	1	1	0	
1	1	0	0	0	
1	1	0	1	0	
1	1	1	0	d	a
1	1	1	1	0	

- (a) Draw K-Map of the function corresponding to the truth table shown above. Identify on the K-Map the prime implicants of the function and identify which of the prime implicants (if any) are essential.
- (b) Write the minimum cover as a sum-of-products Boolean function using the prime implicants found in (a)
- (c) Draw the K-Map of the complement function, Y'. Identify on the K-Map the prime implicants of the function and identify which of the prime implicants (if any) are essential.
- (d) Write the minimum cover as a sum-of-products Boolean function using the prime implicants found in (c).
- (e) Write the complement function, Y', as a minimal product-of-sums.
- (f) Draw the truth table of the dual of the function Y, YD.

Answer the question for all parts in the space below.

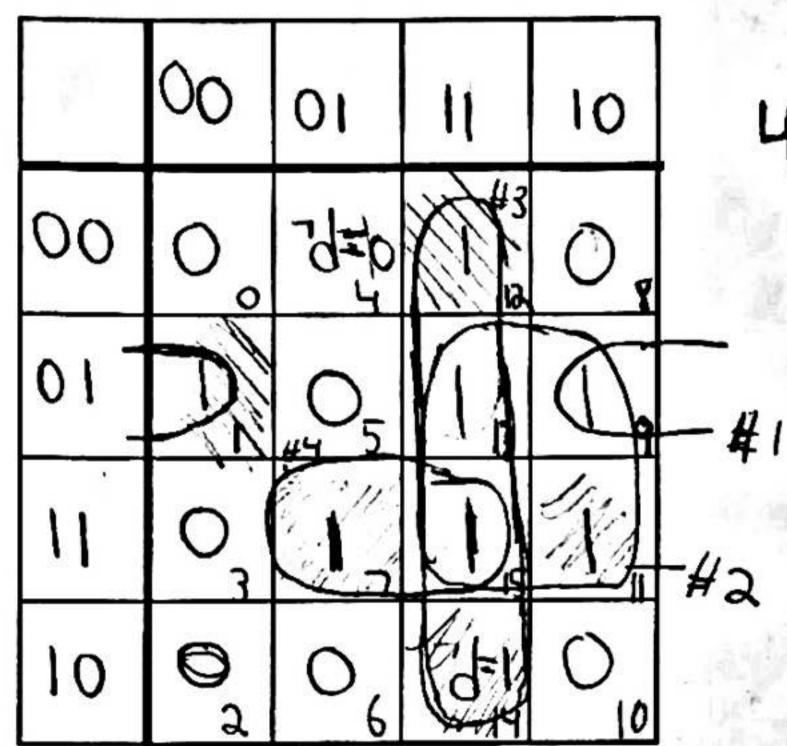
DC



(c)

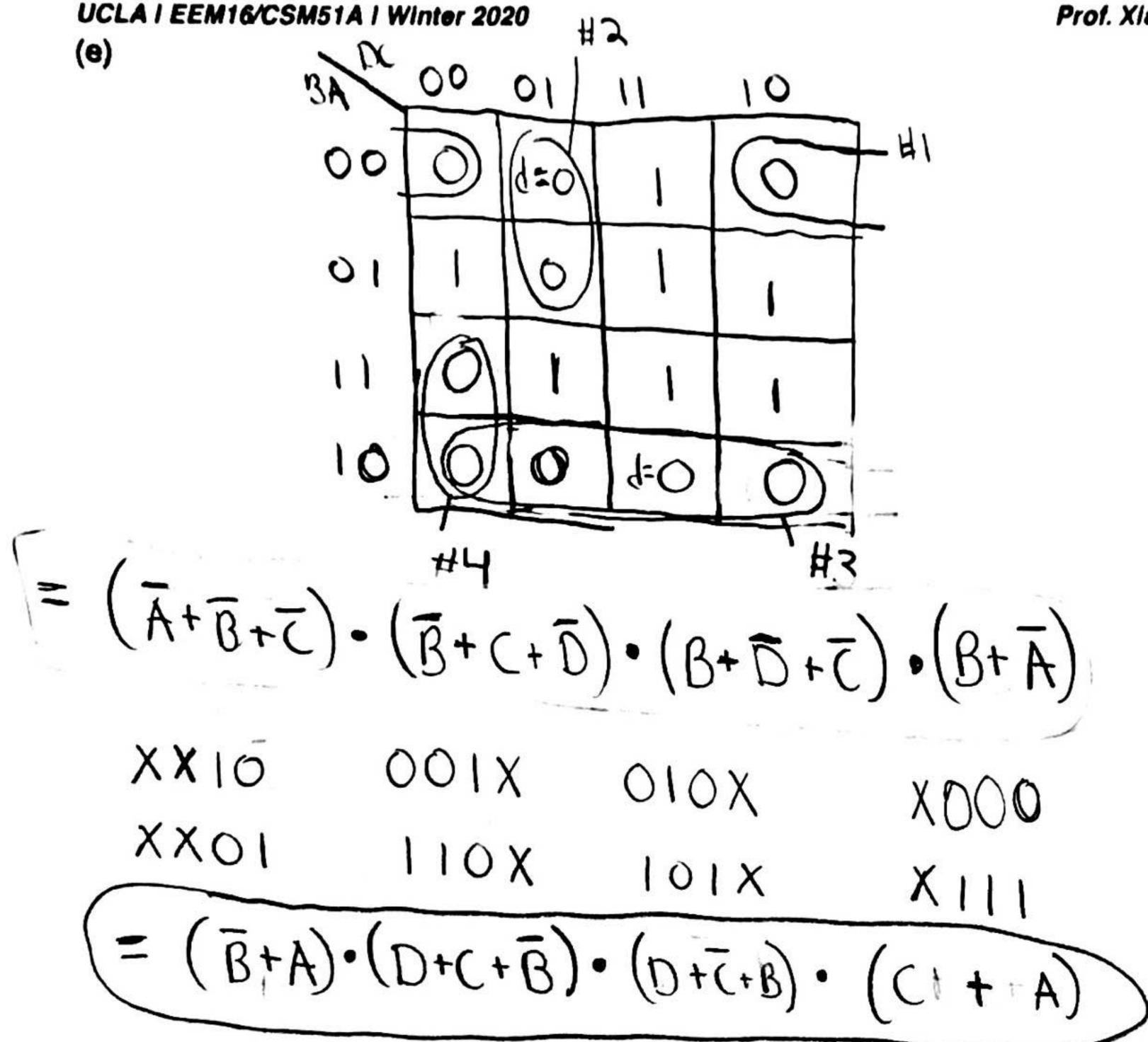
BA

DC



4 Prime Implicants: 14 EPIs: (ells 1,7,11,12,314 So, All PIs are EPIS In this case

(0) Y= DC+ AB C+ DAC



(f)

D	С	В	Α	V.
_ 			<u> </u>	YD
0	0	0	0	
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	1 -
0	1	0	1	0
0	1	1	0	-
0	1	> ₁	1	0
1	0	0	0	
1	0	0	1	0
1	0	^ 1	0	0
1	0	٥ · 1	1	0
1	1	0	0	0
1	1	0	5 1	0
1	1	1	0	1
1	1	1	1	0

$$\chi_{2} = (\beta + \overline{A}) \cdot (\overline{C} + \overline{A}) \cdot (\overline{B} + \overline{D} + C) \cdot (\beta + \overline{D} + \overline{C})$$

$$(1) \qquad (1) \qquad (1)$$

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