

Quiz 10 - Address Translation

Duration: 10 minutes

open notes, please show your work

Name:.....

ID#:.....

Question: Which one of the following is/are **not** correct

- (a) Base and bound address translation is generally more suitable for HPC applications
- (b) Most embedded systems use demand paging
- (c) If a virtual address cache is used, TLB is placed before virtual address cache
- (d) A linear page table can typically fit into TLB in modern architectures