



SEMESTER END EXAMINATIONS – APRIL 2023

Program	: B.E. – Electronics and Communication Engineering	Semester	: III
Course Name	: Analog Circuits	Max. Marks	: 100
Course Code	: EC32(O)	Duration	: 3 Hrs

Instructions to the Candidates:

- Answer one full question from each unit.

UNIT - I

- a) With example explain linear element, bilateral, loop, mesh, super mesh and Super node. CO1 (10)
- b) Using source transformation and source shifting techniques replace the network shown in Fig.1(b) across AB by a single voltage source in series with a resistance. CO1 (10)

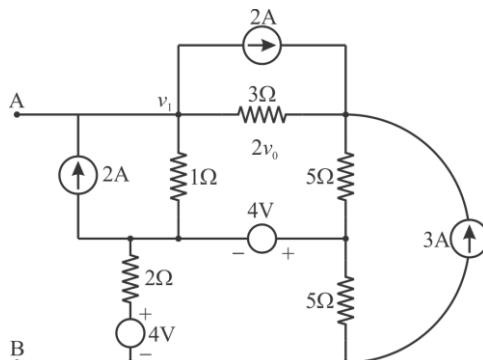


Fig.1(b)

- a) Using mesh analysis, find V_{AB} in the circuit shown in Fig.2(a). CO1 (10)

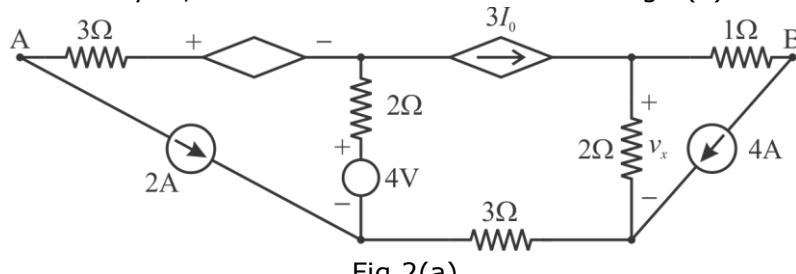


Fig.2(a)

- b) The network shown in Fig.2(b), below consists of two star connected circuits in parallel. Obtain a single delta connected equivalent. CO1 (10)

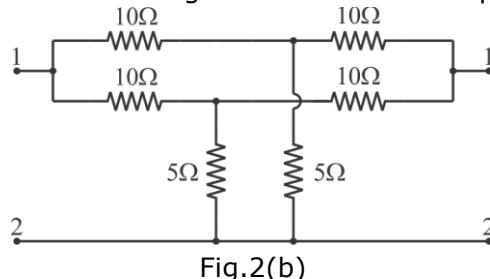


Fig.2(b)

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UNIT - II

3. a) Obtain the Norton's equivalent across the terminals A – B, in the circuit CO2 (10) shown in Fig.3(a).

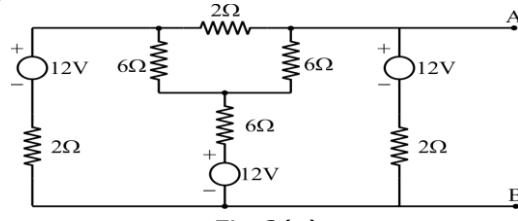


Fig.3(a)

- b) State and prove Thevenin's theorem with an example. CO2 (10)
4. a) Using superposition theorem, find V_x , in the circuit shown in Fig.4(a) CO2 (10)

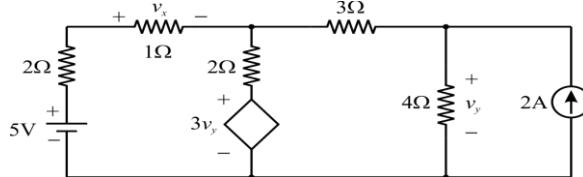


Fig.4(a)

- b) Find the value of R in the circuit shown in Fig.4(b), so that maximum CO2 (10) power is delivered to the load. Also find the maximum power delivered to the load.

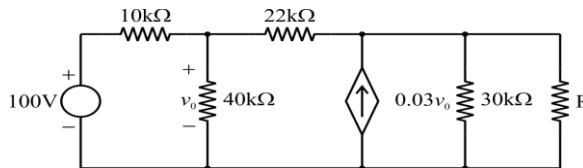


Fig.4(b)

UNIT - III

5. a) Explain the concept of feedback amplifier. An amplifier with an open loop CO3 (07) voltage gain $A_v=1000\pm 100$ is available. It is necessary to have an amplifier whose voltage gain varies by no more than ± 0.1 percent. Find:
 i) The reverse transmission factor β of the feedback network.
 ii) The gain with feedback.
- b) For the given circuit below, determine Z_i , A_f , A_v and Z_o . The CO3 (09) h parameters are $R_E = 1.2K\Omega$, $R_1 = 82K\Omega$, $R_2 = 10K\Omega$, $R_C = 4.7K\Omega$, $R_S = 1K\Omega$, $R_L = 1.2K\Omega$, $h_{fe} = 90$, $h_{ie} = 1K\Omega$, $h_{oe} = 20 \mu A/V$ and $h_{re} = 2 \times 10^{-4}$

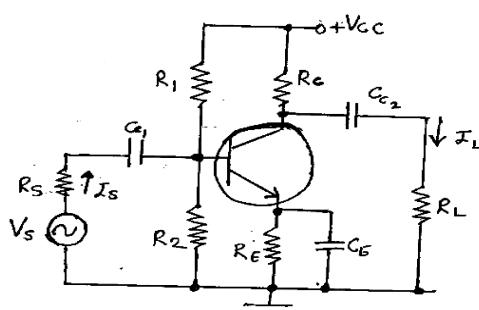


Fig.5(b)

- c) State and prove Miller's theorem. CO3 (04)

EC32(0)

6. a) Define h-parameters. Draw the h-parameter model of a transistor. CO3 (04)
 b) Draw the block diagram of a feedback amplifier. Discuss the function of each block. CO3 (08)
 c) A voltage amplifier has the following parameter values without feedback. CO3 (08)
 Open circuit voltage gain $A_v = -500$
 Input resistance $R_i = 2 \text{ k}\Omega$
 Output resistance $R_o = 402 \text{ k}\Omega$
 Bandwidth $BW = 300\text{KHz}$
 Load resistance $R_L = 10\text{KHz}$
 If 10% negative voltage series feedback is given. Calculate A_{vf} , R_{if} and B_{wf} .

UNIT- IV

7. a) Derive an expression for output power and maximum conversion efficiency of class B push pull amplifier. CO4 (07)
 b) A power amplifier has harmonic distortions $D_2 = 0.1$, $D_3 = 0.02$, $D_4 = 0.01$, the fundamental current $I_1 = 4\text{A}$ and $R_L = 8\Omega$. Calculate the total harmonic distortion, fundamental power and total power. CO4 (07)
 c) Illustrate the process of heat flow from power transistor with an equivalent analogous circuit. CO4 (06)
8. a) An ideal class B push pull power amplifier with input and output transformers, has $V_{cc} = 20\text{V}$, $N_2 = 2N_1$ and $R_L = 20\Omega$. The transistors have $h_{fe} = 20$. Let the input be sinusoidal. For the maximum output signal at $V_m = V_{cc}$, determine, the output signal power the collector dissipation in each transistor and conversion efficiency. CO4 (08)
 b) Explain the working of series fed, directly coupled class A amplifier, with the help of neat circuit diagram and prove that the maximum efficiency is 25%. CO4 (07)
 c) What are the different possible distortions in AF power amplifier? Which is the most significant? Why? CO4 (05)

UNIT - V

9. a) It is required to design the circuit of figure below to establish a dc drain current $I_D = 0.5\text{mA}$. The MOSFET is specified to have $V_t = 1\text{V}$ and $K'_n \frac{W}{L} = 1\text{mA/V}^2$. For simplicity neglect the channel length modulation (i.e. assume $\lambda = 0$). Use a power supply $V_{DD} = 15\text{V}$. Calculate the percentage change in the value of I_D obtained when the MOSFET is replaced with another unit having the same $K'_n \frac{W}{L}$ but $V_t = 1.5\text{V}$.

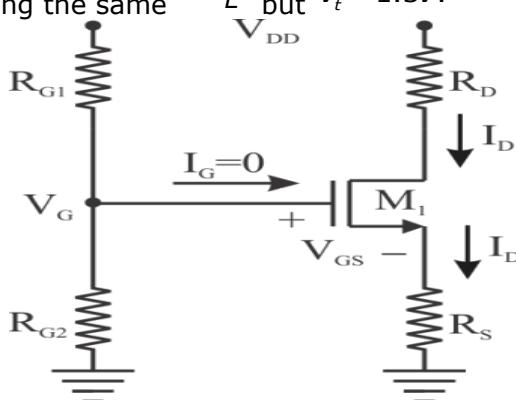


Fig.9(a)

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- b) Explain the biasing technique using drain to gate feedback and compare CO5 (06)
with other techniques.
- c) Draw the small signal model of a Common source amplifier and explain CO5 (04)
the model.
10. a) Derive the voltage gain expression and low frequency response for a CS CO5 (10)
amplifier using T-model.
- b) With neat circuit diagram explain the operation of CS amplifier & draw CO5 (10)
the transfer characteristics. Show that it can be used as a linear
amplifier.
