RK3326 I/O LIST

Version	Revision Date	Revision Description	Author
V1.0	2018.03.20	The initial version release.	Linus.Lin
V1.1	2018.06.06	Correct the pin number of "DDR3_CLKN/DDR4_CLKN". Correct the pin number of "DDR3_RESETN/DDR4_RESETN". Correct the pin number of "VSS_91". Modify the pin name of "LCD_BL_PWM" to "LCDC_BL_PWM".	Linus.Lin
V1.2	2018.08.03	Add the description of "UART0/UART5"; Add the Pin definition of function for AI-VoiceAssistant.	Linus.Lin
V1.3	2018.09.13	Correct the pin number of "DDR3_BA1/DDR4_CASN/DDR4_A15". Correct the pin number of "DDR3_CLKN/DDR4_CLKN". Modify the pin description of "TVSS".	Linus.Lin
V1.4	2019.07.22	Publish English Version.	Linus.Lin

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
PART I	PMUIO1											
W20	NPOR	I	I	up		NPOR				RESET	System reset input	RESET
W19	TVSS	I	I	down		TVSS				VSS	Digital power ground	VSS
U20	OSC_24M_IN	I	I	N/A		OSC_24M_IN				OSC_IN	Oscillator 24MHz clock input	OSC_IN
U21	OSC_24M_OUT	O	O	N/A		OSC_24M_OUT				OSC_OUT	Oscillator 24MHz clock output	OSC_OUT
Y21	GPIO0_A0/REF_CLKO	I/O	I	down	4mA	GPIO0A0	REF_CLKO			REF_CLKO	Reference clock output	BT_REG_ON
Y20	GPIO0_A1	I/O	I	down	4mA	GPIO0A1				HOST_WAKE_BT	AP wake up BT module	HOST_WAKE_BT
V21	GPIO0_A2	I/O	I	down	4mA	GPIO0A2				WIFI_REG_ON	WIFI module power enable	WIFI_REG_ON
AA20	GPIO0_A3/SDMMC0_DETN	I/O	I	up	4mA	GPIO0A3	SDMMC0_DETN			SDMMC0_DET	SDMMC0 detect	SDMMC0_DET
V20	GPIO0_A4/PMIC_SLEEP/TSADC_SHUT_M1	I/O	I	down	4mA	GPIO0A4	PMIC_SLEEP	TSADC_SHUT_M1		PMIC_SLEEP	PMIC sleep control	PMIC_SLEEP
T21	GPIO0_A5	I/O	I	up	4mA	GPIO0A5				WIFI_WAKE_HOST	WIFI module wake up AP	WIFI_WAKE_HOST
W21	GPIO0_A6/TSADC_SHUT_M0/TSADC_SHUTORG	I/O	I	high-z	4mA	GPIO0A6	TSADC_SHUT_M0	TSADC_SHUTORG		TSADC_SHUT	Over-temperature protection reset	TSADC_SHUT
Y19	GPIO0_A7	I/O	I	up	4mA	GPIO0A7				BT_WAKE_HOST	BT module wake up AP	BT_WAKE_HOST
N18	PLL_AVDD_1V0	P	N/A	N/A						VDD_1V0	PLL analog power supply	VDD_1V0
M18	PLL_AVDD_1V8	P	N/A	N/A						VCC_1V8	PLL analog power supply	VCC_1V8
N17	AVSS	P	N/A	N/A						VSS	PLL analog power ground	VSS
P17	PMUIO_VDD_1V0	P	N/A	N/A						VDD_1V0	PMUIO1 domain logic power supply	VDD_1V0
U19	PMUIO1	P	N/A	N/A						VCC3V0_PMU default	PMUIO1 domain digital IO power supply	VCC_1V8 default
T13	OTP_VCC_1V8	P	N/A	N/A						VCC_1V8	OTP IO power supply	VCC_1V8
PART J	PMUIO2											
R21	GPIO0_B0/I2C0_SCL	I/O	I	up	4mA	GPIO0B0	I2C0_SCL			I2C0_SCL_PMIC	I2C serial port 0,for PMIC,need external pull-up	I2C0_SCL_PMIC
P21	GPIO0_B1/I2C0_SDA	I/O	I	up	4mA	GPIO0B1	I2C0_SDA			I2C0_SDA_PMIC	I2C serial port 0,for PMIC,need external pull-up	I2C0_SDA_PMIC
N20	GPIO0_B2/UART0_TXD	I/O	I	down	4mA	GPIO0B2	UART0_TXD			PMIC_INT	PMIC_interrupt	PMIC_INT
P18	GPIO0_B3/UART0_RXD	I/O	I	down	4mA	GPIO0B3	UART0_RXD			TP_INT	Touch pannel interrupt	TP_INT
R18	GPIO0_B4/UART0_CTS	I/O	I	up	4mA	GPIO0B4	UART0_CTS			TP_RST	Touch pannel reset	TP_RST
N19	GPIO0_B5/UART0_RTS/TEST_CLK1	I/O	I	up	4mA	GPIO0B5	UART0_RTS	TEST_CLK1		LCD_PWREN	LCD panel power enable	
M19	GPIO0_B7/PWM0/OTG_DRV	I/O	I	down	4mA	GPIO0B7	PWM0	OTG_DRV		SENSOR_INT	G-sensor interrupt	SENSOR_INT
N21	GPIO0_C0/PWM1	I/O	I	down	4mA	GPIO0C0	PWM1			LCD_BL	LCD panel backlight brightness control	LCD_BL
P19	GPIO0_C1	I/O	I	down	4mA	GPIO0C1				BT_REG_ON	BT module power enable	SPK_MUTE
T20	GPIO0_C2/I2C1_SCL	I/O	I	down	4mA	GPIO0C2	I2C1_SCL			I2C1_SCL	I2C serial port 1,for TP/Sensor,need external pull-up	I2C1_SCL
R20	GPIO0_C3/I2C1_SDA	I/O	I	down	4mA	GPIO0C3	I2C1_SDA			I2C1_SDA	I2C serial port 1,for TP/Sensor,need external pull-up	I2C1_SDA
P20	GPIO0_C4/CLKIO_32K	I/O	I	high-z	4mA	GPIO0C4	CLK_INOUT_32K			CLKOUT_32K	32KHz real time clock input or output	CLKOUT_32K
R19	FLASH_VOLSEL	I	I	up	4mA		FLASH_VOL_SEL			FLASH_VOLSEL Floating_default	Flash Controller default power supply voltage select for boot	FLASH_VOLSEL Floating_default
T19	PMUIO2	P	N/A	N/A						VCC3V0_PMU	PMUIO2 domain digital IO power supply	VCC3V0_PMU
PART K	VCCIO1											
M20	GPIO1_C0/UART1_RXD	I/O	I	up	8mA	GPIO1C0	UART1_RXD			UART1_RXD	UART1 serial port, for BT module	UART1_RXD
M21	GPIO1_C1/UART1_TXD	I/O	I	up	8mA	GPIO1C1	UART1_TXD			UART1_TXD	UART1 serial port, for BT module	UART1_TXD
J20	GPIO1_C2/UART1_CTS	I/O	I	up	8mA	GPIO1C2	UART1_CTS			UART1_CTS	UART1 serial port, for BT module	UART1_CTS
J19	GPIO1_C3/UART1_RTS	I/O	I	up	8mA	GPIO1C3	UART1_RTS			UART1_RTS	UART1 serial port, for BT module	UART1_RTS
H19	GPIO1_C4/SDIO_CMD	I/O	I	up	8mA	GPIO1C4	SDIO_CMD			SDIO_CMD	SDIO command output,for WIFI module	SDIO_CMD
K20	GPIO1_C5/SDIO_CLK	I/O	I	down	8mA	GPIO1C5	SDIO_CLK			SDIO_CLK	SDIO clock output,for WIFI module	SDIO_CLK
K21	GPIO1_C6/SDIO_D0	I/O	I	up	8mA	GPIO1C6	SDIO_D0			SDIO_D0	SDIO data port ,for WIFI module	SDIO_D0
L21	GPIO1_C7/SDIO_D1	I/O	I	up	8mA	GPIO1C7	SDIO_D1			SDIO_D1	SDIO data port ,for WIFI module	SDIO_D1
L19	GPIO1_D0/SDIO_D2	I/O	I	up	8mA	GPIO1D0	SDIO_D2			SDIO_D2	SDIO data port ,for WIFI module	SDIO_D2
L20	GPIO1_D1/SDIO_D3	I/O	I	up	8mA	GPIO1D1	SDIO_D3			SDIO_D3	SDIO data port ,for WIFI module	SDIO_D3
K19	VCCIO1	P	N/A	N/A						VCC_3V0	VCCIO1 domain digital IO power supply	VCCIO_SDIO
PART H	VCCIO2											
AA18	GPIO1_D2/SDMMC0_D0/UART2_TX_M0	I/O	I	up	8mA	GPIO1D2	SDMMC0_D0	UART2_TX_M0		SDMMC0_D0	SDMMC0 data port	SDMMC0_D0/URAT2_TX
Y17	GPIO1_D3/SDMMC0_D1/UART2_RX_M0	I/O	I	up	8mA	GPIO1D3	SDMMC0_D1	UART2_RX_M0		SDMMC0_D1	SDMMC0 data port	SDMMC0_D1/URAT2_RX
Y16	GPIO1_D4/SDMMC0_D2/JTAG_TCK	I/O	I	up	8mA	GPIO1D4	SDMMC0_D2	JTAG_TCK		SDMMC0_D2	SDMMC0 data port	SDMMC0_D2
AA16	GPIO1_D5/SDMMC0_D3/JTAG_TMS	I/O	I	up	8mA	GPIO1D5	SDMMC0_D3	JTAG_TMS		SDMMC0_D3	SDMMC0 data port	SDMMC0_D3
AA19	GPIO1_D6/SDMMC0_CLKO/TEST_CLK0	I/O	I	down	8mA	GPIO1D6	SDMMC0_CLKO	TEST_CLK0		SDMMC0_CLKO	SDMMC0 clock output	SDMMC0_CLKO
AA17	GPIO1_D7/SDMMC0_CMD	I/O	I	up	8mA	GPIO1D7	SDMMC0_CMD			SDMMC0_CMD	SDMMC0 command output	SDMMC0_CMD
Y18	VCCIO2	P	N/A	N/A						VCCIO_SD	VCCIO2 domain digital I/O power supply	VCCIO_SD
PART B	VCCIO3											

RK3326 I/O LIST

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AA5	GPIO2_A0/CIF_D2_M0	I/O	I	down	4mA	GPIO2A0	CIF_D2_M0			CIF_D2	Camera data port	CIF_D2
AA8	GPIO2_A1/CIF_D3_M0	I/O	I	down	4mA	GPIO2A1	CIF_D3_M0			CIF_D3	Camera data port	CIF_D3
AA7	GPIO2_A2/CIF_D4_M0	I/O	I	down	4mA	GPIO2A2	CIF_D4_M0			CIF_D4	Camera data port	CIF_D4
Y6	GPIO2_A3/CIF_D5_M0	I/O	I	down	4mA	GPIO2A3	CIF_D5_M0			CIF_D5	Camera data port	CIF_D5
Y8	GPIO2_A4/CIF_D6_M0	I/O	I	down	4mA	GPIO2A4	CIF_D6_M0			CIF_D6	Camera data port	CIF_D6
Y7	GPIO2_A5/CIF_D7_M0	I/O	I	down	4mA	GPIO2A5	CIF_D7_M0			CIF_D7	Camera data port	CIF_D7
W5	GPIO2_A6/CIF_D8_M0	I/O	I	down	4mA	GPIO2A6	CIF_D8_M0			CIF_D8	Camera data port	CIF_D8
W7	GPIO2_A7/CIF_D9_M0	I/O	I	down	4mA	GPIO2A7	CIF_D9_M0			CIF_D9	Camera data port	CIF_D9
Y4	GPIO2_B0/CIF_VSYNC_M0	I/O	I	down	4mA	BPIO2B0	CIF_VSYNC_M0			CIF_VSYNC	Camera vsync input	CIF_VSYNC
AA4	GPIO2_B1/CIF_HREF_M0	I/O	I	down	4mA	GPIO2B1	CIF_HREF_M0			CIF_HREF	Camera href input	CIF_HREF
AA6	GPIO2_B2/CIF_CLKI_M0	I/O	I	down	4mA	GPIO2B2	CIF_CLKI_M0			CIF_CLKI	Camera clock input	CIF_CLKI
Y5	GPIO2_B3/CIF_CLKO_M0	I/O	I	down	4mA	GPIO2B3	CIF_CLKO_M0			CIF_CLKO	Camera clock output	CIF_CLKO
V12	GPIO2_B4/UART2_TX_M1	I/O	I	down	4mA	GPIO2B4	UART2_TX_M1			HDMI_INT	Interrupt for RK618	
V7	GPIO2_B5	I/O	I	down	4mA	GPIO2B5				CAM_PDN1	Camera power down control output for rear	CAM_PDN1
W6	GPIO2_B6/UART2_RX_M1	I/O	I	down	4mA	GPIO2B6	UART2_RX_M1			CAM_PDN0	Camera power down control output for front	CAM_PDN0
U7	GPIO2_B7/I2C2_SCL	I/O	I	up	4mA	GPIO2B7	I2C2_SCL			I2C2_SCL_CAM	I2C serial port 1,for camera,need external pull-up	I2C2_SCL_CAM
V6	GPIO2_C0/I2C2_SDA	I/O	I	up	4mA	GPIO2C0	I2C2_SDA			I2C2_SDA_CAM	I2C serial port 1,for camera,need external pull-up	I2C2_SDA_CAM
U6	VCCIO3	P	N/A	N/A						VCCIO_DVP	VCCIO3 domain digital I/O power supply	VCCIO_DVP
PART M	VCCIO4											
B16	LVDS/MIPI_TX_D0N/LCDC_D11_M1	A	N/A	N/A			LCDC_D11_M1	LVDS/MIPI_D0N		LVDS/MIPI_TXD0N/LCDC_D11	LVDS/MIPI-DSI differential lane 0 negative	LVDS/MIPI_TXD0N
B17	LVDS/MIPI_TX_D0P/LCDC_D8_M1	A	N/A	N/A			LCDC_D8_M1	LVDS/MIPI_D0P		LVDS/MIPI_TXD0P/LCDC_D8	LVDS/MIPI-DSI differential lane 0 positive	LVDS/MIPI_TXD0P
B15	LVDS/MIPI_TX_D1N/LCDC_D1_M1	A	N/A	N/A			LCDC_D1_M1	LVDS/MIPI_D1N		LVDS/MIPI_TXD1N/LCDC_D1	LVDS/MIPI-DSI differential lane 1 negative	LVDS/MIPI_TXD1N
A16	LVDS/MIPI_TX_D1P/LCDC_D10_M1	A	N/A	N/A			LCDC_D10_M1	LVDS/MIPI_D1P		LVDS/MIPI_TXD1P/LCDC_D10	LVDS/MIPI-DSI differential lane 1 positive	LVDS/MIPI_TXD1P
B14	LVDS/MIPI_TX_CLKN/LCDC_D4_M1	A	N/A	N/A			LCDC_D4_M1	LVDS/MIPI_CLKN		LVDS/MIPI_TXCLKN/LCDC_D4	LVDS/MIPI-DSI differential lane clock negative	LVDS/MIPI_TXCLKN
A14	LVDS/MIPI_TX_CLKP/LCDC_D3_M1	A	N/A	N/A			LCDC_D3_M1	LVDS/MIPI_CLKP		LVDS/MIPI_TXCLKP/LCDC_D3	LVDS/MIPI-DSI differential lane clock positive	LVDS/MIPI_TXCLKP
C13	LVDS/MIPI_TX_D2N/LCDC_VSYNC_M1	A	N/A	N/A			LCDC_VSYNC_M1	LVDS/MIPI_D2N		LVDS/MIPI_TXD2N/LCDC_VSYNC	LVDS/MIPI-DSI differential lane 2 negative	LVDS/MIPI_TXD2N
B13	LVDS/MIPI_TX_D2P/LCDC_D5_M1	A	N/A	N/A			LCDC_D5_M1	LVDS/MIPI_D2P		LVDS/MIPI_TXD2P/LCDC_D5	LVDS/MIPI-DSI differential lane 2 positive	LVDS/MIPI_TXD2P
B12	LVDS/MIPI_TX_D3N/LCDC_HSYNC_M1	A	N/A	N/A			LCDC_HSYNC_M1	LVDS/MIPI_D3N		LVDS/MIPI_TXD3N/LCDC_HSYNC	LVDS/MIPI-DSI differential lane 3 negative	LVDS/MIPI_TXD3N
A12	LVDS/MIPI_TX_D3P/LCDC_DEN_M1	A	N/A	N/A			LCDC_DEN_M1	LVDS/MIPI_D3P		LVDS/MIPI_TXD3P/LCDC_DEN	LVDS/MIPI-DSI differential lane 3 positive	LVDS/MIPI_TXD3P
D19	GPIO3_A0/LCDC_CLK	I/O	I	down	4mA	GPIO3A0	LCDC_CLK			LCDC_CLK	LCDC pixel clk output	PA_DRV
E13	GPIO3_A1/LCDC_HSYNC_M0/I2S2_2CH_MCLK/UART5_RXD	I/O	I	down	4mA	GPIO3A1	LCDC_HSYNC_M0	UART5_RXD	I2S2_2CH_MCLK			CHG_DET
F13	GPIO3_A2/LCDC_VSYNC_M0/I2S2_2CH_SCLK/UART5_TXD	I/O	I	down	4mA	GPIO3A2	LCDC_VSYNC_M0	UART5_TXD	I2S2_2CH_SCLK	BT_PCM_CLK	I2S2 port,for BT module	BT_PCM_CLK
E14	GPIO3_A3/LCDC_DEN_M0/CIF_D2_M1/I2S2_2CH_LRCK_TXRX/UART5_CTS	I/O	I	down	4mA	GPIO3A3	LCDC_DEN_M0	CIF_D2_M1 UART5_CTS	I2S2_2CH_LRCK_TXRX	BT_PCM_SYNC	I2S2 port,for BT module	BT_PCM_SYNC
C15	GPIO3_A4/LCDC_D0	I/O	I	down	4mA	GPIO3A4	LCDC_D0			LCDC_D0	LCDC data port	PHONE_DET
E15	GPIO3_A5/LCDC_D1_M0/CIF_D3_M1/I2S2_2CH_SDI/UART5_RTS	I/O	I	down	4mA	GPIO3A5	LCDC_D1_M0	CIF_D3_M1 UART5_RTS	I2S2_2CH_SDI	BT_PCM_OUT	I2S2 port,for BT module	BT_PCM_OUT
C14	GPIO3_A6/LCDC_D2	I/O	I	down	4mA	GPIO3A6	LCDC_D2			LCDC_D2	LCDC data port	MIC_LED_EN 灯圈使能脚 高enable
E16	GPIO3_A7/LCDC_D3_M0/CIF_D4_M1/I2S2_2CH_SDO	I/O	I	down	4mA	GPIO3A7	LCDC_D3_M0	CIF_D4_M1	I2S2_2CH_SDO	BT_PCM_IN	I2S2 port,for BT module	BT_PCM_IN
E17	GPIO3_B0/LCDC_D4_M0/CIF_D5_M1/I2S0_8CH_SDI3/I2S0_8CH_SDI3	I/O	I	down	4mA	GPIO3B0	LCDC_D4_M0	CIF_D5_M1	I2S0_8CH_SDI3	RK618_PWREN	Power enable for RK618	
F17	GPIO3_B1/LCDC_D5_M0/CIF_D6_M1/I2S0_8CH_SDI2/SPI1_CSN0	I/O	I	down	4mA	GPIO3B1	LCDC_D5_M0	CIF_D6_M1 SPI1_CSN0	I2S0_8CH_SDI2	RK618_RST	Reset for RK618	I2S0_SDI2
B18	GPIO3_B2/LCDC_D6/SPI1_CSN1	I/O	I	down	4mA	GPIO3B2	LCDC_D6			LCDC_D6	LCDC data port	
C17	GPIO3_B3/LCDC_D7/I2S0_8CH_SDI1	I/O	I	down	4mA	GPIO3B3	LCDC_D7		I2S0_8CH_SDI1	LCDC_D7	LCDC data port	I2S0_SDI1

RK3326 I/O LIST

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F18	GPIO3_B4/LCDC_D8_M0/CIF_D7_M1/I2S0_8CH_SCLKRX/I2S0_8CH_SCLKRX/SPI1_MOSI	I/O	I	down	4mA	GPIO3B4	LCDC_D8_M0	CIF_D7_M1 SPI1_MOSI	I2S0_8CH_SCLKRX	COMP_INT	Compass interrupt	I2S0_SCLK_RX
C16	GPIO3_B5/LCDC_D9_M0/I2S0_8CH_LRCKRX	I/O	I	down	4mA	GPIO3B5	LCDC_D9		I2S0_8CH_LRCKRX	LCDC_D9	LCDC data port	I2S0_LRCK_RX
G18	GPIO3_B6/LCDC_D10_M0/CIF_D8_M1/I2S0_8CH_SDO3/SPI1_MISO	I/O	I	down	4mA	GPIO3B6	LCDC_D10_M0	CIF_D8_M1 SPI1_MOSI	I2S0_8CH_SDO3	WIFI_PWREN	WIFI module power enable	
G17	GPIO3_B7/LCDC_D11_M0/CIF_D9_M1/I2S0_8CH_SDO2/SPI1_CLK	I/O	I	down	4mA	GPIO3B7	LCDC_D11_M0	CIF_D9_M1 SPI1_CLK	I2S0_8CH_SDO2	LCD_RST	LCD panel reset	
A20	GPIO3_C0/LCDC_D12/I2S0_8CH_SDO1	I/O	I	down	4mA	GPIO3C0	LCDC_D12		I2S0_8CH_SDO1	LCDC_D12	LCDC data port	
B20	GPIO3_C1/LCDC_D13/I2S0_8CH_MCLK	I/O	I	down	4mA	GPIO3C1	LCDC_D13		I2S0_8CH_MCLK	LCDC_D13	LCDC data port	I2S0_MCLK
C19	GPIO3_C2/LCDC_D14/I2S0_8CH_LRCKTX/TDM_FSYNC	I/O	I	down	4mA	GPIO3C2	LCDC_D14	TDM_FSYNC	I2S0_8CH_LRCKTX	LCDC_D14	LCDC data port	
B19	GPIO3_C3/LCDC_D15/I2S0_8CH_SCLKTX/TDM_SCLK	I/O	I	down	4mA	GPIO3C3	LCDC_D15	TDM_SCLK	I2S0_8CH_SCLKTX	LCDC_D15	LCDC data port	
C18	GPIO3_C4/LCDC_D16/I2S0_8CH_SDO0/TDM_SDO	I/O	I	down	4mA	GPIO3C4	LCDC_D16	TDM_SDO	I2S0_8CH_SDO0	LCDC_D16	LCDC data port	
A18	GPIO3_C5/LCDC_D17/I2S0_8CH_SDI0/TDM_SDI	I/O	I	down	4mA	GPIO3C5	LCDC_D17	TDM_SDI	I2S0_8CH_SDI0	LCDC_D17	LCDC data port	I2S0_SDI0
D13	GPIO3_C6/LCDC_D18/PDM_CLK0_M0	I/O	I	down	4mA	GPIO3C6	LCDC_D18		PDM_CLK0_M0	LCDC_D18	LCDC data port	
D14	GPIO3_C7/LCDC_D19/PDM_CLK1	I/O	I	down	4mA	GPIO3C7	LCDC_D19		PDM_CLK1	LCDC_D19	LCDC data port	PDM_CLK
D15	GPIO3_D0/LCDC_D20/CIF_CLKOUT_M1/PDM_SDI1	I/O	I	down	4mA	GPIO3D0	LCDC_D20	CIF_CLKOUT_M1	PDM_SDI1	LCDC_D20	LCDC data port	PDM_SDI1
D16	GPIO3_D1/LCDC_D21/CIF_VSYNC_M1/PDM_SDI2/ISP_PRELIGHT_TRIG	I/O	I	down	4mA	GPIO3D1	LCDC_D21	CIF_VSYNC_M1	PDM_SDI2	LCDC_D21	LCDC data port	PDM_SDI2
D17	GPIO3_D2/LCDC_D22/CIF_HREF_M1/PDM_SDI3/ISP_FLASH_TRIGOUT	I/O	I	down	4mA	GPIO3D2	LCDC_D22	CIF_HREF_M1	PDM_SDI3	LCDC_D22	LCDC data port	PDM_SDI3
D18	GPIO3_D3/LCDC_D23/CIF_CLKIN_M1/PDM_SDI0_M0/ISP_FLASH_TRIGIN	I/O	I	down	4mA	GPIO3D3	LCDC_D23	CIF_CLKIN_M1	PDM_SDI0_M0	LCDC_D23	LCDC data port	
G12	LVDS_RBIAS	A	N/A	N/A						LVDS_RBIAS	LVDS reference current generate,connect a 2K%1 resistor to VSS	LVDS_RBIAS
F11	LVDS/MIPI_AVDD_1V0	P	N/A	N/A						VDD_1V0	LVDS/MIPI phy analog power supply	VDD_1V0
F12	LVDS/MIPI_AVDD_1V8	P	N/A	N/A						VDD_1V8	LVDS/MIPI phy analog power supply	VDD_1V8
E12	LVDS/MIPI_AVDD_3V3	P	N/A	N/A						VCC_3V0	LVDS/MIPI phy analog power supply	VCC_3V0
D12	VCCIO4	P	N/A	N/A						VCC_3V0 default	VCCIO4 domain digital I/O power supply	VCC_1V8 default
PART G	VCCIO5											
AA15	GPIO2_C1/I2S1_LRCK_TXRX	I/O	I	down		GPIO2C1	I2S1_LRCK_TXRX			I2S1_LRCK_TXRX	I2S 1 port, for RK817 audio codec	I2S1_LRCK_TXRX
AA14	GPIO2_C2/I2S1_SCLK	I/O	I	down		GPIO2C2	I2S1_SCLK			I2S1_SCLK	I2S 1 port, for RK817 audio codec	I2S1_SCLK
Y14	GPIO2_C3/I2S1_MCLK	I/O	I	down		GPIO2C3	I2S1_MCLK			I2S1_MCLK	I2S 1 port, for RK817 audio codec	I2S1_MCLK
Y15	GPIO2_C4/I2S1_SDO	I/O	I	down		GPIO2C4	I2S1_SDO			I2S1_SDO	I2S 1 port, for RK817 audio codec	I2S1_SDO
AA13	GPIO2_C5/I2S1_SDI/PDM_SDI0_M1	I/O	I	down		GPIO2C5	I2S1_SDI	PDM_SDI0_M1		I2S1_SDI	I2S 1 port, for RK817 audio codec	PDM_SDI0_M1
W15	GPIO2_C6/PDM_CLK0_M1	I/O	I	down		GPIO2C6		PDM_CLK0_M1		PHONE_DET	Headphone insert detect	PDM_CLK0_M1
W16	VCCIO5	P	N/A	N/A						VCC_3V0	VCCIO5 domain digital I/O power supply	VCC_3V0
PART L	VCCIO6											
E20	GPIO1_A0/FLASH_D0/EMMC_D0/SFC_SIO0	I/O	I	up	8mA	GPIO1A0	FLASH_D0	EMMC_D0	SFC_SIO0	FLASH_D0	Nand flash data port	FLASH_D0
D21	GPIO1_A1/FLASH_D1/EMMC_D1/SFC_SIO1	I/O	I	up	8mA	GPIO1A1	FLASH_D1	EMMC_D1	SFC_SIO1	FLASH_D1	Nand flash data port	FLASH_D1
C21	GPIO1_A2/FLASH_D2/EMMC_D2/SFC_SIO2	I/O	I	up	8mA	GPIO1A2	FLASH_D2	EMMC_D2	SFC_SIO2	FLASH_D2	Nand flash data port	FLASH_D2
E19	GPIO1_A3/FLASH_D3/EMMC_D3/SFC_SIO3	I/O	I	up	8mA	GPIO1A3	FLASH_D3	EMMC_D3	SFC_SIO3	FLASH_D3	Nand flash data port	FLASH_D3
E21	GPIO1_A4/FLASH_D4/EMMC_D4/SFC_CSN0	I/O	I	up	8mA	GPIO1A4	FLASH_D4	EMMC_D4	SFC_CSN0	FLASH_D4	Nand flash data port	FLASH_D4
D20	GPIO1_A5/FLASH_D5/EMMC_D5	I/O	I	up	8mA	GPIO1A5	FLASH_D5	EMMC_D5		FLASH_D5	Nand flash data port	FLASH_D5
C20	GPIO1_A6/FLASH_D6/EMMC_D6	I/O	I	up	8mA	GPIO1A6	FLASH_D6	EMMC_D6		FLASH_D6	Nand flash data port	FLASH_D6
B21	GPIO1_A7/FLASH_D7/EMMC_D7	I/O	I	up	8mA	GPIO1A7	FLASH_D7	EMMC_D7		FLASH_D7	Nand flash data port	FLASH_D7
F19	GPIO1_B0/FLASH_CS0	I/O	I	up	8mA	GPIO1B0	FLASH_CS0		SFC_CLK	FLASH_CS0n	Nand flash select0 port	FLASH_CS0n
H18	GPIO1_B1/FLASH_RDY/EMMC_CLKOUT/SFC_CLK	I/O	I	up	8mA	GPIO1B1	FLASH_RDY	EMMC_CLK		FLASH_RDY/EMMC_CLK	Nand flash read enable	FLASH_RDY/EMMC_CLK
G19	GPIO1_B2/FLASH_DQS/EMMC_CMD	I/O	I	up	8mA	GPIO1B2	FLASH_DQS	EMMC_CMD		FLASH_DQS/EMMC_CMD	Nand flash DQS	FLASH_DQS/EMMC_CMD
E18	GPIO1_B3/FLASH_ALE/EMMC_RSTN	I/O	I	down	8mA	GPIO1B3	FLASH_ALE	EMMC_RSTN		FLASH_ALE/EMMC_RST	Nand flash address latch enable	FLASH_ALE/EMMC_RST
G21	GPIO1_B4/FLASH_CLE/SPI0_MOSI	I/O	I	down	8mA	GPIO1B4	FLASH_CLE	SPI0_MOSI		FLASH_CLE	Nand flash command latch enable	FLASH_CLE

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
H20	GPIO1_B5/FLASH_WRN/SPI0_MISO	I/O	I	up	8mA	GPIO1B5	FLASH_WRN	SPI0_MISO		FLASH_WRN	Nand flash write enable	FLASH_WRN
G20	GPIO1_B6/FLASH_CS1/SPI0_CSN	I/O	I	up	8mA	GPIO1B6	FLASH_CS1	SPI0_CSN		FLASH_CS1n	Nand flash select1 port	FLASH_CS1n
H21	GPIO1_B7/FLASH_RDN/SPI0_CLK	I/O	I	up	8mA	GPIO1B7	FLASH_RDN	SPI0_CLK		FLASH_RDN	Nand flash read enable	FLASH_RDN
J18	VCCIO6	P	N/A	N/A						VCCIO_FLASH	VCCIO6 domain digital I/O power supply	VCCIO_FLASH
PART F	SAR ADC											
V14	ADC_IN0	A	N/A	N/A						ADC0_HW_ID	Hardware version recognition	ADC0_HW_ID
W14	ADC_IN1	A	N/A	N/A						ADC1_HP_HOOK	Headphone key detect	
V15	ADC_IN2	A	N/A	N/A						ADC2_KEY_IN	AD keyboard input	ADC2_KEY_IN
U13	ADC_AVDD_1V8	AP	N/A	N/A						VCC_1V8	SARADC analog power supply	VCC_1V8
PART D	MIPI CSI PHY											
W10	MIPI_CSI_D0P	A	N/A	N/A						MIPI_CSI_D0P	MIPI-CSI differential lane 0 positive	MIPI_CSI_D0P
V11	MIPI_CSI_D0N	A	N/A	N/A						MIPI_CSI_D0N	MIPI-CSI differential lane 0 negative	MIPI_CSI_D0N
Y9	MIPI_CSI_D1P	A	N/A	N/A						MIPI_CSI_D1P	MIPI-CSI differential lane 1 positive	MIPI_CSI_D1P
W9	MIPI_CSI_D1N	A	N/A	N/A						MIPI_CSI_D1N	MIPI-CSI differential lane 1 negative	MIPI_CSI_D1N
V10	MIPI_CSI_CLKP	A	N/A	N/A						MIPI_CSI_CLKP	MIPI-CSI differential clock lane positive	MIPI_CSI_CLKP
U10	MIPI_CSI_CLKN	A	N/A	N/A						MIPI_CSI_CLKN	MIPI-CSI differential clock lane negative	MIPI_CSI_CLKN
U9	MIPI_CSI_D2P	A	N/A	N/A						MIPI_CSI_D2P	MIPI-CSI differential lane 2 positive	MIPI_CSI_D2P
V8	MIPI_CSI_D2N	A	N/A	N/A						MIPI_CSI_D2N	MIPI-CSI differential lane 2 negative	MIPI_CSI_D2N
V9	MIPI_CSI_D3P	A	N/A	N/A						MIPI_CSI_D3P	MIPI-CSI differential lane 3 positive	MIPI_CSI_D3P
W8	MIPI_CSI_D3N	A	N/A	N/A						MIPI_CSI_D3N	MIPI-CSI differential lane 3 negative	MIPI_CSI_D3N
U11	MIPI_CSI_RBIAS	A	N/A	N/A						MIPI_CSI_RBIAS	MIPI-CSI reference current generate,connect a 2K%1 resistor to VSS	MIPI_CSI_RBIAS
U8	MIPI_CSI_VCCA_1V0	P	N/A	N/A						VDD_1V0	MIPI-CSI analog power supply	VDD_1V0
PART E	USB 2.0 PHY											
AA10	USB_OTG_DM	A	N/A	N/A		OTG_DM	UART2_TX_M2			OTG_DM	OTG Data Plus port	OTG_DM
Y10	USB_OTG_DP	A	N/A	N/A		OTG_DP	UART2_RX_M2			OTG_DP	OTG Data Minus port	OTG_DP
Y11	USB_ID	A	N/A	up						USB_ID	USB ID detect input,200kohm internal pull-up to USB_AVDD_1V8	USB_ID
Y13	USB_VBUS	A	N/A	N/A						USB_DET	USB vbus power detect	USB_DET
AA11	USB_RBIAS	A	N/A	N/A						USB_RBIAS	USB PHY reference current generate,connect a 133ohm resistor to VSS.	USB_RBIAS
U12	USB_AVDD_1V0	P	N/A	N/A						VDD_1V0	USB analog power supply	VDD_1V0
V13	USB_AVDD_1V8	P	N/A	N/A						VCC_1V8	USB analog power supply	VCC_1V8
W13	USB_AVDD_3V3	P	N/A	N/A						VCC3V0_PMU	USB analog power supply	VCC3V0_PMU
PART A	DDR CTRL											
R4	DDR_DQ0	A	N/A	N/A						DDR_DQ0	DRAM data port	
R1	DDR_DQ1	A	N/A	N/A						DDR_DQ1	DRAM data port	
U3	DDR_DQ2	A	N/A	N/A						DDR_DQ2	DRAM data port	
Y2	DDR_DQ3	A	N/A	N/A						DDR_DQ3	DRAM data port	
N2	DDR_DQ4	A	N/A	N/A						DDR_DQ4	DRAM data port	
R3	DDR_DQ5	A	N/A	N/A						DDR_DQ5	DRAM data port	
R2	DDR_DQ6	A	N/A	N/A						DDR_DQ6	DRAM data port	
Y1	DDR_DQ7	A	N/A	N/A						DDR_DQ7	DRAM data port	
AA2	DDR_DQ8	A	N/A	N/A						DDR_DQ8	DRAM data port	
P3	DDR_DQ9	A	N/A	N/A						DDR_DQ9	DRAM data port	
Y3	DDR_DQ10	A	N/A	N/A						DDR_DQ10	DRAM data port	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
M2	DDR_DQ11	A	N/A	N/A						DDR_DQ11	DRAM data port	
V4	DDR_DQ12	A	N/A	N/A						DDR_DQ12	DRAM data port	
M3	DDR_DQ13	A	N/A	N/A						DDR_DQ13	DRAM data port	
V3	DDR_DQ14	A	N/A	N/A						DDR_DQ14	DRAM data port	
N4	DDR_DQ15	A	N/A	N/A						DDR_DQ15	DRAM data port	
F4	DDR_DQ16	A	N/A	N/A						DDR_DQ16	DRAM data port	
G3	DDR_DQ17	A	N/A	N/A						DDR_DQ17	DRAM data port	
F3	DDR_DQ18	A	N/A	N/A						DDR_DQ18	DRAM data port	
K2	DDR_DQ19	A	N/A	N/A						DDR_DQ19	DRAM data port	
L2	DDR_DQ20	A	N/A	N/A						DDR_DQ20	DRAM data port	
M1	DDR_DQ21	A	N/A	N/A						DDR_DQ21	DRAM data port	
E2	DDR_DQ22	A	N/A	N/A						DDR_DQ22	DRAM data port	
L1	DDR_DQ23	A	N/A	N/A						DDR_DQ23	DRAM data port	
K3	DDR_DQ24	A	N/A	N/A						DDR_DQ24	DRAM data port	
J4	DDR_DQ25	A	N/A	N/A						DDR_DQ25	DRAM data port	
C2	DDR_DQ26	A	N/A	N/A						DDR_DQ26	DRAM data port	
D2	DDR_DQ27	A	N/A	N/A						DDR_DQ27	DRAM data port	
K4	DDR_DQ28	A	N/A	N/A						DDR_DQ28	DRAM data port	
J3	DDR_DQ29	A	N/A	N/A						DDR_DQ29	DRAM data port	
D1	DDR_DQ30	A	N/A	N/A						DDR_DQ30	DRAM data port	
E1	DDR_DQ31	A	N/A	N/A						DDR_DQ31	DRAM data port	
W2	DDR_DQS0_P	A	N/A	N/A						DDR_DQS0P	DRAM data strobe 0	
W1	DDR_DQS0_N	A	N/A	N/A						DDR_DQS0N	DRAM data strobe 0	
U2	DDR_DQS1_P	A	N/A	N/A						DDR_DQS1P	DRAM data strobe 1	
U1	DDR_DQS1_N	A	N/A	N/A						DDR_DQS1N	DRAM data strobe 1	
J2	DDR_DQS2_P	A	N/A	N/A						DDR_DQS2P	DRAM data strobe 2	
J1	DDR_DQS2_N	A	N/A	N/A						DDR_DQS2N	DRAM data strobe 2	
G2	DDR_DQS3_P	A	N/A	N/A						DDR_DQS3P	DRAM data strobe 3	
G1	DDR_DQS3_N	A	N/A	N/A						DDR_DQS3N	DRAM data strobe 3	
T2	DDR_DM0	A	N/A	N/A						DDR_DM0	DRAM data mask 0	
U4	DDR_DM1	A	N/A	N/A						DDR_DM1	DRAM data mask 1	
G4	DDR_DM2	A	N/A	N/A						DDR_DM2	DRAM data mask 2	
M4	DDR_DM3	A	N/A	N/A						DDR_DM3	DRAM data mask 3	
C7	DDR3_A0/DDR4_A10	A	N/A	N/A		DDR3_A0	DDR4_A10			DDR_A0	DRAM address port	
D10	DDR3_A1/DDR4_A10	A	N/A	N/A		DDR3_A1	DDR4_A10			DDR_A1	DRAM address port	
D8	DDR3_A2/DDR4_A4	A	N/A	N/A		DDR3_A2	DDR4_A4			DDR_A2	DRAM address port	
B5	DDR3_A3/DDR4_A6	A	N/A	N/A		DDR3_A3	DDR4_A6			DDR_A3	DRAM address port	
A8	DDR3_A4/DDR4_A5	A	N/A	N/A		DDR3_A4	DDR4_A5			DDR_A4	DRAM address port	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
A4	DDR3_A5/DDR4_A8	A	N/A	N/A		DDR3_A5	DDR4_A8			DDR_A5	DRAM address port	
B8	DDR3_A6/DDR4_A7	A	N/A	N/A		DDR3_A6	DDR4_A7			DDR_A6	DRAM address port	
B4	DDR3_A7/DDR4_A11	A	N/A	N/A		DDR3_A7	DDR4_A11			DDR_A7	DRAM address port	
C10	DDR3_A8/DDR4_A13	A	N/A	N/A		DDR3_A8	DDR4_A13			DDR_A8	DRAM address port	
C6	DDR3_A9/DDR4_A0	A	N/A	N/A		DDR3_A9	DDR4_A0			DDR_A9	DRAM address port	
A10	DDR3_A10/DDR4_CS0N	A	N/A	N/A		DDR3_A10	DDR4_CS0N			DDR_A10	DRAM address port	
B7	DDR3_A11/DDR4_A3	A	N/A	N/A		DDR3_A11	DDR4_A3			DDR_A11	DRAM address port	
A9	DDR3_A12/DDR4_BA1	A	N/A	N/A		DDR3_A12	DDR4_BA1			DDR_A12	DRAM address port	
D6	DDR3_A13/DDR4_A2	A	N/A	N/A		DDR3_A13	DDR4_A2			DDR_A13	DRAM address port	
A7	DDR3_A14/DDR4_A1	A	N/A	N/A		DDR3_A14	DDR4_A1			DDR_A14	DRAM address port	
C9	DDR3_A15/DDR4_ODT0	A	N/A	N/A		DDR3_A15	DDR4_ODT0			DDR_A15	DRAM address port	
A3	DDR3_BA0/DDR4_BG0	A	N/A	N/A		DDR3_BA0	DDR4_BG0			DDR_BA0	DRAM bank select 0	
B10	DDR3_BA1/DDR4_CASN/DDR4_A15	A	N/A	N/A		DDR3_BA1	DDR4_CASN	DDR4_A15		DDR_BA1	DRAM bank select 1	
A5	DDR3_BA2/DDR4_BA0	A	N/A	N/A		DDR3_BA2	DDR4_BA0			DDR_BA2	DRAM bank select 2	
B3	DDR3_CSN0/DDR4_ACTN	A	N/A	N/A		DDR3_CSN0	DDR4_ACTN			DDR_CSN0	DRAM chip select 0	
D11	DDR3_CSN1/DDR4_CS1N	A	N/A	N/A		DDR3_CSN1	DDR4_CS1N			DDR_CSN1	DRAM chip select 1	
B2	DDR3_CLKP/DDR4_CLKP	A	N/A	N/A		DDR3_CKP	DDR4_CLKP			DDR_CK0P	DRAM differential clock output	
B1	DDR3_CLKN/DDR4_CLKN	A	N/A	N/A		DDR3_CKN	DDR4_CLKN			DDR_CK0N	DRAM differential clock output	
D5	DDR3_ODT0/DDR4_WEN/DDR4_A14	A	N/A	N/A		DDR3_ODT0	DDR4_WEN	DDR4_A14		DDR_ODT0	DRAM on die termination control 0	
C11	DDR3_ODT1/DDR4_ODT1	A	N/A	N/A		DDR3_ODT1	DDR4_ODT1			DDR_ODT1	DRAM on die termination control 1	
A2	DDR3_RESETN/DDR4_RESETN	A	N/A	N/A		DDR3_RESETN	DDR4_RESETN			DDR_RESETN	DRAM reset output	
D9	DDR3_CASN/DDR4_A12	A	N/A	N/A		DDR3_CASN	DDR4_A12			DDR_CASN	DRAM column address strobe output	
C4	DDR3_RASN/DDR4_CKE	A	N/A	N/A		DDR3_RASN	DDR4_CKE			DDR_RASN	DRAM row address strobe output	
B11	DDR3_CKE/DDR4_RASN/DDR4_A16	A	N/A	N/A		DDR3_CKE	DDR4_RASN	DDR4_A16		DDR_CKE0	DRAM clock enable 0	
C8	DDR3_WEN/DDR4_BG1	A	N/A	N/A		DDR3_WEN	DDR4_BG1			DDR_WEN	DRAM write enable strobe output	
F8	DDR_VDD_1	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
F9	DDR_VDD_2	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
F10	DDR_VDD_3	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
G9	DDR_VDD_4	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
G10	DDR_VDD_5	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
J6	DDR_VDD_6	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
J7	DDR_VDD_7	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
K6	DDR_VDD_8	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
K7	DDR_VDD_9	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
L6	DDR_VDD_10	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
L7	DDR_VDD_11	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
M6	DDR_VDD_12	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	
M7	DDR_VDD_13	P	N/A	N/A						DDR_VDD	DRAM Digital power supply	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
PART N	POWER SUPPLY											
K9	LOGIC_VDD_1	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
L9	LOGIC_VDD_2	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
K10	LOGIC_VDD_3	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
L10	LOGIC_VDD_4	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
N11	LOGIC_VDD_5	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
N13	LOGIC_VDD_6	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
N12	LOGIC_VDD_7	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
M11	LOGIC_VDD_8	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
L11	LOGIC_VDD_9	P	N/A	N/A						VDD_LOG	Logic&GPU power supply	
H14	CPU_VDD_1	P	N/A	N/A						VDD_ARM	CPU core power supply	
H15	CPU_VDD_2	P	N/A	N/A						VDD_ARM	CPU core power supply	
H16	CPU_VDD_3	P	N/A	N/A						VDD_ARM	CPU core power supply	
J14	CPU_VDD_4	P	N/A	N/A						VDD_ARM	CPU core power supply	
J15	CPU_VDD_5	P	N/A	N/A						VDD_ARM	CPU core power supply	
J16	CPU_VDD_6	P	N/A	N/A						VDD_ARM	CPU core power supply	
K14	CPU_VDD_7	P	N/A	N/A						VDD_ARM	CPU core power supply	
K15	CPU_VDD_8	P	N/A	N/A						VDD_ARM	CPU core power supply	
K16	CPU_VDD_9	P	N/A	N/A						VDD_ARM	CPU core power supply	
PART X	POWER GROUND											
A1	VSS_1	G	N/A	N/A						VSS	Digital power ground	
A21	VSS_2	G	N/A	N/A						VSS	Digital power ground	
AA21	VSS_3	G	N/A	N/A						VSS	Digital power ground	
AA1	VSS_4	G	N/A	N/A						VSS	Digital power ground	
A11	VSS_5	G	N/A	N/A						VSS	Digital power ground	
B6	VSS_6	G	N/A	N/A						VSS	Digital power ground	
B9	VSS_7	G	N/A	N/A						VSS	Digital power ground	
C3	VSS_8	G	N/A	N/A						VSS	Digital power ground	
C5	VSS_9	G	N/A	N/A						VSS	Digital power ground	
C12	VSS_10	G	N/A	N/A						VSS	Digital power ground	
D3	VSS_11	G	N/A	N/A						VSS	Digital power ground	
D4	VSS_12	G	N/A	N/A						VSS	Digital power ground	
D7	VSS_13	G	N/A	N/A						VSS	Digital power ground	
E3	VSS_14	G	N/A	N/A						VSS	Digital power ground	
E4	VSS_15	G	N/A	N/A						VSS	Digital power ground	
E5	VSS_16	G	N/A	N/A						VSS	Digital power ground	
E6	VSS_17	G	N/A	N/A						VSS	Digital power ground	
E7	VSS_18	G	N/A	N/A						VSS	Digital power ground	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
E8	VSS_19	G	N/A	N/A						VSS	Digital power ground	
E9	VSS_20	G	N/A	N/A						VSS	Digital power ground	
E10	VSS_21	G	N/A	N/A						VSS	Digital power ground	
F2	VSS_22	G	N/A	N/A						VSS	Digital power ground	
F5	VSS_23	G	N/A	N/A						VSS	Digital power ground	
E11	VSS_24	G	N/A	N/A						VSS	Digital power ground	
F14	VSS_25	G	N/A	N/A						VSS	Digital power ground	
F15	VSS_26	G	N/A	N/A						VSS	Digital power ground	
F16	VSS_27	G	N/A	N/A						VSS	Digital power ground	
G5	VSS_28	G	N/A	N/A						VSS	Digital power ground	
G7	VSS_29	G	N/A	N/A						VSS	Digital power ground	
G8	VSS_30	G	N/A	N/A						VSS	Digital power ground	
G11	VSS_31	G	N/A	N/A						VSS	Digital power ground	
W3	VSS_32	G	N/A	N/A						VSS	Digital power ground	
G13	VSS_33	G	N/A	N/A						VSS	Digital power ground	
G14	VSS_34	G	N/A	N/A						VSS	Digital power ground	
W11	VSS_35	G	N/A	N/A						VSS	Digital power ground	
T12	VSS_36	G	N/A	N/A						VSS	Digital power ground	
H2	VSS_37	G	N/A	N/A						VSS	Digital power ground	
H3	VSS_38	G	N/A	N/A						VSS	Digital power ground	
H4	VSS_39	G	N/A	N/A						VSS	Digital power ground	
H5	VSS_40	G	N/A	N/A						VSS	Digital power ground	
H7	VSS_41	G	N/A	N/A						VSS	Digital power ground	
H8	VSS_42	G	N/A	N/A						VSS	Digital power ground	
H9	VSS_43	G	N/A	N/A						VSS	Digital power ground	
H10	VSS_44	G	N/A	N/A						VSS	Digital power ground	
H11	VSS_45	G	N/A	N/A						VSS	Digital power ground	
H12	VSS_46	G	N/A	N/A						VSS	Digital power ground	
G16	VSS_47	G	N/A	N/A						VSS	Digital power ground	
G15	VSS_48	G	N/A	N/A						VSS	Digital power ground	
J5	VSS_49	G	N/A	N/A						VSS	Digital power ground	
J8	VSS_50	G	N/A	N/A						VSS	Digital power ground	
J9	VSS_51	G	N/A	N/A						VSS	Digital power ground	
J10	VSS_52	G	N/A	N/A						VSS	Digital power ground	
J11	VSS_53	G	N/A	N/A						VSS	Digital power ground	
J12	VSS_54	G	N/A	N/A						VSS	Digital power ground	
H13	VSS_55	G	N/A	N/A						VSS	Digital power ground	
J17	VSS_56	G	N/A	N/A						VSS	Digital power ground	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
H17	VSS_57	G	N/A	N/A						VSS	Digital power ground	
K5	VSS_58	G	N/A	N/A						VSS	Digital power ground	
K8	VSS_59	G	N/A	N/A						VSS	Digital power ground	
M13	VSS_60	G	N/A	N/A						VSS	Digital power ground	
K11	VSS_61	G	N/A	N/A						VSS	Digital power ground	
M12	VSS_62	G	N/A	N/A						VSS	Digital power ground	
K12	VSS_63	G	N/A	N/A						VSS	Digital power ground	
J13	VSS_64	G	N/A	N/A						VSS	Digital power ground	
K13	VSS_65	G	N/A	N/A						VSS	Digital power ground	
L14	VSS_66	G	N/A	N/A						VSS	Digital power ground	
K17	VSS_67	G	N/A	N/A						VSS	Digital power ground	
K18	VSS_68	G	N/A	N/A						VSS	Digital power ground	
L13	VSS_69	G	N/A	N/A						VSS	Digital power ground	
L3	VSS_70	G	N/A	N/A						VSS	Digital power ground	
L4	VSS_71	G	N/A	N/A						VSS	Digital power ground	
L5	VSS_72	G	N/A	N/A						VSS	Digital power ground	
L8	VSS_73	G	N/A	N/A						VSS	Digital power ground	
N14	VSS_74	G	N/A	N/A						VSS	Digital power ground	
M14	VSS_75	G	N/A	N/A						VSS	Digital power ground	
W18	VSS_76	G	N/A	N/A						VSS	Digital power ground	
L12	VSS_77	G	N/A	N/A						VSS	Digital power ground	
L17	VSS_78	G	N/A	N/A						VSS	Digital power ground	
L18	VSS_79	G	N/A	N/A						VSS	Digital power ground	
M5	VSS_80	G	N/A	N/A						VSS	Digital power ground	
M8	VSS_81	G	N/A	N/A						VSS	Digital power ground	
M15	VSS_82	G	N/A	N/A						VSS	Digital power ground	
M16	VSS_83	G	N/A	N/A						VSS	Digital power ground	
M9	VSS_84	G	N/A	N/A						VSS	Digital power ground	
M10	VSS_85	G	N/A	N/A						VSS	Digital power ground	
M17	VSS_86	G	N/A	N/A						VSS	Digital power ground	
L15	VSS_87	G	N/A	N/A						VSS	Digital power ground	
L16	VSS_88	G	N/A	N/A						VSS	Digital power ground	
N3	VSS_89	G	N/A	N/A						VSS	Digital power ground	
N5	VSS_90	G	N/A	N/A						VSS	Digital power ground	
N6	VSS_91	G	N/A	N/A						VSS	Digital power ground	
N7	VSS_92	G	N/A	N/A						VSS	Digital power ground	
N8	VSS_93	G	N/A	N/A						VSS	Digital power ground	
N15	VSS_94	G	N/A	N/A						VSS	Digital power ground	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
N16	VSS_95	G	N/A	N/A						VSS	Digital power ground	
N9	VSS_96	G	N/A	N/A						VSS	Digital power ground	
N10	VSS_97	G	N/A	N/A						VSS	Digital power ground	
AA3	VSS_98	G	N/A	N/A						VSS	Digital power ground	
W17	VSS_99	G	N/A	N/A						VSS	Digital power ground	
P4	VSS_100	G	N/A	N/A						VSS	Digital power ground	
P5	VSS_101	G	N/A	N/A						VSS	Digital power ground	
P6	VSS_102	G	N/A	N/A						VSS	Digital power ground	
P7	VSS_103	G	N/A	N/A						VSS	Digital power ground	
P8	VSS_104	G	N/A	N/A						VSS	Digital power ground	
P9	VSS_105	G	N/A	N/A						VSS	Digital power ground	
P10	VSS_106	G	N/A	N/A						VSS	Digital power ground	
P11	VSS_107	G	N/A	N/A						VSS	Digital power ground	
P12	VSS_108	G	N/A	N/A						VSS	Digital power ground	
P13	VSS_109	G	N/A	N/A						VSS	Digital power ground	
P14	VSS_110	G	N/A	N/A						VSS	Digital power ground	
P16	VSS_111	G	N/A	N/A						VSS	Digital power ground	
P15	VSS_112	G	N/A	N/A						VSS	Digital power ground	
R5	VSS_113	G	N/A	N/A						VSS	Digital power ground	
R6	VSS_114	G	N/A	N/A						VSS	Digital power ground	
R7	VSS_115	G	N/A	N/A						VSS	Digital power ground	
R8	VSS_116	G	N/A	N/A						VSS	Digital power ground	
R9	VSS_117	G	N/A	N/A						VSS	Digital power ground	
R10	VSS_118	G	N/A	N/A						VSS	Digital power ground	
R11	VSS_119	G	N/A	N/A						VSS	Digital power ground	
R12	VSS_120	G	N/A	N/A						VSS	Digital power ground	
R13	VSS_121	G	N/A	N/A						VSS	Digital power ground	
R14	VSS_122	G	N/A	N/A						VSS	Digital power ground	
R15	VSS_123	G	N/A	N/A						VSS	Digital power ground	
R16	VSS_124	G	N/A	N/A						VSS	Digital power ground	
R17	VSS_125	G	N/A	N/A						VSS	Digital power ground	
T3	VSS_126	G	N/A	N/A						VSS	Digital power ground	
T4	VSS_127	G	N/A	N/A						VSS	Digital power ground	
T5	VSS_128	G	N/A	N/A						VSS	Digital power ground	
T6	VSS_129	G	N/A	N/A						VSS	Digital power ground	
T7	VSS_130	G	N/A	N/A						VSS	Digital power ground	
T8	VSS_131	G	N/A	N/A						VSS	Digital power ground	
T9	VSS_132	G	N/A	N/A						VSS	Digital power ground	

RK3326 I/O LIST

Pin No.	Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Default function for Tablet	Description for default function	Function for AI-VoiceAssistant
T10	VSS_133	G	N/A	N/A						VSS	Digital power ground	
T11	VSS_134	G	N/A	N/A						VSS	Digital power ground	
W4	VSS_135	G	N/A	N/A						VSS	Digital power ground	
T14	VSS_136	G	N/A	N/A						VSS	Digital power ground	
T15	VSS_137	G	N/A	N/A						VSS	Digital power ground	
T16	VSS_138	G	N/A	N/A						VSS	Digital power ground	
T17	VSS_139	G	N/A	N/A						VSS	Digital power ground	
T18	VSS_140	G	N/A	N/A						VSS	Digital power ground	
U5	VSS_141	G	N/A	N/A						VSS	Digital power ground	
U14	VSS_142	G	N/A	N/A						VSS	Digital power ground	
U15	VSS_143	G	N/A	N/A						VSS	Digital power ground	
U16	VSS_144	G	N/A	N/A						VSS	Digital power ground	
U17	VSS_145	G	N/A	N/A						VSS	Digital power ground	
U18	VSS_146	G	N/A	N/A						VSS	Digital power ground	
V2	VSS_147	G	N/A	N/A						VSS	Digital power ground	
V5	VSS_148	G	N/A	N/A						VSS	Digital power ground	
V16	VSS_149	G	N/A	N/A						VSS	Digital power ground	
V17	VSS_150	G	N/A	N/A						VSS	Digital power ground	
V18	VSS_151	G	N/A	N/A						VSS	Digital power ground	
V19	VSS_152	G	N/A	N/A						VSS	Digital power ground	
PART O	RESERVE											
AA12	NC1	N/A	N/A	N/A						NC	Reserve.	
Y12	NC2	N/A	N/A	N/A						NC	Reserve.	