

## FAKULTÄT FÜR INFORMATIK

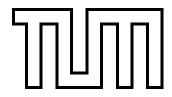
DER TECHNISCHEN UNIVERSITÄT MÜNCHEN

Master's Thesis in Informatics

# Design and Prototypical Implementation of a High-Level Synchronization Component for Dynamic Updates of Task Run Queues in L4 Fiasco.OC/Genode

Gurusiddesha Chandrasekhara





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Entwurf und Prototypische Implementierung einer High-Level Synchronisationskomponente fuer dynamische Updates von Task-Warteschlangen in L4 Fiasco.OC/Genode

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Date: October 15, 2016



I confirm that this master's the all sources and material used.	in informatics is my own work and I have documented
Munich, October 15, 2016	Gurusiddesha Chandrasekhara

## Acknowledgments

### **Abstract**

Abstract abstracting the abstractness

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## 1 Introduction

The implemented high-level synchronization component in this master thesis is part of the KIA4SM project of the department of operating systems. The work aims to implement a method for dynamic update of tasks ready queues in L4 Fiasco.OC/Genode while providing a synchronized access to them.

#### 1.1 Overview of KIA4SM project

KIA4SM (stands for Cooperative Integration Architecture for Future Smart Mobility Solutions) is a research project at the department of operating systems. Traditionally Cooperative Intelligent Transport Systems have been built on heterogeneous systems. KIA4SM aims to provide an architecture of having an homogeneous platform for heterogeneous systems.

KIA4SM focuses on developing systems for the interaction and coordination between computer-assisted vehicles, be it partially or fully autonomously functioning actors. KIA4SM aims to improve on the ad-hoc networking between vehicles

The final vision of the project is illustrate in (Fig). The goals of the project are,

- A common platform as foundation for device in- dependent (vehicles, mobile devices, traffic and transport architecture) provision and execution of software-based functionality
- Mechanisms that allow for online dynamic reconfig- uration, based on
  - en-/disabling and relocation/migration of software-based functionality
  - adaptive (data-centric) routing policy
  - flexible scheduling of tasks per ECU

In order to achieve the goals of the project a number of different methods have been applied. This has led to the application of Organic computing paradigm. igner and user. Organic Computing (OC) has the vision to address the challenges of complex distributed systems by making them more life-like (organic), i.e. endowing them with abilities such as self- organization, self-configuration, self-repair, or adaptation. In order to realize this, universally applicable Electronic Control Units (ECU) and a common run-time environment are used which provides Hardware/Software Plug-and-Play properties.

#### 1.2 Motivation

There are number of micro controllers used for different calculations in a modern vehicle, this project aims to replace them with use more power-full and standardized hardware universally applicable ECUs.

OC approach proposes a Observer-Controller architecture similar to MAPE architecture(monitor, analyze, plan, execute). An observer collects the data from the all the ECUs and computes and generates indicators where a controller takes a decision based on the indicator, generates an action.

One such action of the controller is to decide what tasks should be executed at what time in order to meet the aforementioned requirements of the safety critical systems. So the controller decides and produces a run/ready queue (RQ). There needs to be a method which allows to safely update the scheduler run queues of the system.

Therefore it is essential to be able to add threads and modify the execution order during operation time, A flexible thread handling is also required for example in case a ECU is malfunctioning. In this case it would be possible to swap the threads from the malfunctioning one to working ones. So it is important to generate new ready-queues based on the information we are receiving from the other ECUs in the grid, and then exchange them with the actual ready-queue the scheduler uses.

#### 1.3 Thesis structure

The thesis is structured in a way that the reader understands the importance of the work carried out here and also the surrounding concepts before delving in to the specifics (inverted traingle).

The second chapter summarizes the related work on the state of the art algorithms for synchronization and different types of schedulers in use and at the end of the section an evaluation of the synchronization methods is provided inorder to chose the best possible approach for the existing project.

The third chapter explains the Genode and L4 Fiasco.OC details in brief, in order for the user to have an overview of the system.

The fourth chapter deals with the design considerations along with implementation details.

The fifth chapter is dedicated to explains testing method and the results obtained. And the final chapter concludes the thesis with the limitations and future work to be done.

# 2 Related Work

# 3 Genode OS framework and L4 Fiasco.OC

## 3.1 section

I cite [1]

# 4 Design and Implementation

# **5 Testing and Results**

# 6 Conclusion

- 6.1 Limitations
- **6.2 Future Work**

# Bibliography

[1] Kunihiko Sadakane and Gonzalo Navarro. Fully-functional static and dynamic succinct trees. *CoRR*, abs/0905.0768, 2009.