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false.asm has been uploaded.

Close Assembler code

Close Lexical analisation

Close Detected errors

row number	first word	second word	third word	fourth word	fifth word
0	.386				
1	mcrs	MACRO			
2	And	parm	,	dd	
3	ENDM				
4	data	segment			
5	str	db	"big ukrainian boss's string"		
6	hex	dw	3Eh		
7	bina	dd	01101101b		
8	deca	dd	21		
9	data	ends			
10	cod1	segment			
11	assume	cs:cod1	,	ds:data	
12	Neg	bh			
13	Neg	ebx			
14	Dec	deca			
15	Adc	bh	,	ptr [ebx+6]	
16	Adc	ecx	,	[ebx+6]	
17	Adc	bh	,	bina	
18	Adc	ecx	,	hexa	
19	Cmp	es:[ebx+6]	,	ah	
20	Cmp	[ebx+6]	,	eax	
21	Cmp	deca	,	ah	
22	Cmp	bina	,	eax	
23	Or	eax	,	bh	
24	Or	bh	,	bl	
25	Or	al	,	edx	
26	Or	ecx	,	edx	
27	Mov	eax	,	bl	
28	Mov	eax	,	bx	
29	Mov	eax	,	ebx	
30	Mov	al	,	bl	
31	Mov	al	,	bx	
32	Mov	al	,	ebx	
33	met1:				
34	;And	[bx+si]	,	8Ah	
35	And	[bx+bp+di+5]	,	8AF2h	
36	And	[ebx+6]	,	8AF22E4Bh	
37	And	hexa	,	bh	
38	And	bina	,	bx	
39	And	deca	,	ebx	
40	Jc	met1			

row number	first word	second word	third word	fourth word	fifth word
41	Jmp	met2			
42	cod1	ends			
43	cod2	segment			
44	assume	cs:cod2	,	ds:data	
45	met2:				
46	cod2	ends			
47	END				

.386

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
0	0	.386	Undefined lexem	undefined	255	0					

unexpected lexem ".386" on line 0 on position 0

mcrs MACRO

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
1	0	mcrs	macro name	identifier	4	0					
1	1	MACRO	MACRO	directive	5	0					

And parm , dd

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
2	0	And	and	command	3	0		register/memory		register/memory/const/variable	
2	1	parm	identifier	identifier	4	0					
2	2	,	,	char	4	0					
2	3	dd	DD	directive	2	4	identifier		ds		

"register/memory" is required at the next position of lexem on line 2 on position 0
"register/memory/const/variable" is missed after lexem on line 2 on position 0
"identifier" is required at the previous position of lexem on line 2 on position 3
"ds" is required before this row by lexem on line 2 on position 3

ENDM

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
3	0	ENDM	ENDM	directive	4	0					

data segment

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
4	0	data	segment name	identifier	4	0					
4	1	segment	segment	directive	7	0					

str db "big ukrainian boss's string"

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
5	0	str	variable	identifier	4	0					
5	1	db	DB	directive	2	1					

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
5	2	"big ukrainian boss's string"	str const	const	255	0					

hex dw 3Eh

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
6	0	hex	variable	identifier	4	0					
6	1	dw	DW	directive	2	2					
6	2	3Eh	hex const	const	255	0					

bina dd 01101101b

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
7	0	bina	variable	identifier	4	0					
7	1	dd	DD	directive	2	4					
7	2	01101101b	bin const	const	255	0					

deca dd 21

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
8	0	deca	variable	identifier	4	0					
8	1	dd	DD	directive	2	4					
8	2	21	dec const	const	255	0					

data ends

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
9	0	data	segment name	identifier	4	0					
9	1	ends	ENDS	directive	4	0					

cod1 segment

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
10	0	cod1	segment name	identifier	4	0					
10	1	segment	segment	directive	7	0					

assume cs:cod1 , ds:data

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
11	0	assume	ASSUME	directive	6	0					
11	1	cs:cod1	code segment	register:code segment identifier	255	0					
11	2	,	,	char	4	0					
11	3	ds:data	data segment	register:data segment identifier	255	0					

Neg bh

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
12	0	Neg	neg	command	3	0					
12	1	bh	register08	register	3	0					

Neg ebx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
13	0	Neg	neg	command	3	0					
13	1	ebx	register32	register	3	0					

Dec deca

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
14	0	Dec	dec	command	3	0		register/memory			
14	1	deca	variable	identifier	4	0					

"register/memory" is required at the next position of lexem on line 14 on position 0

Adc bh , ptr [ebx+6]

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
15	0	Adc	adc	command	3	0					
15	1	bh	register08	register	3	0					
15	2	,	,	char	4	0					
15	3	ptr [ebx+6]	pointer	memory	255	0					

Adc ecx , [ebx+6]

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
16	0	Adc	adc	command	3	0					
16	1	ecx	register32	register	3	0					
16	2	,	,	char	4	0					
16	3	[ebx+6]	pointer	memory	255	0					

Adc bh , bina

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
17	0	Adc	adc	command	3	0					
17	1	bh	register08	register	3	0					
17	2	,	,	char	4	0					
17	3	bina	variable	identifier	4	0					

Adc ecx , hexa

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
18	0	Adc	adc	command	3	0				register/memory/const/variable	
18	1	ecx	register32	register	3	0					
18	2	,	,	char	4	0					
18	3	hexa	identifier	identifier	4	0					

"register/memory/const/variable" is missed after lexem on line 18 on position 0

Cmp es:[ebx+6] , ah

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
19	0	Cmp	cmp	command	3	0					
19	1	es:[ebx+6]	pointer	memory	255	0					
19	2	,	,	char	4	0					
19	3	ah	register08	register	3	0					

Cmp [ebx+6] , eax

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
20	0	Cmp	cmp	command	3	0					
20	1	[ebx+6]	pointer	memory	255	0					
20	2	,	,	char	4	0					
20	3	eax	register32	register	3	0					

Cmp deca , ah

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
21	0	Cmp	cmp	command	3	0					
21	1	deca	variable	identifier	4	0					
21	2	,	,	char	4	0					
21	3	ah	register08	register	3	0					

Cmp bina , eax

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
22	0	Cmp	cmp	command	3	0					
22	1	bina	variable	identifier	4	0					
22	2	,	,	char	4	0					
22	3	eax	register32	register	3	0					

Or eax , bh

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
23	0	Or	or	command	2	0					
23	1	eax	register32	register	3	0					
23	2	,	,	char	4	0					
23	3	bh	register08	register	3	0					

Operand types must match on line 23

Or bh , bl

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
24	0	Or	or	command	2	0					
24	1	bh	register08	register	3	0					
24	2	,	,	char	4	0					
24	3	bl	register08	register	3	0					

Or al , edx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
25	0	Or	or	command	2	0					
25	1	al	register08	register	3	0					
25	2	,	,	char	4	0					
25	3	edx	register32	register	3	0					

Operand types must match on line 25

Or ecx , edx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
26	0	Or	or	command	2	0					
26	1	ecx	register32	register	3	0					
26	2	,	,	char	4	0					
26	3	edx	register32	register	3	0					

Mov eax , bl

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
27	0	Mov	mov	command	3	0					
27	1	eax	register32	register	3	0					
27	2	,	,	char	4	0					
27	3	bl	register08	register	3	0					

Operand types must match on line 27

Mov eax , bx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
28	0	Mov	mov	command	3	0					
28	1	eax	register32	register	3	0					
28	2	,	,	char	4	0					
28	3	bx	register16	register	3	0					

Operand types must match on line 28

Mov eax , ebx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
29	0	Mov	mov	command	3	0					
29	1	eax	register32	register	3	0					
29	2	,	,	char	4	0					
29	3	ebx	register32	register	3	0					

Mov al , bl

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexeme
30	0	Mov	mov	command	3	0					
30	1	al	register08	register	3	0					
30	2	,	,	char	4	0					
30	3	bl	register08	register	3	0					

Mov al , bx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexe
31	0	Mov	mov	command	3	0					
31	1	al	register08	register	3	0					
31	2	,	,	char	4	0					
31	3	bx	register16	register	3	0					

Operand types must match on line 31

Mov al , ebx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexe
32	0	Mov	mov	command	3	0					
32	1	al	register08	register	3	0					
32	2	,	,	char	4	0					
32	3	ebx	register32	register	3	0					

Operand types must match on line 32

met1:

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
33	0	met1:	met	identifier	4	0					

;And [bx+si] , 8Ah

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lex
34	0	;And	Undefined lexem	undefined	255	0					
34	1	[bx+si]	pointer	memory	255	0					
34	2	,	,	char	4	0					
34	3	8Ah	hex const	const	255	0					

unexpected lexem ";And" on line 34 on position 0

And [bx+bp+di+5] , 8AF2h

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	require
35	0	And	and	command	3	0		register/memory			
35	1	[bx+bp+di+5]	Undefined lexem	undefined	255	0					
35	2	,	,	char	4	0					
35	3	8AF2h	hex const	const	255	0					

"register/memory" is required at the next position of lexem on line 35 on position 0
unexpected lexem "[bx+bp+di+5]" on line 35 on position 1

And [ebx+6] , 8AF22E4Bh

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_l
36	0	And	and	command	3	0					
36	1	[ebx+6]	pointer	memory	255	0					
36	2	,	,	char	4	0					

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lex
36	3	8AF22E4Bh	hex const	const	255	0					

And hexa , bh

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lex
37	0	And	and	command	3	0		register/memory			
37	1	hexa	identifier	identifier	4	0					
37	2	,	,	char	4	0					
37	3	bh	register08	register	3	0					

"register/memory" is required at the next position of lexem on line 37 on position 0

And bina , bx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lex
38	0	And	and	command	3	0		register/memory			
38	1	bina	variable	identifier	4	0					
38	2	,	,	char	4	0					
38	3	bx	register16	register	3	0					

"register/memory" is required at the next position of lexem on line 38 on position 0

And deca , ebx

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lex
39	0	And	and	command	3	0		register/memory			
39	1	deca	variable	identifier	4	0					
39	2	,	,	char	4	0					
39	3	ebx	register32	register	3	0					

"register/memory" is required at the next position of lexem on line 39 on position 0

Jc met1

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
40	0	Jc	jc	command	2	0					
40	1	met1	identifier	identifier	4	0					

Jmp met2

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
41	0	Jmp	jmp	command	3	0		met name			
41	1	met2	identifier	identifier	4	0					

"met name" is required at the next position of lexem on line 41 on position 0

cod1 ends

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
42	0	cod1	segment name	identifier	4	0					
42	1	ends	ENDS	directive	4	0					

cod2 segment

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexem
43	0	cod2	segment name	identifier	4	0					
43	1	segment	segment	directive	7	0					

assume cs:cod2 , ds:data

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_
44	0	assume	ASSUME	directive	6	0					
44	1	cs:cod2	code segment identifier	register:code segment identifier	255	0					
44	2	,	,	char	4	0					
44	3	ds:data	data segment	register:data segment identifier	255	0					

met2:

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
45	0	met2:	met	identifier	4	0					met name

Can not find in code "met name" that is required by lexem on line 45 on position 0

cod2 ends

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
46	0	cod2	segment name	identifier	4	0					
46	1	ends	ENDS	directive	4	0					

END

row number	word number	code	name	type	max_length	weight	required_prev	required_next_elem	required_before	required_after	required_lexems
47	0	END	END	directive	3	0					

Error near line 0:

unexpected lexem ".386

" on line 0 on position 0

Error near line 2:

"register/memory" is required at the next position of lexem on line 2 on position 0

"register/memory/const/variable" is missed after lexem on line 2 on position 0

"identifier" is required at the previous position of lexem on line 2 on position 3

"ds" is required before this row by lexem on line 2 on position 3

Error near line 14:

"register/memory" is required at the next position of lexem on line 14 on position 0

Error near line 18:

"register/memory/const/variable" is missed after lexem on line 18 on position 0

Error near line 23:

Operand types must match on line 23

Error near line 25:

Operand types must match on line 25

Error near line 27:

Operand types must match on line 27

Error near line 28:

Operand types must match on line 28

Error near line 31:

Operand types must match on line 31

Error near line 32:

Operand types must match on line 32

Error near line 34:

unexpected lexem ";And" on line 34 on position 0

Error near line 35:

"register/memory" is required at the next position of lexem on line 35 on position 0

unexpected lexem "[bx+bp+di+5]" on line 35 on position 1

Error near line 37:

"register/memory" is required at the next position of lexem on line 37 on position 0

Error near line 38:

"register/memory" is required at the next position of lexem on line 38 on position 0

Error near line 39:

"register/memory" is required at the next position of lexem on line 39 on position 0

Error near line 41:

"met name" is required at the next position of lexem on line 41 on position 0

Error near line 45:

Can not find in code "met name" that is required by lexem on line 45 on position 0